



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

MCH3475 — N-Channel Silicon MOSFET General-Purpose Switching Device Applications

Features

- Ultrahigh speed switching
- 4V drive

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		1.8	A
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	7.2	A
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm ² ×0.8mm)	0.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

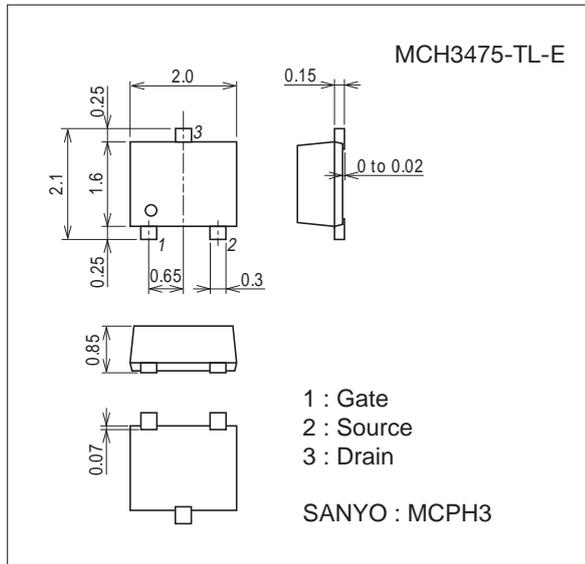
This product is designed to "ESD immunity < 200V**", so please take care when handling.

* Machine Model

Package Dimensions

unit : mm (typ)

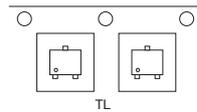
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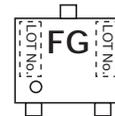
Product & Package Information

- Package : MCPH3
- JEITA, JEDEC : SC-70, SOT-323
- Minimum Packing Quantity : 3,000 pcs./reel

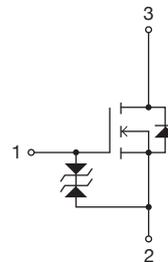
Packing Type : TL



Marking



Electrical Connection

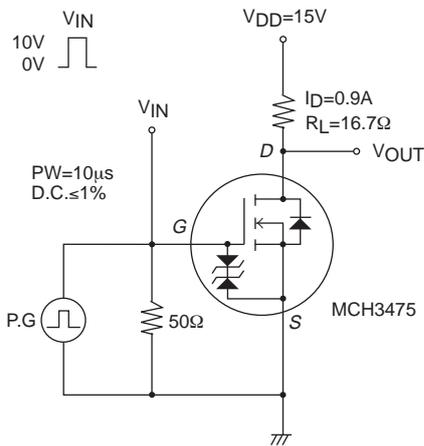


MCH3475

Electrical Characteristics at Ta=25°C

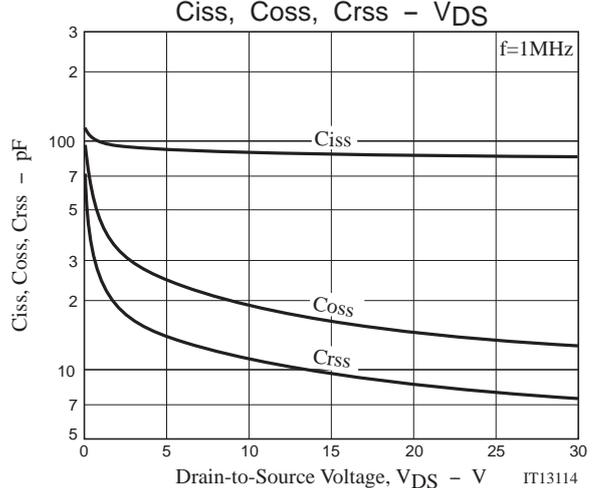
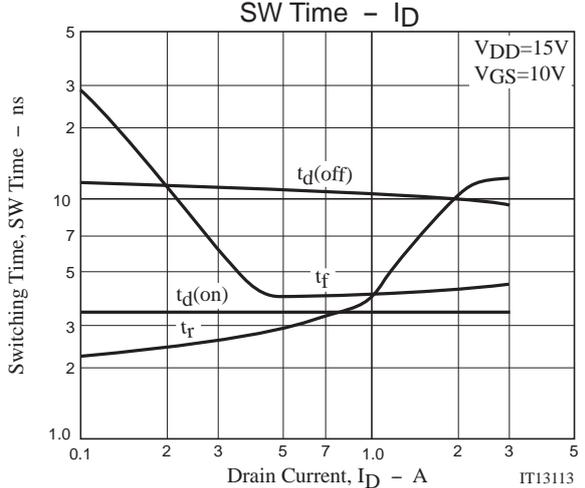
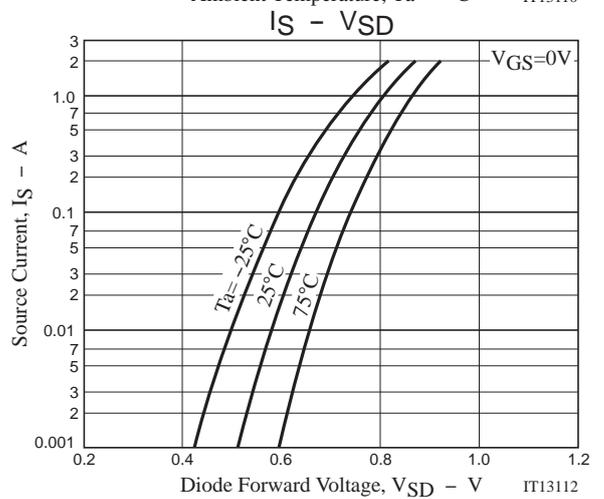
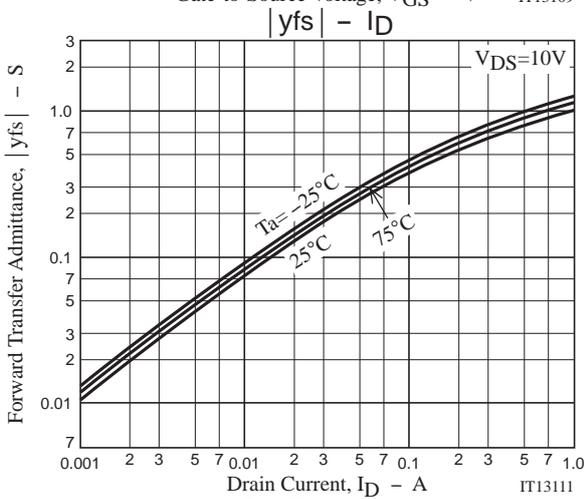
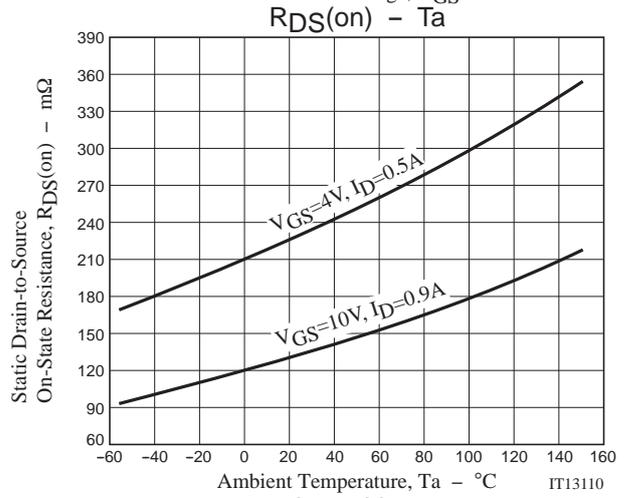
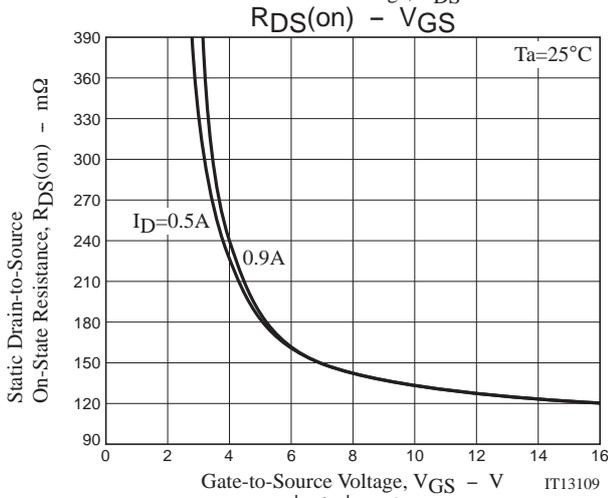
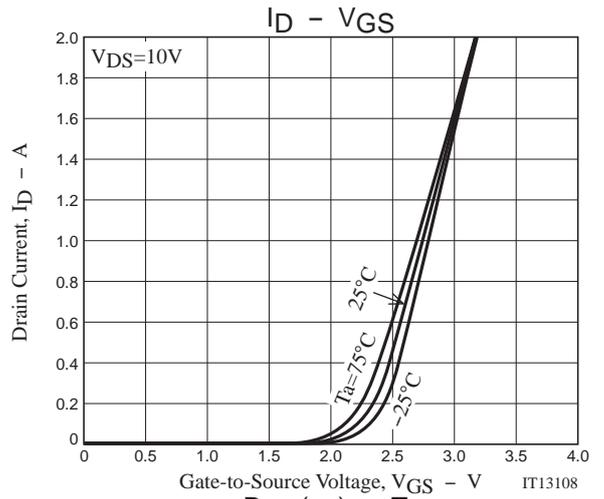
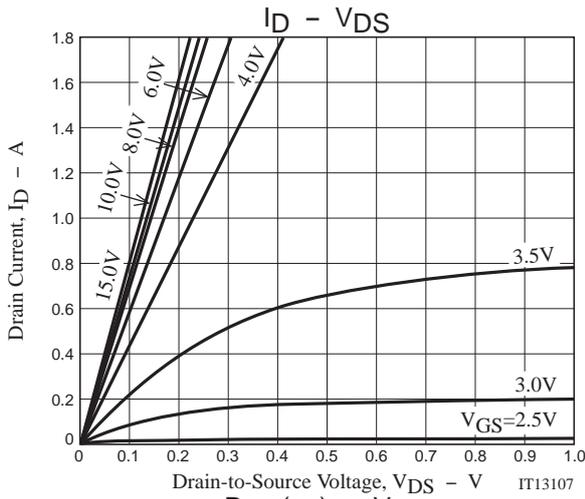
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0V	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±16V, V _{DS} =0V			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =0.9A	0.66	1.1		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =0.9A, V _{GS} =10V		135	180	mΩ
	R _{DS(on)2}	I _D =0.5A, V _{GS} =4V		230	330	mΩ
Input Capacitance	C _{iss}	V _{DS} =10V, f=1MHz		88		pF
Output Capacitance	C _{oss}			19		pF
Reverse Transfer Capacitance	C _{rss}			11		pF
Turn-ON Delay Time	t _{d(on)}			3.4		ns
Rise Time	t _r	See specified Test Circuit.		3.6		ns
Turn-OFF Delay Time	t _{d(off)}			10.5		ns
Fall Time	t _f			4.0		ns
Total Gate Charge	Q _g			2.0		nC
Gate-to-Source Charge	Q _{gs}	V _{DS} =10V, V _{GS} =10V, I _D =1.8A		0.33		nC
Gate-to-Drain "Miller" Charge	Q _{gd}			0.29		nC
Diode Forward Voltage	V _{SD}		I _S =1.8A, V _{GS} =0V	0.86	1.2	V

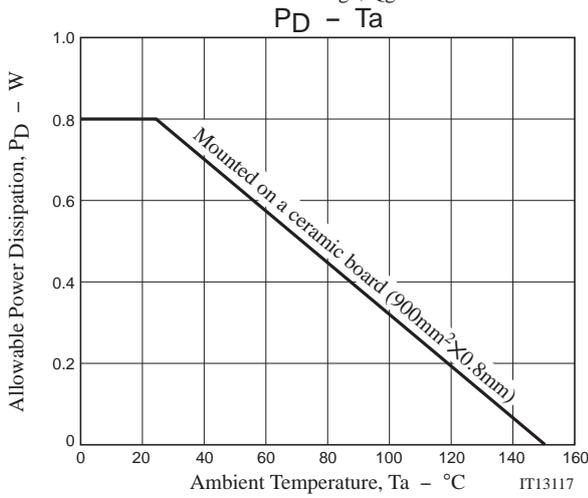
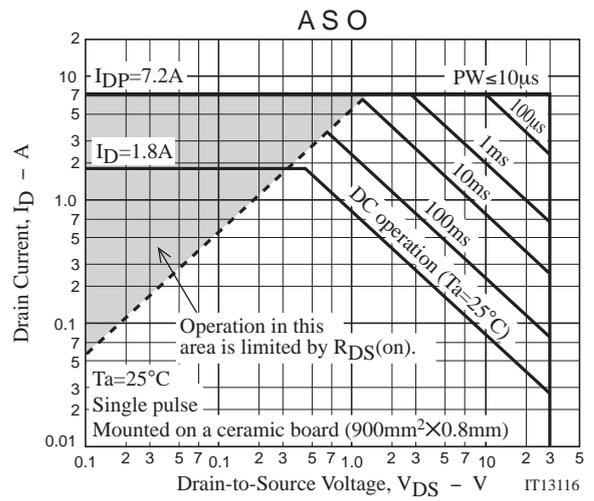
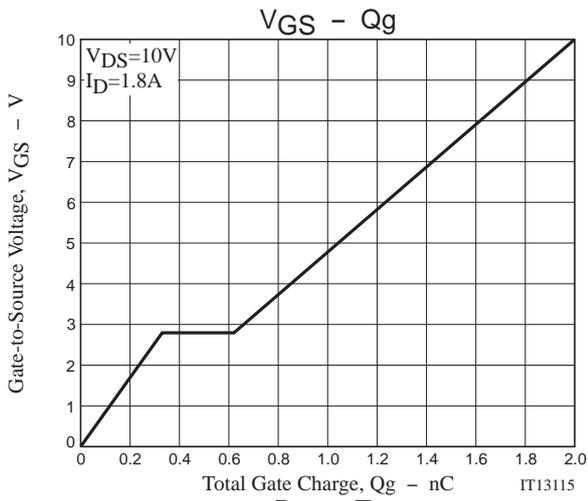
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
MCH3475-TL-E	MCPH3	3,000pcs./reel	Pb Free





Taping Specification

MCH3475-TL-E

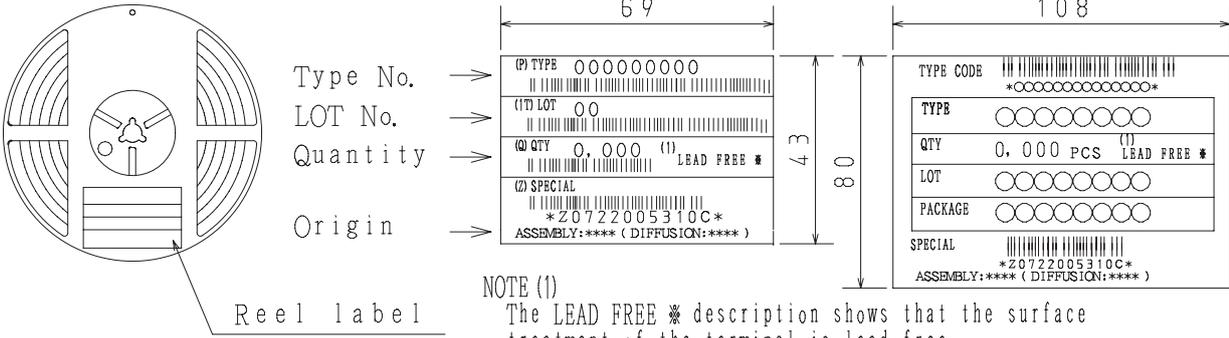
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH3	MCPH3	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method

Reel label, Inner box label (unit:mm) Outer box label

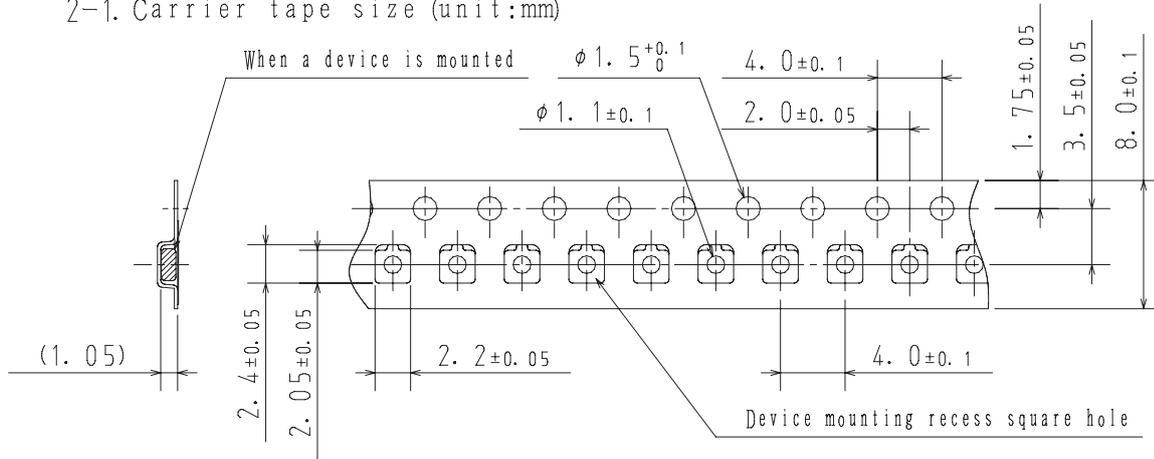
It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.



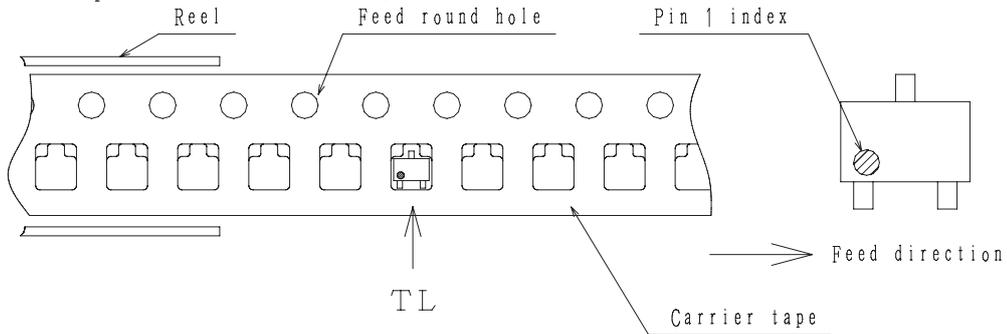
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



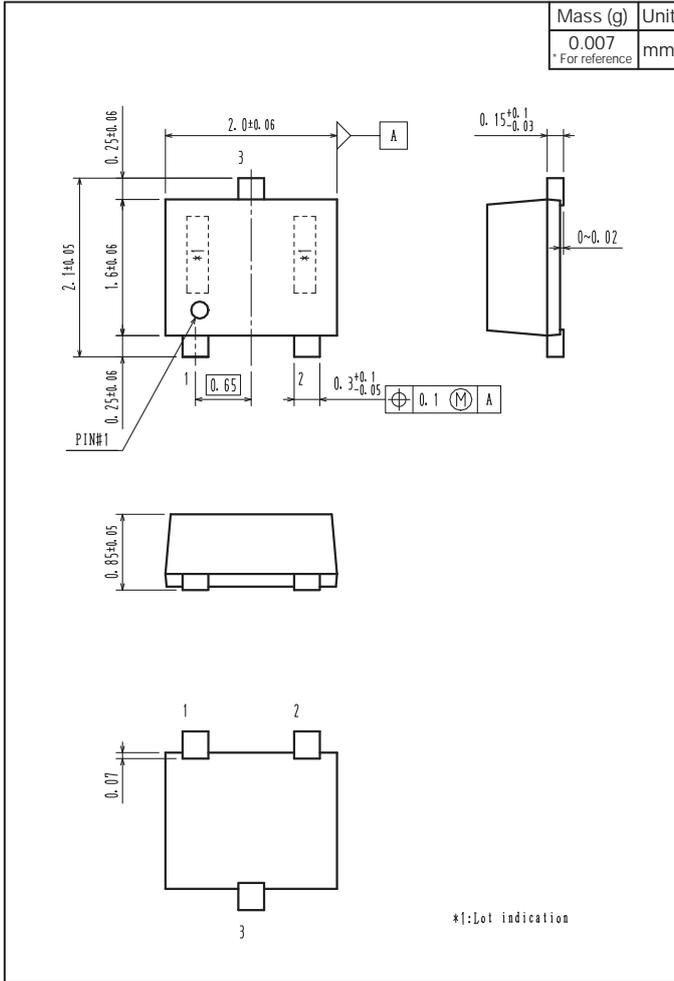
2-2. Device placement direction



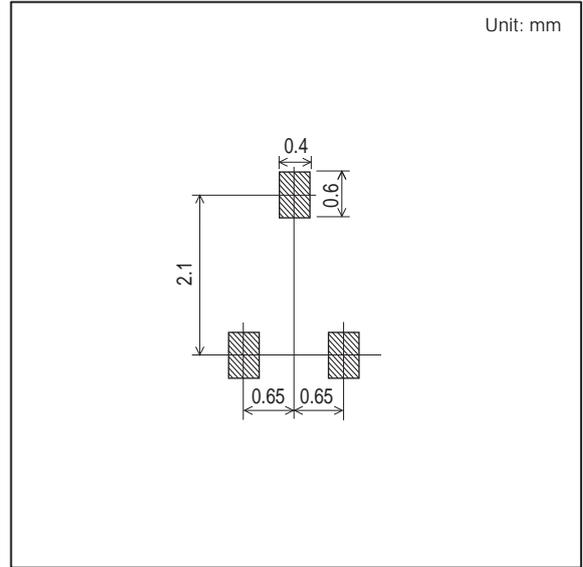
Those with pin 1 index on the feed hole side.....TL

MCH3475

Outline Drawing MCH3475-TL-E



Land Pattern Example



Note on usage : Since the MCH3475 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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