

## SIOV metal oxide varistors

Leaded varistors, SNF high operating temperature varistors, SNF SuperioR-MP, SNF20 series

Series/Type: B722\*

Date: January 2018

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## SNF SuperioR-MP, SNF20 series

#### Construction

- Round varistor element, leaded
- Coating: silicon resin, flame-retardant to UL 94 V-0
- Terminals: tinned wire

#### **Features**

- Wide operating voltage range 130 ... 550 V<sub>RMS</sub>
- All types duty cycle @ 6 kV/ 3 kA = >10 pulses, according to IEC 62368-1; G.8.2 and IEC 60950-1; Annex Q, IEC 61051-2
- All types I<sub>n</sub> @ 5 kA = >15 impulses according to UL 1449, 4<sup>th</sup> edition surge current generator (8/20 µs), type 5 listed
- Enhanced resistance against heat and humidity 85 °C, 85% r.h., 0.85 · V<sub>V</sub> (1 mA), 1000 h for use in harsh environments
- Multiple pulse handling capability

#### **Approvals**

- UL
- CSA (all types ≤320 V<sub>RMS</sub>)
- VDE
- CQC
- IEC

## **Options**

- Types qualified to AEC-Q200 available in SNF automotive series or upon request
- Special insulation types upon request

#### **Delivery mode**

Bulk (standard)

#### General technical data

Climatic category	to IEC 60068-1	40/125/56	
Operating temperature	to IEC 61051	-40 +125	°C
Storage temperature		-40 +150	°C
Electric strength	to IEC 61051	≥ 2.5	kV <sub>RMS</sub>
Insulation resistance	to IEC 61051	≥ 100	$M\Omega$



## SNF SuperioR-MP, SNF20 series



## Electrical specifications and ordering codes Maximum ratings ( $T_A = 125$ °C)

Ordering code	Туре	$V_{RMS}$	$V_{DC}$	i <sub>max</sub>	I <sub>n</sub> 1)	$W_{max}$	P <sub>max</sub>
	(untaped)			(8/20 µs)	(8/20 µs)	(2 ms)	
	SIOV-			1 time	15 times		
		V	٧	Α	Α	J	W
B72220U3131K502	SNF20K130E3S5K1	130	170	12000	5000	135	1.00
B72220U3141K502	SNF20K140E3S5K1	140	180	12000	5000	145	1.00
B72220U3151K502	SNF20K150E3S5K1	150	200	12000	5000	155	1.00
B72220U3171K502	SNF20K175E3S5K1	175	225	12000	5000	180	1.00
B72220U3211K502	SNF20K210E3S5K1	210	270	12000	5000	215	1.00
B72220U3231K502	SNF20K230E3S5K1	230	300	12000	5000	235	1.00
B72220U3251K502	SNF20K250E3S5K1	250	320	12000	5000	255	1.00
B72220U3271K502	SNF20K275E3S5K1	275	350	12000	5000	280	1.00
B72220U3301K502	SNF20K300E3S5K1	300	385	12000	5000	305	1.00
B72220U3321K502	SNF20K320E3S5K1	320	420	12000	5000	330	1.00
B72220U3351K502	SNF20K350E3S5K1	350	460	12000	5000	335	1.00
B72220U3381K502	SNF20K385E3S5K1	385	505	12000	5000	370	1.00
B72220U3421K502	SNF20K420E3S5K1	420	560	12000	5000	405	1.00
B72220U3461K502	SNF20K460E3S5K1	460	615	12000	5000	445	1.00
B72220U3511K502	SNF20K510E3S5K1	510	670	10000	5000	445	1.00
B72220U3551K502	SNF20K550E3S5K1	550	745	10000	5000	490	1.00

<sup>&</sup>lt;sup>1)</sup> **Note:** Nominal discharge current I<sub>n</sub> according to UL 1449, 4<sup>th</sup> edition.

## Characteristics (T<sub>A</sub> = 25 °C)

Ordering code	Туре	$V_{v}$	$\Delta V_{v}$	V <sub>c,max</sub>	i <sub>c</sub>	$C_{typ}$
•	(untaped)	(1 mA)	(1 mA)	(i <sub>c</sub> )		(1 kHz)
	SIOV-	V	%	V	Α	pF
B72220U3131K502	SNF20K130E3S5K1	205	±10	340	100	2400
B72220U3141K502	SNF20K140E3S5K1	220	±10	360	100	2250
B72220U3151K502	SNF20K150E3S5K1	240	±10	395	100	2050
B72220U3171K502	SNF20K175E3S5K1	270	±10	455	100	1800
B72220U3211K502	SNF20K210E3S5K1	330	±10	545	100	1500
B72220U3231K502	SNF20K230E3S5K1	360	±10	595	100	1400
B72220U3251K502	SNF20K250E3S5K1	390	±10	650	100	1300
B72220U3271K502	SNF20K275E3S5K1	430	±10	710	100	1150
B72220U3301K502	SNF20K300E3S5K1	470	±10	775	100	1050
B72220U3321K502	SNF20K320E3S5K1	510	±10	840	100	1000
B72220U3351K502	SNF20K350E3S5K1	560	±10	910	100	900
B72220U3381K502	SNF20K385E3S5K1	620	±10	1025	100	800
B72220U3421K502	SNF20K420E3S5K1	680	±10	1120	100	730
B72220U3461K502	SNF20K460E3S5K1	750	±10	1240	100	660
B72220U3511K502	SNF20K510E3S5K1	820	±10	1355	100	600
B72220U3551K502	SNF20K550E3S5K1	910	±10	1500	100	550

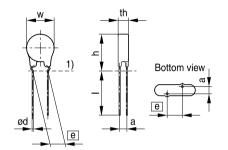




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## SNF SuperioR-MP, SNF20 series

## **Dimensional drawings**



## Weight

Nominal diameter	$V_{RMS}$	Weight
mm	V	g
20	130 550	7.0 10.5

The weight of varistors in between these voltage classes can be interpolated.

1) Seating plane to IEC 60717

VAR0727-N-E

**Please note:** Paint legs may have cracks or chips due to the mechanical forces acting on the wires, but this does not affect the performance of the component.

#### Dimensions

Ordering code	[e] ±1	a (typical)	W <sub>max</sub>	th <sub>max</sub>	h <sub>max</sub>	I <sub>min</sub>	d ±0.05
•	mm	mm	mm	mm	mm	mm	mm
B72220U3131K502	10.0	2.2	24.5	8.1	31.5	25.0	1.0
B72220U3141K502	10.0	2.3	24.5	8.2	31.5	25.0	1.0
B72220U3151K502	10.0	2.4	24.5	8.3	31.5	25.0	1.0
B72220U3171K502	10.0	2.6	24.5	8.0	31.5	25.0	1.0
B72220U3211K502	10.0	2.9	24.5	8.8	31.5	25.0	1.0
B72220U3231K502	10.0	3.1	24.5	9.0	31.5	25.0	1.0
B72220U3251K502	10.0	3.2	24.5	9.1	31.5	25.0	1.0
B72220U3271K502	10.0	3.5	24.5	9.5	31.5	25.0	1.0
B72220U3301K502	10.0	3.8	24.5	9.8	31.5	25.0	1.0
B72220U3321K502	10.0	3.9	24.5	9.9	31.5	25.0	1.0
B72220U3351K502	10.0	4.1	24.5	10.0	31.5	25.0	1.0
B72220U3381K502	10.0	4.5	24.5	11.3	32.0	25.0	1.0
B72220U3421K502	10.0	4.7	24.5	11.6	32.0	25.0	1.0
B72220U3461K502	10.0	4.8	24.5	11.9	32.0	25.0	1.0
B72220U3511K502	10.0	4.9	25.0	12.3	32.5	25.0	1.0
B72220U3551K502	10.0	5.0	25.0	12.8	32.5	25.0	1.0



# Leaded varistors, SNF high operating temperature B722\* SNF SuperioR-MP, SNF20 series



## Reliability data

Test	Test methods/ conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called $V_V$ (1 mA <sub>DC</sub> @ 0.2 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Endurance at upper category temperature	1000 h at UCT After having continuously applied the maximum allowable AC voltage at UCT $\pm 2$ °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V $_{\rm V}$ shall be measured.	IΔV/V (1 mA)l ≤10%
Surge current derating, 8/20 μs	10 surge currents (8/20 $\mu$ s), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 $\mu$ s	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 10 impulses at 2 ms	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Electric strength	IEC 61051-1, test 4.9.2  Metal balls method, 2500 V <sub>RMS</sub> , 60 s  The varistor is placed in a container holding 1.6 ±0.2 mm diameter metal balls such that only the terminations of the varistor are protruding.  The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	No breakdown





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## SNF SuperioR-MP, SNF20 series

Test	Test methods/ conditions	Requirement
Climatic sequence	The specimen shall be subjected to: a) dry heat at UCT, 16 h, IEC 60068-2-2, test Ba b) damp heat, 1st cycle: $55^{\circ}\text{C}$ , $93\%\text{r}$ . H., $24\text{h}$ , IEC 60068-2-30, test Db c) cold, LCT, 2 h, IEC 60068-2-1, test Aa d) damp heat, additional 5 cycles: $55^{\circ}\text{C}/25^{\circ}\text{C}$ , $93\%\text{r}$ . H., $24\text{h}/\text{cycle}$ , IEC 60068-2-30, test Db. Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of $V_{\text{V}}$ shall be measured. Thereafter, insulation resistance $R_{\text{ins}}$ shall be measured at $V=500\text{V}$ .	IΔV/V (1 mA)I ≤10% R <sub>ins</sub> ≥100 MΩ
Rapid change of temperature  Damp heat	IEC 60068-2-14, test Na, LCT/UCT, dwell time 10 min, 1000 cycles IEC 60068-2-78, test Cy 85 °C, 85% r. H., 0.85 * V <sub>v</sub> (1 mA), 1000	IΔV/V (1 mA)I ≤5% No visible damage IΔV/V (1 mA)I ≤10% No visible damage
Solderability	IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s: After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	The inspection shall be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.



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Test	Test methods/ conditions	Requirement
Resistance to soldering	IEC 60068-2-20, test Tb, method 1A,	∆V/V (1 mA)  ≤5%
heat	260 °C, 10 s:	No visible damage
	Each lead shall be dipped into a solder	
	bath having a temperature of 260 ±5 °C	
	to a point 2.0 to 2.5 mm from the body of	
	the specimen, be held there for $10 \pm 1 \text{ s}$	
	and then be stored at room temperature	
	and normal humidity for 1 to 2 h.	
	The change of V <sub>V</sub> shall be measured and	
	the specimen shall be visually examined.	
Tensile strength	IEC 60068-2-21, test Ua1	l∆V/V (1 mA)l ≤5%
	After gradually applying the force	No break of solder joint,
	specified below and keeping the unit	no wire break
	fixed for 10 s, the terminal shall be	
	visually examined for any damage.	
	Force for wire diameter:	
	0.6 mm = 10 N	
	0.8 mm = 10 N	
	1.0 mm = 20 N	
Vibration	IEC 60068-2-6, test Fc, method B4	∆V/V (1 mA)  ≤5%
	Frequency range: 10 55 Hz	No visible damage
	Amplitude: 0.75 mm or 98 m/s <sup>2</sup>	_
	Duration: 6 h (3 · 2 h)	
	Pulse: sine wave	
	After repeatedly applying a single	
	harmonic vibration according to the table	
	above.	
	The change of V <sub>V</sub> shall be measured and	
	the specimen shall be visually examined.	
Bump	IEC 60068-2-29, test Eb	l∆V/V (1 mA)l ≤5%
	Pulse duration: 6 ms	No visible damage
	Max. acceleration: 400 m/s <sup>2</sup>	
	Number of bumps:4000	
	Pulse: half sine	
Fire hazard	IEC 60695-11-5 (needle flame test)	5 s max.
	Severity: vertical 10 s	

#### Note:

UCT = Upper category temperature LCT = Lower category temperature

R<sub>ins</sub> = Insulation resistance



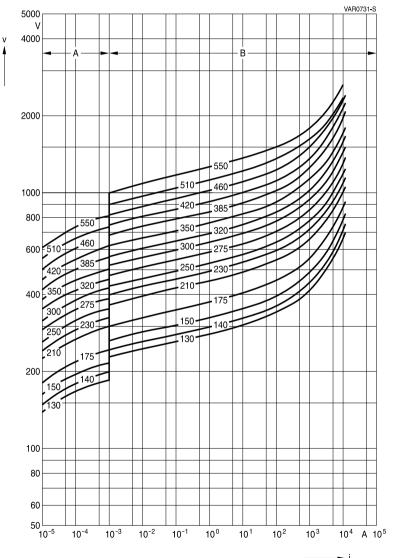


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## SNF SuperioR-MP, SNF20 series

#### v/i characteristics

v = f(i) - for explanation of the characteristics refer to "General technical information", 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances



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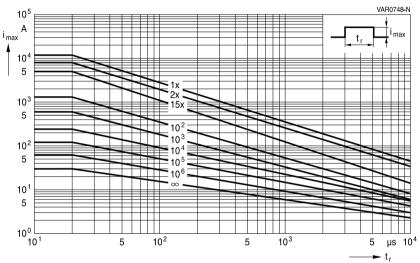
## SNF SuperioR-MP, SNF20 series



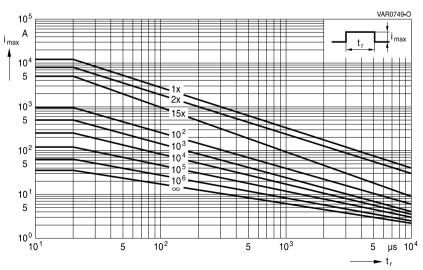
## **Derating curves**

Maximum surge current  $i_{max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", section 1.8.1



#### SIOV-SNF20K130 ... K320E3S5K1



SIOV-SNF20K350 ... K460E3S5K1





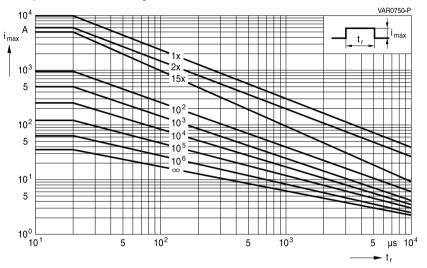
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## SNF SuperioR-MP, SNF20 series

## **Derating curves**

Maximum surge current  $i_{max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-SNF20K510 ... K550E3S5K1



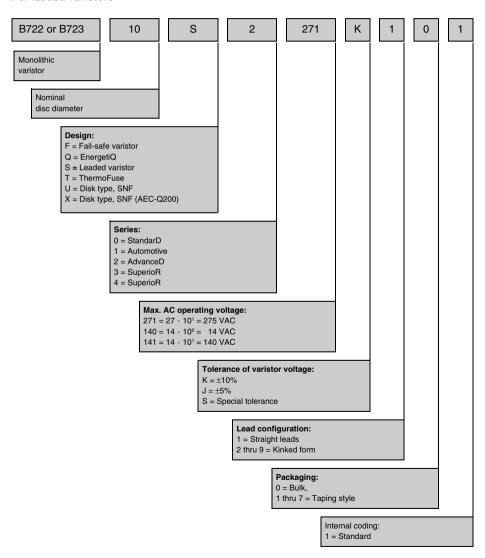
# Leaded varistors, SNF high operating temperature B722\* SNF SuperioR-MP, SNF20 series



## Taping, packaging and lead configuration

#### 1 EPCOS ordering code system

#### For leaded varistors







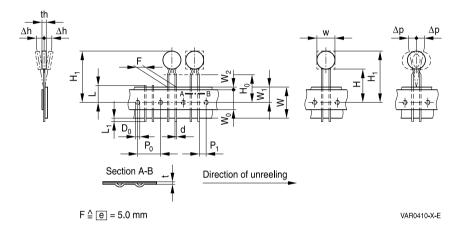
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#### SNF SuperioR-MP, SNF20 series

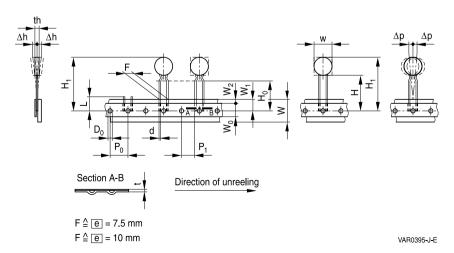
## 2 Taping and packaging of leaded varistors

Tape packaging for lead spacing  $\boxed{e}$  = 5 fully conforms to IEC 60286-2, while for lead spacings  $\boxed{e}$  = 7.5 and 10 the taping mode is based on this standard.

## 2.1 Taping in accordance with IEC 60286-2 for lead spacing 5.0 mm



## 2.2 Taping based on IEC 60286-2 for lead spacing 7.5 and 10 mm





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## SNF SuperioR-MP, SNF20 series



## 2.3 Tape dimensions (in mm)

Sym-	<i>e</i> = 5.0	Tolerance	<i>e</i> = 7.5	Tolerance	<i>e</i> = 10.0	Tolerance	Remarks
bol							
w		max.		max.		max.	see tables in
							each series
th		max.		max.		max.	under
							"Dimensions"
d	0.6	±0.05	0.8	±0.05	1.0	±0.05	
$P_0$	12.7	±0.3	12.71)	±0.3	12.7	±0.3	±1 mm/20
							sprocket holes
P <sub>1</sub>	3.85	±0.7	8.95	±0.8	7.7	±0.8	
F	5.0	+0.6/-0.1	7.5	±0.8	10.0	±0.8	
$\Delta h$	0	±2.0	depends of	n s	depends on	S	measured at
Δр	0	±1.3	0	±2.0	0	±2.0	top of compo-
							nent body
W	18.0	±0.5	18.0	±0.5	18.0	±0.5	
$W_{o}$	5.5	min.	11.0	min.	11.0	min.	Peel-off
							force ≥ 5 N
$W_1$	9.0	±0.5	9.0	+0.75/-0.5	9.0	+0.75/-0.5	
$W_2$	3.0	max.	3.0	max.	3.0	max.	
Н	18.0	+2.0/-0	18.0	+2.0/-0	18.0	+2.0/-0	2)
$H_0$	16.0	±0.5	16.0	±0.5	16.0	±0.5	3)
	(18.0)		(18.0)				
H <sub>1</sub>	32.2	max.	45.0	max.	45.0	max.	
$\overline{D_0}$	4.0	±0.2	4.0	±0.2	4.0	±0.2	
t	0.9	max.	0.9	max.	0.9	max.	without lead
L	11.0	max.	11.0	max.	11.0	max.	
$L_1$	0.5	max.					

<sup>1)</sup> Taping with  $P_0 = 15.0$  mm upon request

<sup>2)</sup> Applies only to uncrimped types

<sup>3)</sup> Applies only to crimped types ( $H_0 = 18$  upon request)





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## SNF SuperioR-MP, SNF20 series

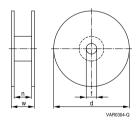
### 2.4 Taping mode

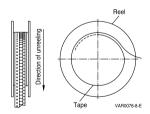
Example: B72210S0271K1 5 1

Digit 14

Digit 14	Taping	Reel type	Seating plane height H <sub>0</sub>	Seating plane height H	Pitch distance
J	mode	,,	for crimped types	for uncrimped types	P <sub>0</sub>
			mm	mm	mm
0	_	Bulk	_	_	_
1	G	1	16	18	12.7
2	G2	1	18	_	12.7
3	G3	II	16	18	12.7
4	G4	II	18	_	12.7
5	G5	Ш	16	18	12.7
6	GA	Ammo pack	16	18	12.7
7	G2A	Ammo pack	18	_	12.7
Internal	coding fo	r special tapin	g		_
	G6	Ш	18	_	12.7
	G10	II	16	18	15.0
	G11	II	18	_	15.0
	G10A	Ammo pack	16	18	15.0
	G11A	Ammo pack	18	_	15.0

#### 2.5 Reel dimension





## Dimensions (in mm)

Reel type	d	f	n	w
I	360 max.	31 ±1	approx. 45	54 max.
II	360 max.	31 ±1	approx. 55	64 max.
III	500 max.	23 ±1	approx. 59	72 max.

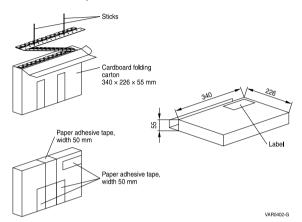
If reel type III is not compatible with insertion equipment because of its large diameter, nominal disk diameter 10 mm and 14 mm can be supplied on reel II upon request (taping mode G3).

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#### SNF SuperioR-MP, SNF20 series



## 2.6 Ammo pack dimensions



## 3 Lead configuration

Straight leads are standard for disk varistors. Other lead configurations as crimp style or customer-specific lead wire length according to 3.1, 3.2, 3.3 and 3.4 are optional. Crimped leads (non-standard) are differently crimped for technical reasons; the individual crimp styles are denoted by consecutive numbers (S, S2 through S5) as shown in the dimensional drawings below.

The crimp styles of the individual types can be seen from the type designation in the ordering tables.

## 3.1 Crimp style mode

Example: B72210S0271K 5 01 | Digit 13

Digit 13 of ordering code	Crimp style	Figure
1	Standard, straight leads	1
2	S2	2
3	S3	3
5	S5	4
Available upon request		
Internal coding	1	5





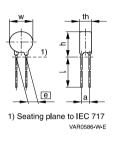
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## SNF SuperioR-MP, SNF20 series

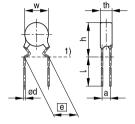
### 3.2 Standard leads and non-standard crimp styles

The basic dimensions in figure 1 to 5 are valid for types with either round or square (EnergetiQ series) component head.

## Standard, straight leads

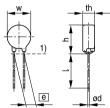


## Non-standard, crimp style S2



1) Seating plane to IEC 60717 VAR0411-F-E

Non-standard, crimp style S3



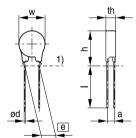
1) Seating plane to IEC 60717 VAR0396-R-E

Figure 1

Figure 2

Figure 3

## Non-standard, crimp style S5



1) Seating plane to IEC 60717 VAR0726-M-E

Figure 4



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## SNF SuperioR-MP, SNF20 series

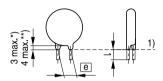


## 3.3 Trimmed leads (non-standard)

Varistors with cut leads available upon request.

Lead length tolerances:

Straight leads +/-0.8 mmCrimped leads +/-0.5 mmMinimum lead length 3.0 mm



- 1) Seating plane to IEC 60717
- \*) For round component head
- \*\*) For EnergetiQ series, square component head

Figure 5





**R722** 

#### SNF SuperioR-MP, SNF20 series

#### Cautions and warnings

#### General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- 2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

#### Storage

- 1. Store SIOVs only in original packaging. Do not open the package prior to processing.
- 2. Recommended storage conditions in original packaging:

Storage temperature: −25 °C ... +45 °C,

Relative humidity: <75% annual average,

<95% on maximum 30 days a year.

Dew precipitation: is to be avoided.

- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- The SIOV type series should be soldered after shipment from EPCOS within the time specified:

SIOV-S, -Q, -LS, -B, -SNF 24 months ETFV/ T series. -CU 12 months.

#### Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

#### Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.
- Temperatures of all preheat stages and the solder bath must be strictly controlled especially for T series (T14 and T20).



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## SNF SuperioR-MP, SNF20 series



### Mounting

- Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

#### Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.

### Display of ordering codes for EPCOS products

The ordering code for one and the same EPCOS product can be represented differently in data sheets, data books, other publications, on the EPCOS website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.epcos.com/orderingcodes





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## SNF SuperioR-MP, SNF20 series

## Symbols and terms

Symbol	Term
С	Capacitance
$C_{typ}$	Typical capacitance
i	Current
i <sub>c</sub>	Current at which V <sub>c, max</sub> is measured
I <sub>leak</sub>	Leakage current
i <sub>max</sub>	Maximum surge current (also termed peak current)
I <sub>max</sub>	Maximum discharge current
I <sub>n</sub>	Nominal discharge current to UL 1449
LCT	Lower category temperature
$L_{typ}$	Typical inductance
$P_{\text{max}}$	Maximum average power dissipation
R <sub>ins</sub>	Insulation resistance
$R_{\text{min}}$	Minimum resistance
$T_A$	Ambient temperature
t <sub>r</sub>	Duration of equivalent rectangular wave
UCT	Upper category temperature
V	Voltage
$V_{clamp}$	Clamping voltage
V <sub>c, max</sub>	Maximum clamping voltage at specified current i <sub>c</sub>
$V_{DC}$	DC operating voltage
$V_{\text{jump}}$	Maximum jump start voltage
$V_{max}$	Maximum voltage
$V_{op}$	Operating voltage
$V_{RMS}$	AC operating voltage, root-mean-square value
$V_{RMS,\;op,\;max}$	Root-mean-square value of max. DC operating voltage incl. ripple current
$V_{\text{surge}}$	Super imposed surge voltage
$V_{V}$	Varistor voltage
$\Delta V_{V}$	Tolerance of varistor voltage
$W_{LD}$	Maximum load dump
$W_{\text{max}}$	Maximum energy absorption
е	Lead spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.



#### Important notes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
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- 7. Our manufacturing sites serving the automotive business apply the IATF 16949 standard. The IATF certifications confirm our compliance with requirements regarding the quality management system in the automotive industry. Referring to customer requirements and customer specific requirements ("CSR") TDK always has and will continue to have the policy of respecting individual agreements. Even if IATF 16949 may appear to support the acceptance of unilateral requirements, we hereby like to emphasize that only requirements mutually agreed upon can and will be implemented in our Quality Management System. For clarification purposes we like to point out that obligations from IATF 16949 shall only become legally binding if individually agreed upon.



#### Important notes

8. The trade names EPCOS, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, ExoCore, FilterCap, FormFit, LeaXield, MiniBlue, MiniCell, MKD, MKK, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, ThermoFuse, WindCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.

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