# 1. General description

NPN high-voltage low  $V_{CEsat}$  Breakthrough In Small Signal (BISS) transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

PNP complement: PBHV3160Z

## 2. Features and benefits

- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability
- High collector current gain h<sub>FE</sub> at high I<sub>C</sub>

# 3. Applications

- Electronic ballast for fluorecent lighting
- LED driver for LED chain module
- LCD backlighting
- HID front lighting
- · Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	600	V
I <sub>C</sub>	collector current		-	-	0.1	Α



600 V, 0.1 A NPN high-voltage low VCEsat (BISS) transistor

# 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	2, 4
2	С	collector		1—
3	Е	emitter		. 1
4	С	collector	☐1 ☐2 ☐3 SC-73 (SOT223)	3 sym016

# 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBHV2160Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223

# 7. Marking

Table 4. Marking codes

•	
Type number	Marking code
PBHV2160Z	HV216Z

600 V, 0.1 A NPN high-voltage low VCEsat (BISS) transistor

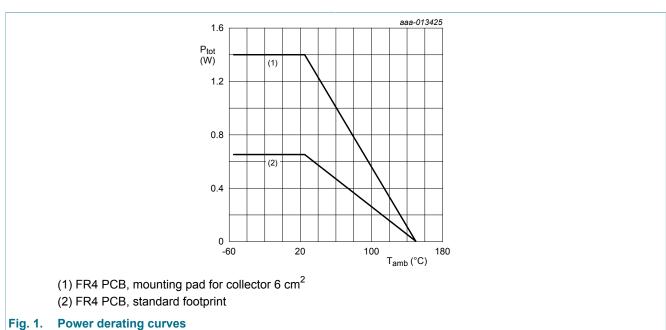
# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	600	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	600	V
V <sub>CESM</sub>	collector-emitter peak voltage	V <sub>BE</sub> = 0 V		-	600	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	0.1	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.4	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



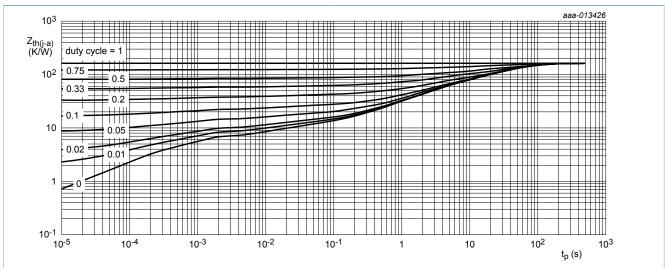
600 V, 0.1 A NPN high-voltage low VCEsat (BISS) transistor

## Thermal characteristics

Table 6. **Thermal characteristics** 

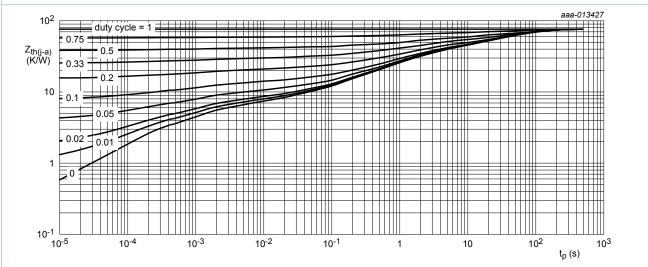
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
fro	thermal resistance from junction to ambient	in free air	[1]	-	-	190	K/W
			<u>[2]</u>	-	-	89	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	20	K/W

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



FR4 PCB, single-sided copper, tin-plated and standard footprint.

Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig. 2.



FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

PBHV2160Z

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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### 600 V, 0.1 A NPN high-voltage low VCEsat (BISS) transistor

## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 400 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
	current	V <sub>CB</sub> = 400 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	10	μA
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = 400 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	-	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB}$ = 4.8 V; $I_{C}$ = 0 A; $T_{amb}$ = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C	70	125	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 30 \text{ mA}; I_B = 6 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	65	125	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C}$ = 50 mA; $I_{B}$ = 5 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb}$ = 25 °C	-	-	950	mV
C <sub>c</sub>	collector capacitance	$V_{CB} = 20 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	1.7	-	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = 0.5 V; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	81	-	pF

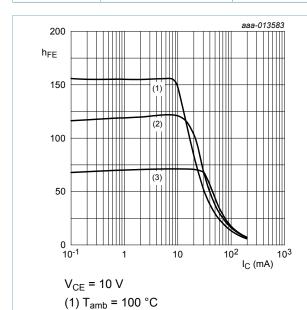
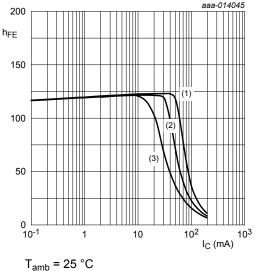


Fig. 4. DC current gain as a function of collector current; typical values

(2)  $T_{amb} = 25 \, ^{\circ}C$ (3)  $T_{amb} = -55 \, ^{\circ}C$ 



 $I_{amb} = 25 \text{ C}$ (1)  $V_{CE} = 50 \text{ V}$ (2)  $V_{CE} = 25 \text{ V}$ (3)  $V_{CE} = 10 \text{ V}$ 

Fig. 5. DC current gain as a function of collector current; typical values

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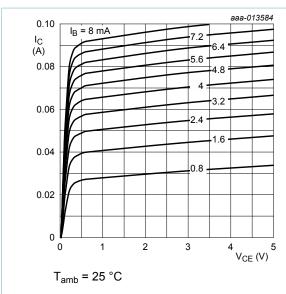
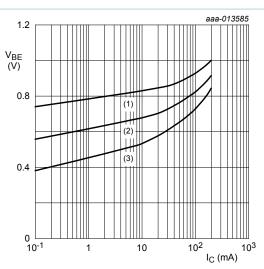


Fig. 6. Collector current as a function of collectoremitter voltage; typical values



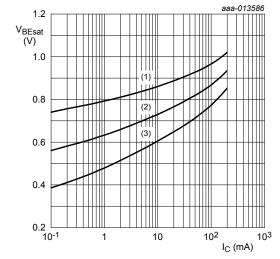
 $V_{CE}$  = 10 V

(1)  $T_{amb} = -55 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb}$  = 100 °C

Fig. 7. Base-emitter voltage as a function of collector current; typical values



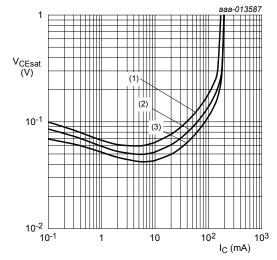
 $I_C/I_B = 5$ 

(1)  $T_{amb} = -55 \,^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3) T<sub>amb</sub>= 100 °C

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values



 $I_C/I_B = 5$ 

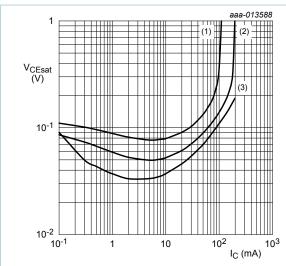
(1)  $T_{amb} = 100 \, ^{\circ}C$ 

(2)  $T_{amb}$  = 25 °C

(3)  $T_{amb} = -55 \, ^{\circ}C$ 

ig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

## 600 V, 0.1 A NPN high-voltage low VCEsat (BISS) transistor



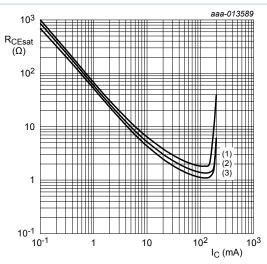
$$T_{amb}$$
 = 25 °C

(1) 
$$I_C/I_B = 10$$

(2) 
$$I_C/I_B = 5$$

(3) 
$$I_C/I_B = 2.5$$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values



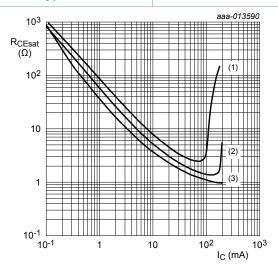
$$I_{\rm C}/I_{\rm B}=5$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values



$$T_{amb}$$
 = 25 °C

(1) 
$$I_C/I_B = 10$$

(2) 
$$I_C/I_B = 5$$

(3) 
$$I_C/I_B = 2.5$$

Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

600 V, 0.1 A NPN high-voltage low VCEsat (BISS) transistor

# 11. Package outline

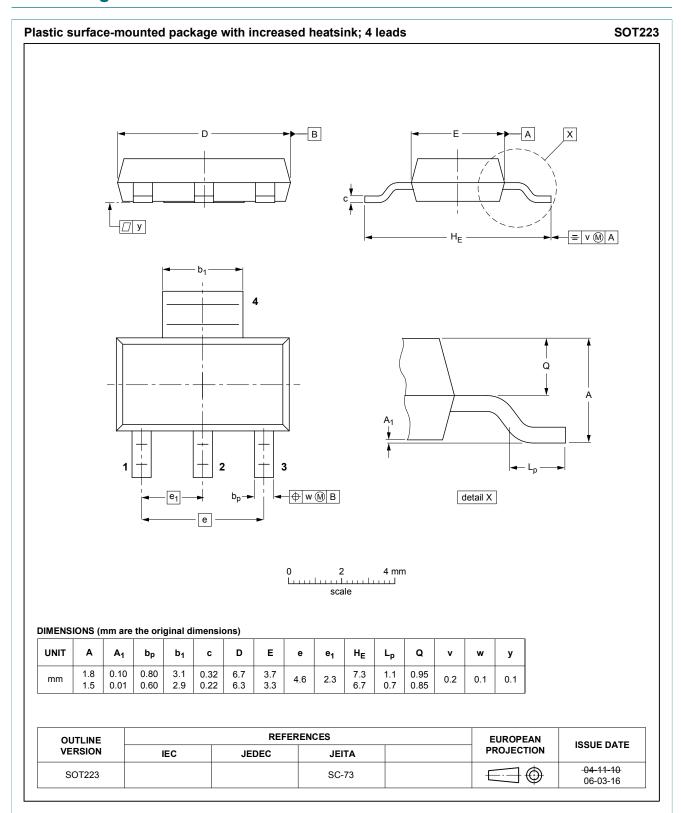


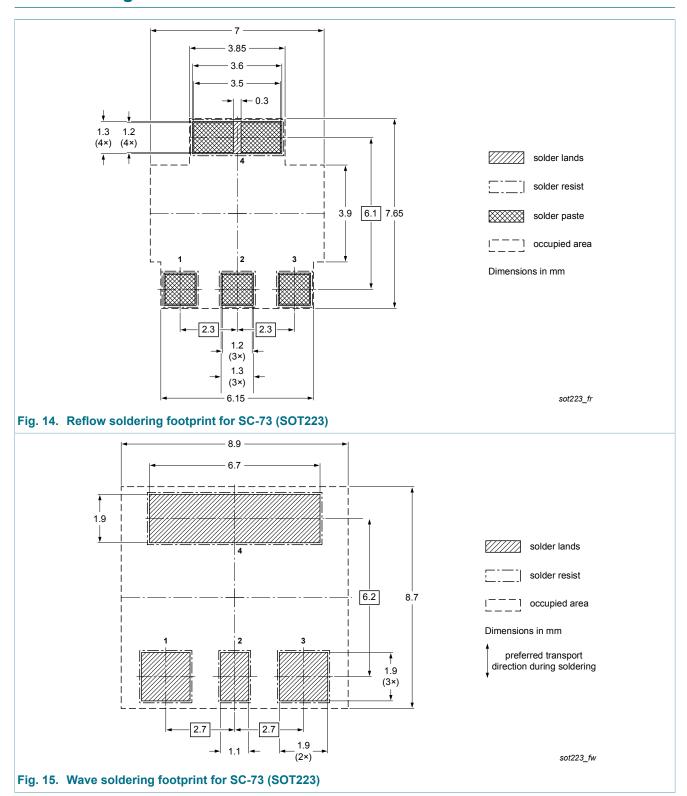
Fig. 13. Package outline SC-73 (SOT223)

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# 12. Soldering



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# 13. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBHV2160Z v.1	20150624	Product data sheet	-	-

### 600 V, 0.1 A NPN high-voltage low VCEsat (BISS) transistor

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