

PESD2ETH-X

Ultra low capacitance double rail-to-rail ESD protection diode

24 February 2016 Product data sheet

1. General description

Ultra low capacitance double rail-to-rail ElectroStatic Discharge (ESD) protection diode in a small SOT143B Surface-Mounted Device (SMD) plastic package.

The device is designed to protect two high-speed data lines or high-frequency signal lines from the damage caused by ESD and other transients.

The device integrates two ultra low capacitance rail-to-rail diodes and one additional ESD protection diode to ensure signal line protection even if no supply voltage is available.

2. Features and benefits

- ESD protection of two high-speed data lines
- Ultra low capacitance: C_d = 1 pF
- IEC 61000-4-2 up to 8 kV
- Very low reverse current
- AEC-Q101 qualified

3. Applications

- 100BASE-T1 / OPEN Alliance BroadR-Reach automotive Ethernet
- Low-Voltage Differential Signaling (LVDS) automotive
- USB 2.0 automotive

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Per diode	Per diode							
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	1	1.5	pF	
			[2]	-	0.6	-	pF	
			[3]	-	16	-	pF	
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	5.5	V	

- [1] Measured from pin 2 and 3 to ground.
- [2] Measured from pin 2 to pin 3.
- [3] Measured from pin 4 to ground.



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Pinning information

Table 2. **Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	GND	ground	4 3	
2	I/O 1	input/output 1		1 1 4
3	I/O 2	input/output 2		
4	V _{CC}	supply line	1 2 SOT143B	006aaa482

Ordering information

Table 3. **Ordering information**

Type number		Package					
		Name	Description	Version			
	PESD2ETH-X	SOT143B	plastic surface-mounted package; 4 leads	SOT143B			

Marking codes Table 4.

Type number	Marking code
	[1]
PESD2ETH-X	2B%

[1] % = placeholder for manufacturing site code

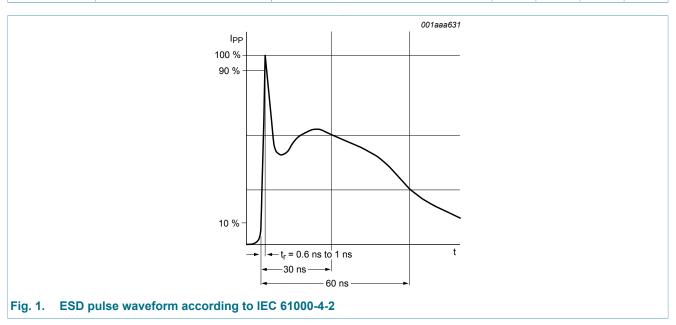
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7. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
T _{amb}	ambient temperature		-40	125	°C
T _{stg}	storage temperature		-55	125	°C
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; level 4; contact discharge	-	8	kV



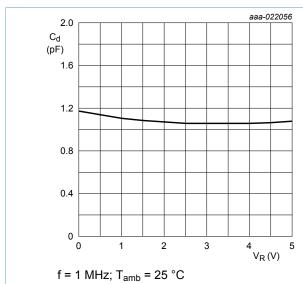
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8. Characteristics

Table 6. Characteristics

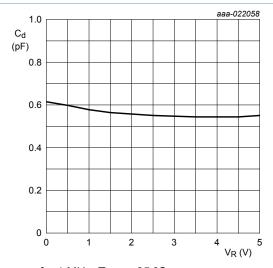
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per diode			'				_
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	5.5	V
V_{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C	[1]	6	-	9	V
V _F	forward voltage	I _F = 1 mA; T _{amb} = 25 °C	[2]	-	0.7	-	V
I _R	reverse current	V _R = 3 V; T _{amb} = 25 °C	[3]	-	1	100	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[2]	-	1	1.5	pF
			[4]	-	0.6	-	pF
			[1]	-	16	-	pF

- [1] Measured from pin 4 to ground.
- [2] Measured from pin 2 and 3 to ground.
- [3] Measured from pin 2, 3 and 4 to ground.
- [4] Measured from pin 2 to pin 3.



Measured from pin 2 and 3 to ground.

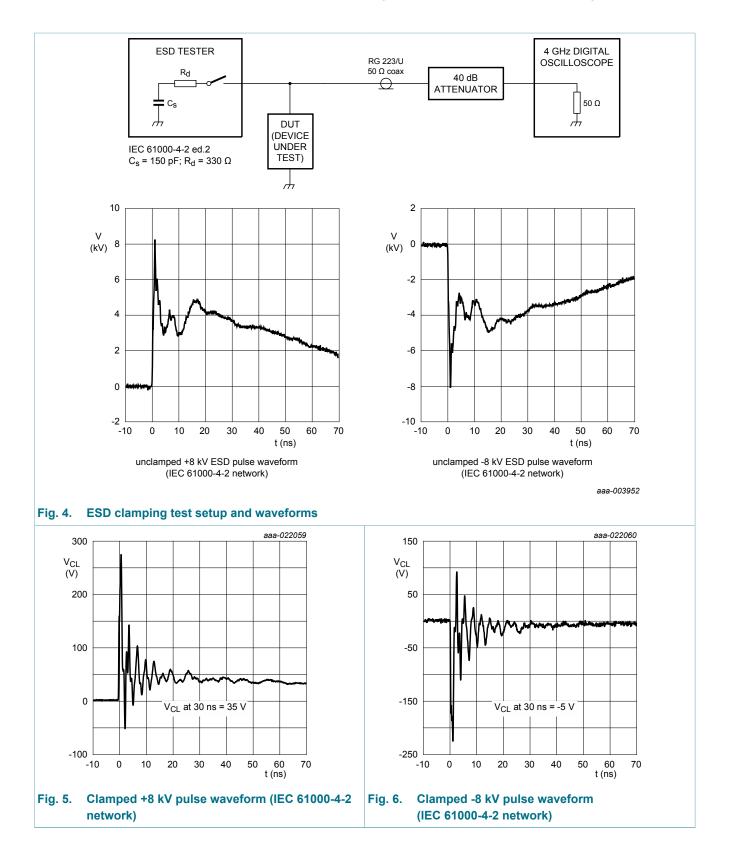
Fig. 2. Diode capacitance as a function of reverse voltage; typical values



f = 1 MHz; T_{amb} = 25 °C Measured from pin 2 to pin 3.

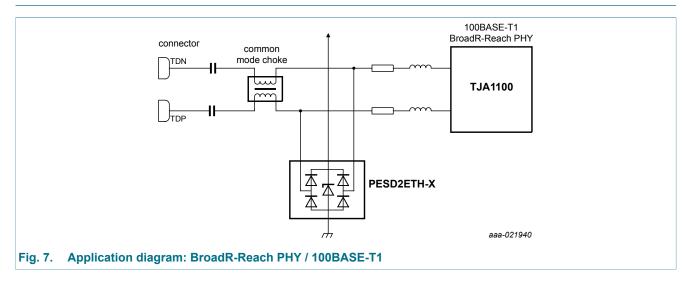
Fig. 3. Diode capacitance as a function of reverse voltage; typical values

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9. Application information



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

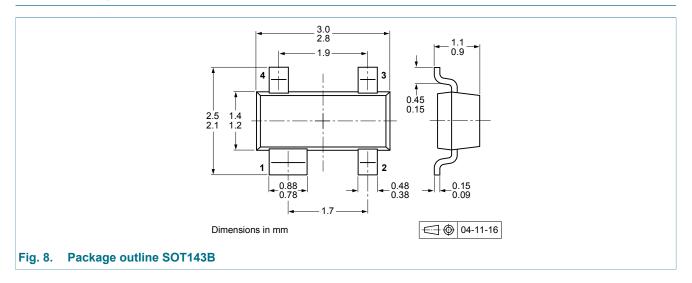
10. Test information

10.1 Quality information

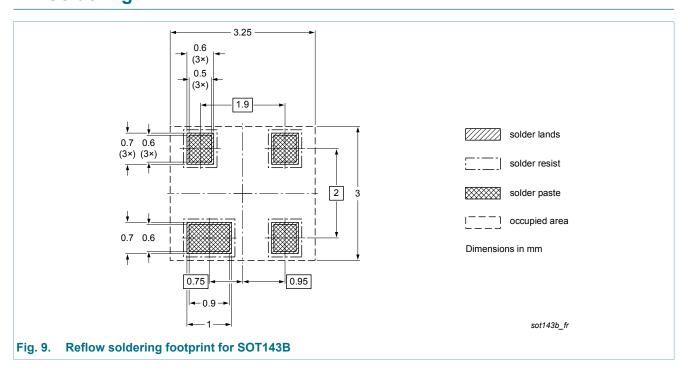
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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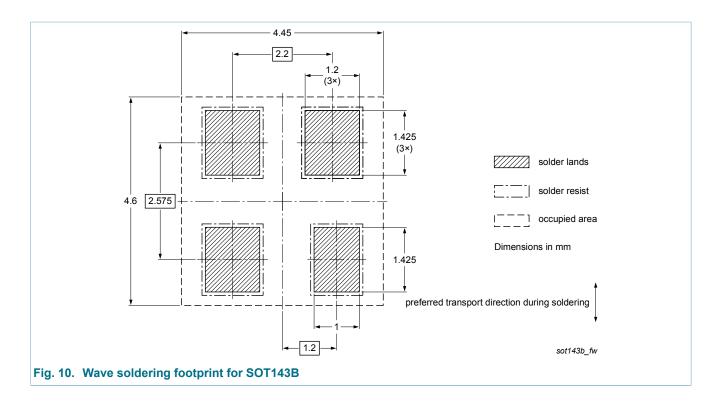
11. Package outline



12. Soldering



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13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD2ETH-X v.1	20160224	Product data sheet	-	-

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14. Legal information

14.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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