

RS1E240BN

Nch 30V 40A Middle Power MOSFET

V _{DSS}	30V
R _{DS(on)} (Max.)	3.2mΩ
Ι _D	±40A
P _D	30W

Features

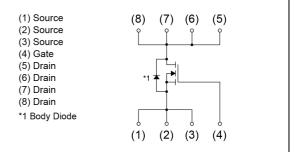
- 1) Low on resistance.
- 2) High Power small mold Package (HSOP8).
- 3) Pb-free lead plating ; RoHS compliant.
- 4) Halogen Free.

Application

Switching

●Outline	
HSOP8	

●Inner circuit



Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	RS1E240BN

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Paramete	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	30	V	
Continuous ducin cumont	$T_c = 25^{\circ}C$	I _D *1	±40	А
Continuous drain current	$T_a = 25^{\circ}C$	I _D	±24	А
Pulsed drain current	I _{DP} *2	±96	А	
Gate - Source voltage	V _{GSS}	±20	V	
Avalanche current, single pulse	I _{AS} *3	40	А	
Avalanche energy, single pulse	E _{AS} *3	140	mJ	
Power dissipation		P _D ^{*1}	30	W
		P _D ^{*4}	3	W
Junction temperature	Tj	150	°C	
Operating junction and storage t	T _{stg}	-55 to +150	°C	

Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	4.2	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*4}	-	-	41.7	°C/W

• Electrical characteristics ($T_a = 25^{\circ}C$)

Deverseter	Currente e l	Canditiana	Values			1.1:4	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1mA$ referenced to 25°C		21	-	mV/°C	
Zero gate voltage drain current	I_{DSS} V_{DS} = 30V, V_{GS} = 0V		-	-	1	μA	
Gate - Source leakage current	I _{GSS}	I_{GSS} $V_{GS} = \pm 16V, V_{DS} = 0V$		-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	V _{DS} = 10V, I _D = 1mA	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	• <u> </u>		-	-3	-	mV/°C	
Static drain - source	D *4	V _{GS} = 10V, I _D = 24A	-	2.3	3.2		
on - state resistance	$R_{DS(on)}^{*4}$	V _{GS} = 4.5V, I _D = 24A	-	3.3	4.6	mΩ	
Gate resistance	R _G	R _G f=1MHz, open drain		1.2	-	Ω	
Forward Transfer Admittance	Y _{fs} ^{*4}			-	-	S	

*1 Tc=25°C, Limited only by maximum temperature allowed.

*2 Pw \leq 10µs, Duty cycle \leq 1%

*3 L \simeq 0.05mH, V_{DD} = 24V, R_G = 25\Omega, STARTING T_j = 25 $^{\circ}$ C Fig.3-1,3-2

- *4 Mounted on a Cu Board (40×40×0.8mm)
- *5 Pulsed





• Electrical characteristics ($T_a = 25^{\circ}C$)

Parameter	Sumbol	Conditions	Values			Unit	
	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	3900	-		
Output capacitance	C _{oss}	V _{DS} = 15V	-	450	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	350	-		
Turn - on delay time	$t_{d(on)}^{*4}$	$V_{DD} \simeq 15V, V_{GS} = 10V$	-	25	-		
Rise time	t _r *4	I _D = 12A	-	70	-	-	
Turn - off delay time	$t_{d(off)}$ *4	R _L ≃ 1.25Ω	-	115	_	ns	
Fall time	t _f *4	R _G = 10Ω	-	26	-		

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	Conditions		Values			Unit
Parameter	Symbol			Min.	Тур.	Max.	Onit
Tatal gata abarga	O *4		V _{GS} = 10V	-	70	-	
Total gate charge	Q _g *4	V _{DD} ≃ 15V		-	35	-	~
Gate - Source charge	Q _{gs} *4	I _D = 24A	V _{GS} = 4.5V	-	10	-	nC
Gate - Drain charge	Q _{gd} ^{*4}			-	14	-	

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Deremeter	Sumbol	Conditions	Values			L lucit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I _S	$T = 25^{\circ}$	-	-	2.5	А
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	96	А
Forward voltage	V _{SD} ^{*4}	V _{GS} = 0V, I _S = 2.5A	-	-	1.2	V





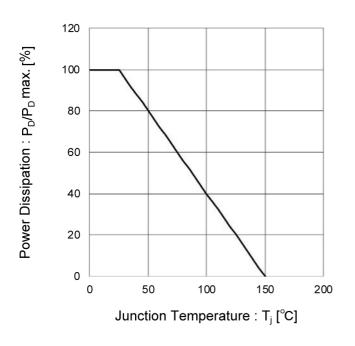
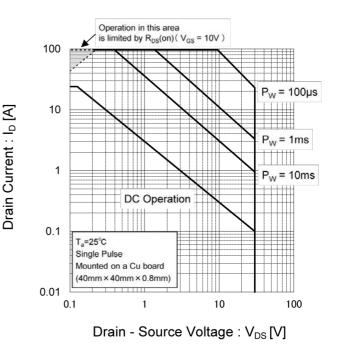


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

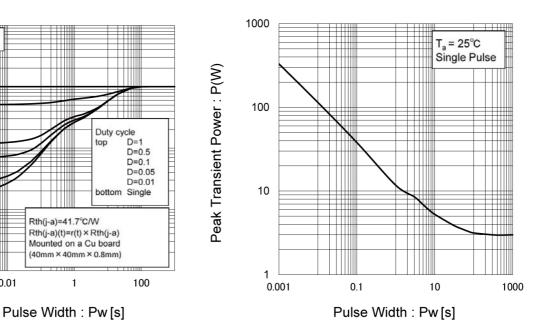


Rth(j-a)=41.7°C/W $Rth(j-a)(t)=r(t) \times Rth(j-a)$ Mounted on a Cu board (40mm × 40mm × 0.8mm)

1

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power dissipation



0.01

Normalized Transient Thermal Resistance : $r_{\scriptscriptstyle (t)}$

10

1

0.1

0.01

0.001

0.0001

T_a=25°C



Fig.5 Typical Output Characteristics(I)

Drain Current : I_D [A]

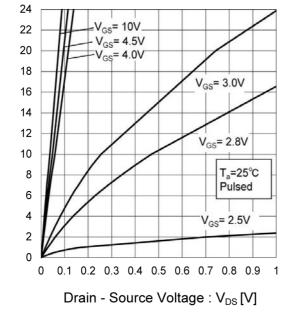


Fig.7 Breakdown Voltage vs. Junction Temperature

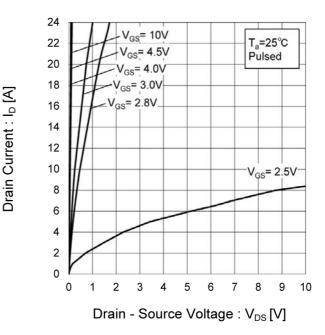
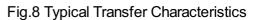
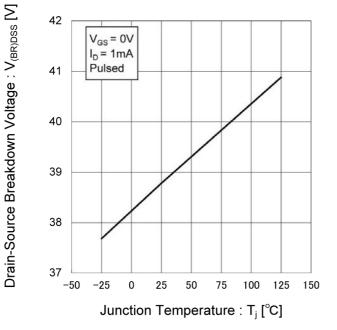
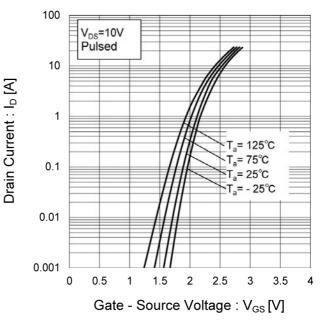


Fig.6 Typical Output Characteristics(II)









• Electrical characteristic curves

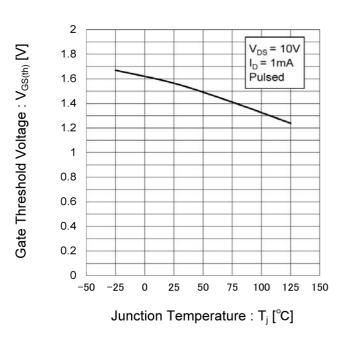


Fig.9 Gate Threshold Voltage vs. Junction Temperature



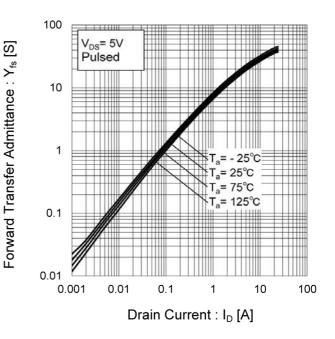
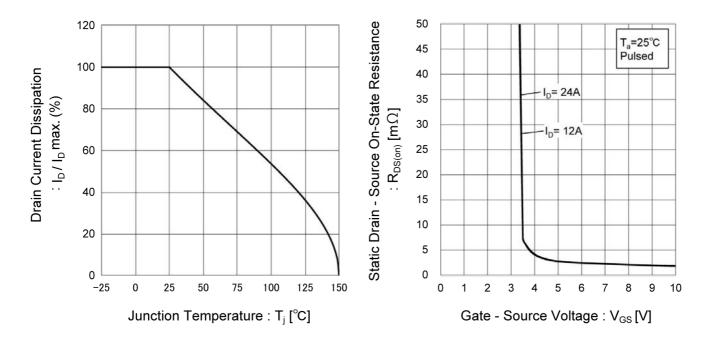


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





• Electrical characteristic curves

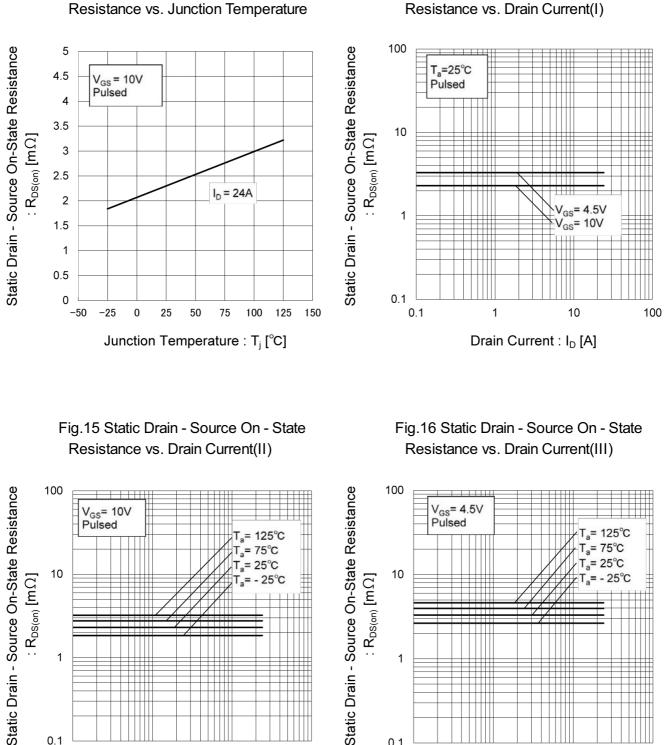


Fig.13 Static Drain - Source On - State Fig.14 Static Drain - Source On - State

1

0.1

0.1

100

10

Drain Current : I_D [A]

0.1

0.1



1

Drain Current : I_D [A]

100

10

• Electrical characteristic curves

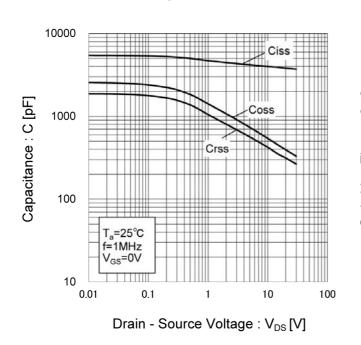


Fig.17 Typical Capacitance vs. Drain -Source Voltage

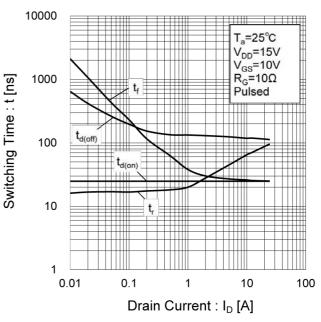


Fig.18 Switching Characteristics

Fig.19 Dynamic Input Characteristics

Gate - Source Voltage : V_{GS} [V]

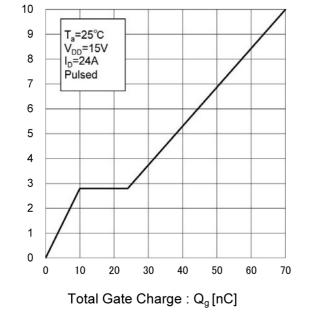
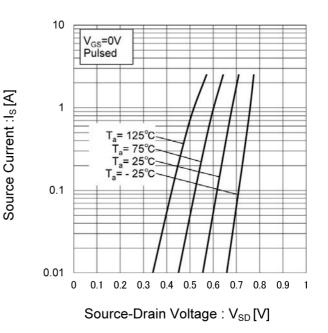


Fig.20 Source Current vs. Source Drain Voltage





Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

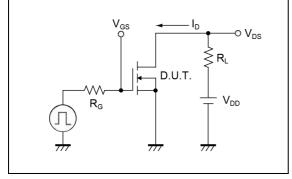


Fig.2-1 Gate Charge Measurement Circuit

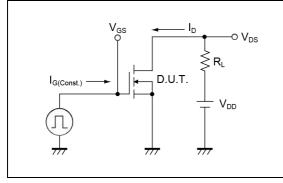


Fig.3-1 Avalanche Measurement Circuit

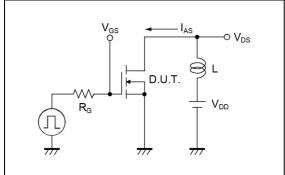


Fig.1-2 Switching Waveforms

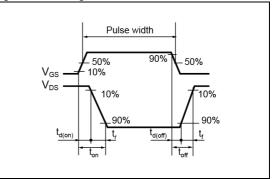


Fig.2-2 Gate Charge Waveform

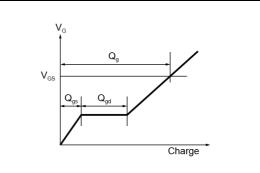
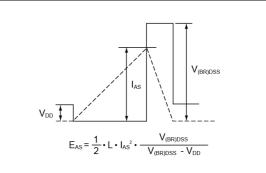


Fig.3-2 Avalanche Waveform

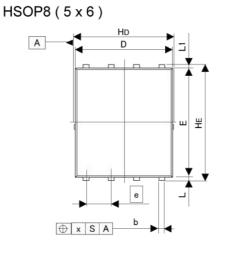


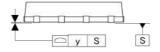
Notice

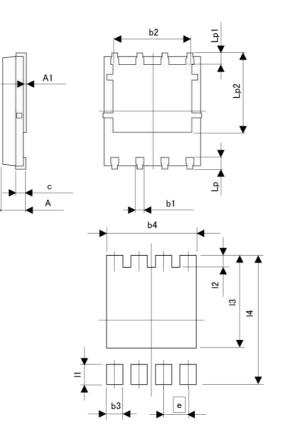
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions







Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIME	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.24	0.42	0.009	0.017
b1	0.29	0.49	0.011	0.019
b2	3.81	4.21	0.150	0.166
С	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.60	5.80	0.220	0.228
e	1.	27	0.0)50
HD	4.90	5.10	0.193	0.201
HE	5.90	6.10	0.232	0.240
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.50	0.70	0.020	0.028
Lp1	0.52	0.72	0.020	0.028
Lp2	3.92	4.32	0.154	0.170
x	2	0.10	-	0.004
у	-	0.10		0.004
	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b3	15	0.59	25	0.023
b4	4	4.21	1.41	0.166
11	2	0.80	.) (<u>a</u> :	0.031
12	-	0.82		0.032
13		4.32	0.000	0.170
14	2	6.10	1.00	0.240

Dimension in mm/inches



Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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