

### 1 Product profile

### 1.1 General description

Two planar PIN diodes in series configuration in a SOT23 small plastic SMD package.

#### 1.2 Features and benefits

- · High voltage, current controlled
- RF resistor for RF attenuators and switches
- · Low diode capacitance
- · Low diode forward resistance
- · Low series inductance
- For applications up to 3 GHz
- AEC-Q101 qualified

### 1.3 Applications

· RF attenuators and switches

## 2 Pinning information

Table 1. Discrete pinning

Pin	Description	Simplified outline	Symbol
1	anode		
2	cathode	$\square^3$	3
3	common connection	1 2	1 2 sym135
		top view	

# 3 Ordering information

**Table 2. Ordering information** 

Type number	Package						
	Name	Description	Version				
BAP64-04	-	plastic surface-mounted package; 3 leads	SOT23				



Silicon PIN diode

### 4 Marking

Table 3. Marking

Table of marking		
Type number	Marking	Description
BAP64-04	4K*	* = t : made in Malaysia
		* = W : made in China

# 5 Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Values are specified per diode.

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	reverse voltage		-	175	V
I <sub>F</sub>	forward current		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>sp</sub> = 90 °C	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-65	+150	°C

### 6 Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		220	K/W

### 7 Characteristics

#### **Table 6. Characteristics**

Values are specified per diode;  $T_i$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 50 mA		-	0.95	1.1	V
I <sub>R</sub> re	reverse current	V <sub>R</sub> = 60 V		-	-	10	μΑ
		V <sub>R</sub> = 20 V		-	-	1	μA
C <sub>d</sub> diode capacitance	diode capacitance	see Figure 1; f = 1 MHz;					
		V <sub>R</sub> = 0 V		-	0.52	-	pF
		V <sub>R</sub> = 1 V		-	0.37	-	pF
		V <sub>R</sub> = 20 V		-	0.23	0.35	pF
r <sub>D</sub>	diode forward resistance	see Figure 2; f = 100 MHz;	[1]				
		I <sub>F</sub> = 0.5 mA		-	20	40	Ω
		I <sub>F</sub> = 1 mA		-	10	20	Ω
		I <sub>F</sub> = 10 mA		-	2.0	3.8	Ω

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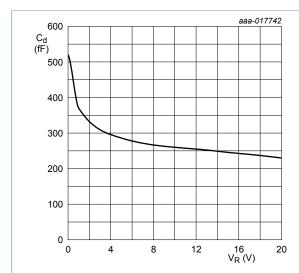
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		I <sub>F</sub> = 100 mA	-	0.7	1.35	Ω
т∟	charge carrier life time	when switched from I <sub>F</sub> = 10 mA to I <sub>R</sub> = 6 mA; R <sub>L</sub> = 100 $\Omega$ ; measured at I <sub>R</sub> = 3 mA	-	1.55	-	μs
L <sub>S</sub>	series inductance		-	1.4	-	nH

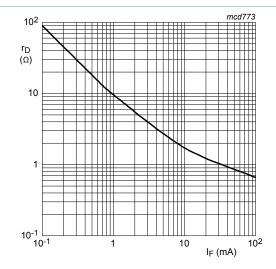
[1] Guaranteed on AQL basis: inspection level S4, AQL 1.0.

### 7.1 Graphical data



 $f = 1 MHz; T_i = 25 °C.$ 

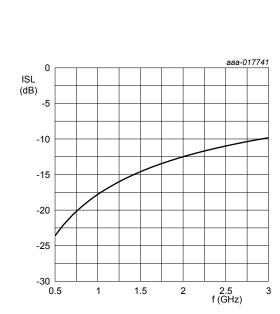
Figure 1. Diode capacitance as a function of reverse voltage; typical values



 $f = 100 \text{ MHz}; T_i = 25 ^{\circ}\text{C}.$ 

Figure 2. Forward resistance as a function of forward current; typical values

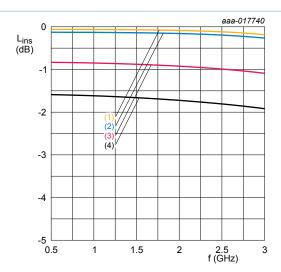
Silicon PIN diode



 $T_{amb} = 25 \, ^{\circ}C$ 

Diode zero biased and inserted in series with a 50  $\Omega$  stripline circuit

Figure 3. Isolation of the diode as a function of frequency; typical values



T<sub>amb</sub> = 25 °C

1. I<sub>F</sub> = 100 mA

2.  $I_F = 10 \text{ mA}$ 

3.  $I_F = 1 \text{ mA}$ 

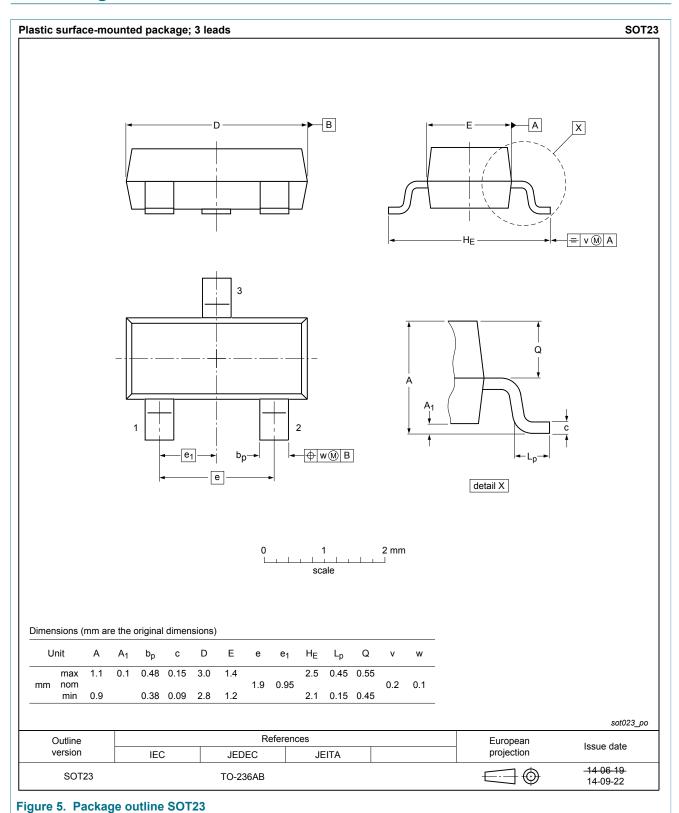
4.  $I_F = 0.5 \text{ mA}$ 

Diode inserted in series with a 50  $\Omega$  stripline circuit and biased via the analyzer Tee network

Figure 4. Insertion loss of the diode as a function of frequency; typical values

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# 8 Package outline



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### 9 Abbreviations

Table 7. Abbreviations

Acronym	Description
AQL	acceptable quality level
PIN	P-type, intrinsic, N-type
SMD	surface mounted device
S4	special inspection level 4

# 10 Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BAP64-04 v.6	20190311	Product data sheet	-	BAP64-04 v.5
Modifications:	• changed V <sub>R</sub> con	dition of I <sub>R</sub> from 175 V to 60 V		'
BAP64-04 v.5	20150428	Product data sheet	-	BAP64-04 v.4
Modifications:	of NXP Semicon	been adapted to the new comp	. ,	, ,
BAP64-04 v.4 (9397 750 06424)	19990921	Product specification	-	BAP64-04 v.3
BAP64-04 v.3 (9397 750 06282)	19990827	Product specification	-	BAP64-04_N v.2
BAP64-04_N v.2 (9397 750 06088)	19990616	Preliminary specification	-	BAP64-04 v.1
BAP64-04 v.1 (9397 750 05559)	19990510	Objective specification	-	-

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### 11 Legal information

#### 11.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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