

### Is Now Part of



# ON Semiconductor®

# To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">guestions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



February 2008

# 74LVX125 Low Voltage Quad Buffer with 3-STATE Outputs

#### **Features**

- Input voltage level translation from 5V to 3V
- Ideal for low power/low noise 3.3V applications
- Guaranteed simultaneous switching noise level and dynamic threshold performance

### **General Description**

The LVX125 contains four independent non-inverting buffers with 3-STATE outputs. The inputs tolerate voltages up to 7V allowing the interface of 5V systems to 3V systems.

### **Ordering Information**

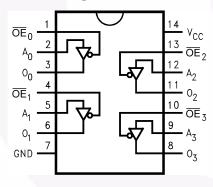
Order Number	Package Number	Package Description
74LVX125M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
74LVX125SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74LVX125MTC	MTC14	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.

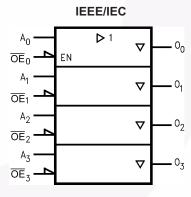


All packages are lead free per JEDEC: J-STD-020B standard.

# **Connection Diagram**



### **Logic Symbol**



## **Pin Description**

Pin Names	Description
A <sub>n</sub>	Inputs
<del>OE</del> <sub>n</sub>	Output Enable Inputs
O <sub>n</sub>	Outputs

### **Truth Table**

Inp	Output			
ŌĒn	OE <sub>n</sub> A <sub>n</sub>			
L	L	L		
L	Н	Н		
Н	Х	Z		

H = HIGH Voltage Level

L = LOW Voltage Level

Z = High Impedance

X = Immaterial

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	-0.5V to +7.0V
I <sub>IK</sub>	DC Input Diode Current, V <sub>I</sub> = -0.5V	–20mA
V <sub>I</sub>	DC Input Voltage	-0.5V to 7V
I <sub>OK</sub>	DC Output Diode Current	
	$V_{O} = -0.5V$	–20mA
	$V_O = V_{CC} + 0.5V$	+20mA
V <sub>O</sub>	DC Output Voltage	-0.5V to V <sub>CC</sub> + 0.5V
I <sub>O</sub>	DC Output Source or Sink Current	±25mA
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current	±50mA
T <sub>STG</sub>	Storage Temperature	–65°C to +150°C
Р	Power Dissipation	180mW

# Recommended Operating Conditions<sup>(1)</sup>

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	2.0V to 3.6V
V <sub>I</sub>	Input Voltage	0V to 5.5V
Vo	Output Voltage	0V to V <sub>CC</sub>
T <sub>A</sub>	Operating Temperature	–40°C to +85°C
Δt / ΔV	Input Rise and Fall Time	0ns/V to 100ns/V

### Note:

1. Unused inputs must be held HIGH or LOW. They may not float.

### **DC Electrical Characteristics**

				T <sub>A</sub> = +25°C		$T_A = +25^{\circ}C$ $T_A = -40^{\circ}C$ $+85^{\circ}C$			
Symbol	Parameter	V <sub>CC</sub>	Conditions	Min.	Тур.	Max.	Min.	Max.	Units
V <sub>IH</sub>	HIGH Level Input	2.0		1.5			1.5		V
	Voltage	3.0		2.0			2.0		
		3.6		2.4			2.4		
V <sub>IL</sub>	LOW Level Input	2.0				0.5		0.5	V
	Voltage	3.0				0.8		0.8	
		3.6				0.8		0.8	
V <sub>OH</sub>	HIGH Level Output Voltage	2.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OH} = -50\mu\text{A}$	1.9	2.0		1.9		V
		3.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OH} = -50\mu\text{A}$	2.9	3.0		2.9		
			$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OH} = -4\text{mA}$	2.58			2.48		
V <sub>OL</sub>	LOW Level Output Voltage	2.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OL} = 50 \mu A$		0.0	0.1		0.1	V
		3.0	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OL} = 50 \mu A$		0.0	0.1		0.1	
			$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OL} = 4\text{mA}$			0.36		0.44	
I <sub>OZ</sub>	3-STATE Output Off-State Current	3.6	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $V_{OUT} = V_{CC} \text{ or GND}$			±0.25		±2.5	μA
I <sub>IN</sub>	Input Leakage Current	3.6	V <sub>IN</sub> = 5.5V or GND			±0.1		±1.0	μA
I <sub>CC</sub>	Quiescent Supply Current	3.6	$V_{IN} = V_{CC}$ or GND			4.0		40.0	μΑ

# Noise Characteristics<sup>(2)</sup>

				$T_A = 25^{\circ}C$		
Symbol	Parameter	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Тур.	Limit	Units
V <sub>OLP</sub>	Quiet Output Maximum Dynamic V <sub>OL</sub>	3.3	50	0.3	0.8	V
V <sub>OLV</sub>	Quiet Output Minimum Dynamic V <sub>OL</sub>	3.3	50	-0.3	-0.8	V
V <sub>IHD</sub>	Minimum HIGH Level Dynamic Input Voltage	3.3	50		2.0	V
V <sub>ILD</sub>	Maximum LOW Level Dynamic Input Voltage	3.3	50		0.8	V

### Note:

2. Input  $t_r = t_f = 3ns$ 

### **AC Electrical Characteristics**

				T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C			
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions	Min.	Тур.	Max.	Min.	Max.	Units
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay	2.7	C <sub>L</sub> = 15pF		5.8	10.1	1.0	13.5	ns
	Time, Data to Output		$C_L = 50pF$		8.3	13.6	1.0	17.0	
	Output	$3.3 \pm 0.3$	C <sub>L</sub> = 15pF		4.4	6.2	1.0	8.5	
			$C_L = 50pF$		6.9	9.7	1.0	12.0	
t <sub>PZH</sub> , t <sub>PZL</sub>	Output Enable Time	2.7	$C_L = 15pF, R_L = 1k\Omega$		5.3	9.3	1.0	12.5	ns
			$C_L = 50 pF, R_L = 1 k\Omega$		7.8	12.8	1.0	16.0	
		$3.3 \pm 0.3$	$C_L = 15pF, R_L = 1k\Omega$		4.0	5.6	1.0	7.5	
			$C_L = 50 pF, R_L = 1 k\Omega$		6.5	9.1	1.0	11.0	
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Output Disable	2.7	$C_L = 50 pF, R_L = 1 k\Omega$		10.0	15.7	1.0	19.0	ns
	Time	3.3 ± 0.3	$C_L = 50 pF, R_L = 1 k\Omega$		8.3	11.2	1.0	13.0	
toshl, toshh	Output to Output	2.7	$C_L = 50pF$			1.5		1.5	ns
	Skew <sup>(3)</sup>	3.3				1.5		1.5	

#### Note

3. Parameter guaranteed by design  $t_{OSLH} = |t_{PLHm} - t_{PLHn}|$ ,  $t_{OSHL} = |t_{PHLm} - t_{PHLn}|$ 

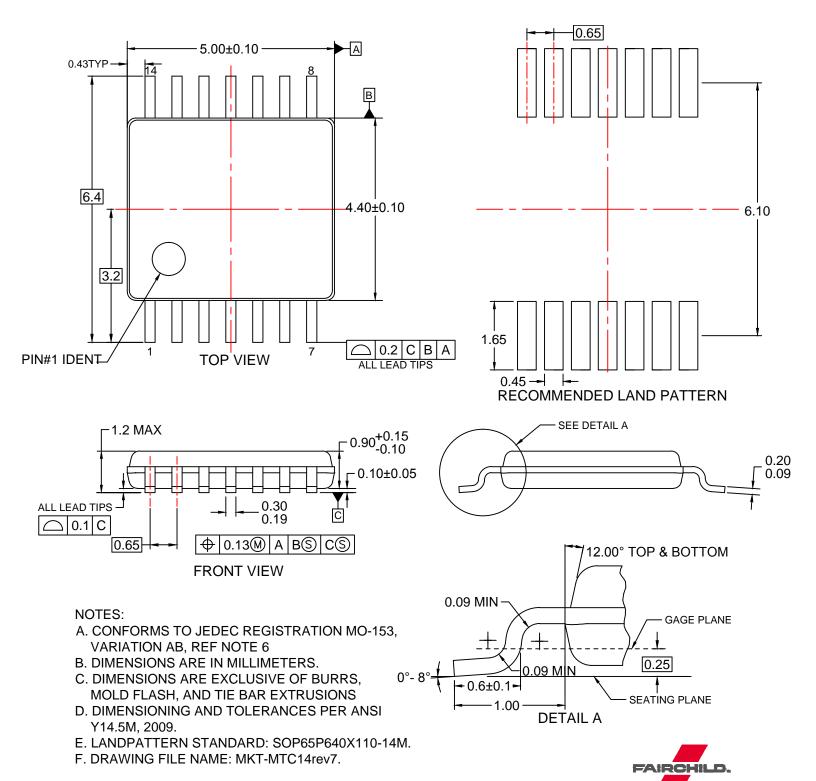
# Capacitance

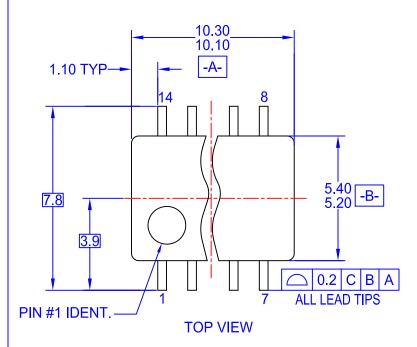
		T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C			
Symbol	Parameter	Min.	Тур.	Max.	Min.	Max.	Units
C <sub>IN</sub>	Input Capacitance		4	10		10	pF
C <sub>PD</sub>	Power Dissipation Capacitance <sup>(4)</sup>		14				pF

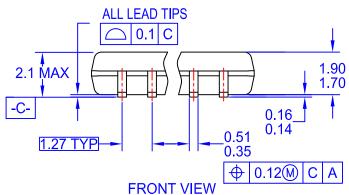
#### Note:

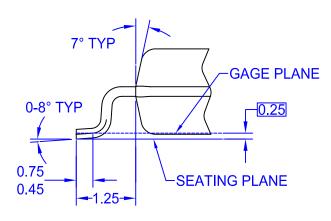
4. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:  $I_{CC(opr.)} = \frac{C_{PD} \times V_{CC} \times f_{IN} \times I_{CC}}{4 \text{ (per bit)}}$ 

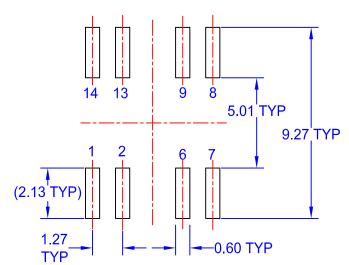




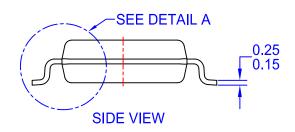




**DETAIL A** 



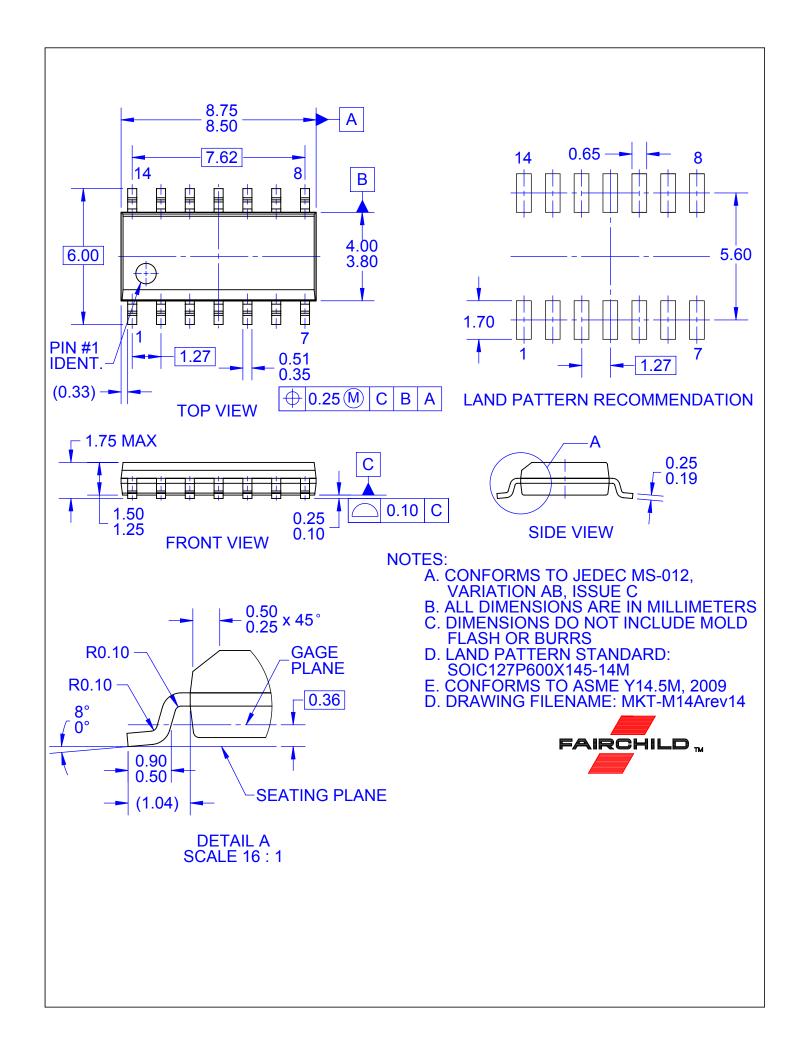
LAND PATTERN RECOMMENDATION



### NOTES:

- A. CONFORMS TO EIAJ EDR-7320 REGISTRATION, ESTABLISHED IN DECEMBER, 1998.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D. DRAWING FILENAME: MKT-M14Drev4.





ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdt/Patent-Marking.pdf">www.onsemi.com/site/pdt/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

# **ON Semiconductor:**

<u>74LVX125SJX</u> <u>74LVX125MTC</u> <u>74LVX125M</u> <u>74LVX125MTCX</u> <u>74LVX125MX</u> <u>74LVX125SJ</u> <u>74LVX125SJ</u> <u>74LVX125MTCX\_NL</u> 74LVX125M\_Q 74LVX125MTC\_Q



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

### Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов:
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001:
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

**Телефон:** 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.