R1240x Series

1.2 A, 30 V Step-Down DC/DC Converter

NO.EA-190-151211

OUTLINE

The R1240x is a CMOS-based Step-down DC/DC converter with internal Nch high side Tr. (0.35 Ω), which can provide the maximum 1.2 A output current. The ICs consists of an Oscillator, a PWM control circuit, a Reference Voltage unit, an Error amplifier, phase compensation circuits, a slope circuit, a soft-start circuit, protection circuits, internal voltage regulators, and a switch for boot strap circuit. The ICs can make up a Step-Down DC/DC Converter with the following external components: an inductor, resistors, a diode, and capacitors. The R1240x is a current mode operating type DC/DC converter which does not require external current sense resistor, and it works high speed response time, high efficiency and compatible with ceramic capacitors. Oscillator frequency is internally set at 1.25 MHz.

As a protection function, it has cycle by cycle peak current limit function, short protection function, thermal shutdown function and UVLO.

There are two types for short protection, A version has latch protection function with 2 ms delay time, and B version has fold-back protection function that keep operating at short condition with lower operating frequency and limiting the Lx current.

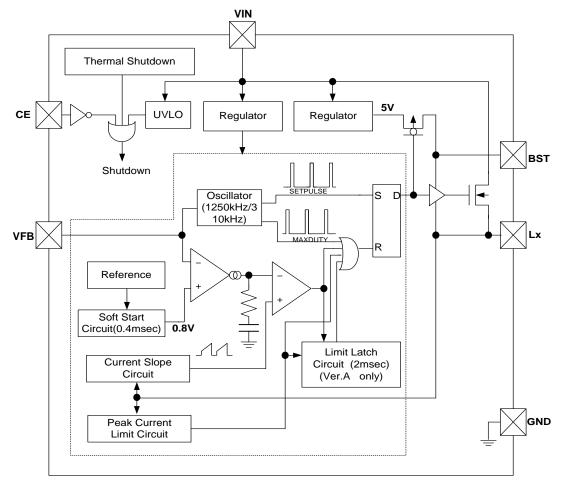
FEATURES

•	Operating Voltage ·······4.5 V to 30 V
•	Internal Nch MOSFET DriverTyp. $Ron = 0.35 \Omega$
•	Adjustable Output Voltage with External Resistor ··· 0.8 V to 15 V
•	Feedback Voltage······0.8 V ±1.5%
•	Peak Current Limit FunctionTyp. 2.0 A
•	UVLO Function
•	Operating Frequency ············1.25 MHz (Ver. B: 310 kHz, Fold-back Condition)
•	Short Protection for Output ······Ver. A: Latch with 2 ms delay or Ver. B: Fold-back
•	Ceramic Capacitor Compatible
•	Stand-by Function ·····Typ. 0 μΑ
•	Package SOT-23-6W, DFN(PLP)2527-10

APPLICATIONS

- Digital Home Appliances: Digital TVs, DVD Players
- OA Equipment: Printers, Fax
- Hand-held Communication Equipment, Cameras, VCRs, Camcorders
- **Battery-powered Equipment**

BLOCK DIAGRAM



R1240x Block Diagram

SELECTION GUIDE

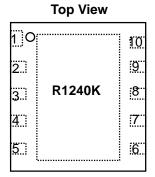
In the R1240x, the Package, type of short protection (Latch or Fold-back) can be selected at the user's request.

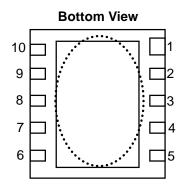
Selection Guide

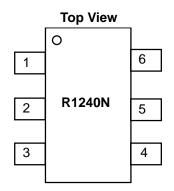
Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1240K003*-TR	DFN(PLP)2527-10	5,000 pcs	Yes	Yes
R1240N001*-TR-FE	SOT-23-6W	3,000 pcs	Yes	Yes

- *: Designation of Optional Function at off state are options as follows.
 - (A) Latch Type protection
 - (B) Fold-back Type protection

PIN DESCRIPTIONS







DFN(PLP)2527-10 Pin Configuration

SOT-23-6W Pin Configuration

R1240N001x Pin Description

Pin No.	Symbol	Description		
1	CE	Chip Enable Pin, Active with "H"		
2	VIN	Power Supply Pin		
3	Lx	Lx Switching Pin		
4	BST	Bootstrap Pin		
5	GND	Ground Pin		
6	VFB	Feedback Pin		

R1240K003x Pin Description

Pin No.	Symbol	Description
1	Lx	Lx Switching Pin
2	VIN	Power Supply Pin
3	VIN	Power Supply Pin
4	CE	Chip Enable Pin, Active with "H"
5	TEST	Test Pin (Open, do not connect to any line.)
6	GND	Ground Pin
7	NC	No Connection
8	VFB	Feedback Pin
9	NC	No Connection
10	BST	Bootstrap Pin

Tab is GND level. (They are connected to the reverse side of this IC.) The tab is better to be connected to the GND, but leaving it open is also acceptable.

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

(GND = 0 V)

Symbol	Item		Rating		Unit
VIN	Input Voltage	-0.3 to 32		V	
V _{BST}	BST Pin Voltage		V_{LX} –0.3 to V_{LX}	+6	V
V _L X	Lx Pin Voltage		−0.3 to V _{IN} +0.	3	V
I _{LX}	Lx Pin Current		2		Α
V _{CE}	CE Pin input Voltage		-0.3 to V _{IN} + 0.3		V
V _{FB}	VFB Pin Voltage		-0.3 to 4		V
		SOT-23-6W	Standard Land Pattern	430	
P _D	Power Dissipation*		Standard Land Pattern	910	mW
	·	DFN(PLP)2527-10	High Wattage Land Pattern	1400	
Та	Operating Temperature Ra	nge	-40 to 85		°C
Tstg	Storage Temperature Rang	je	-55 to 125		°C

^{*} For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.

The functional operation at or over these absolute maximum ratings is not assured.

ELECTRICAL CHARACTERISTICS

Electrical Characteristics

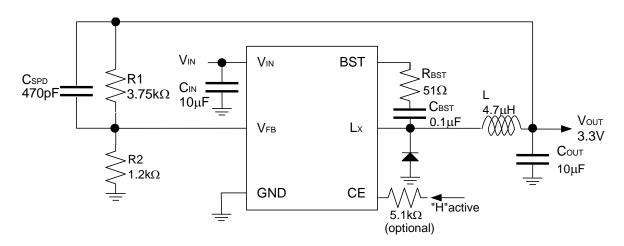
(Otherwise notified, $V_{IN} = 12 \text{ V}$, $Ta = 25^{\circ}\text{C}$)

Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
V _{IN}	Operating Input Voltage		4.5		30	V
I _{IN}	VIN Consumption Current	V _{IN} = 30 V, V _{FB} = 1.0 V		0.5	1.0	mA
V _{UVLO1}	UVLO Detect Voltage	Falling	3.6	3.8	4.0	V
V _{UVLO2}	UVLO Released Voltage	Rising		V _{UVLO1} +0.2	4.2	V
V_{FB}	VFB Voltage Tolerance		0.788	0.800	0.812	V
ΔV _{FB} /ΔTa	VFB Voltage Temperature Coefficient	-40°C ≤ Ta ≤ 85°C		±150		ppm/ºC
fosc	Oscillator Frequency		1000	1250	1500	kHz
V_{FLB}	Fold-back Frequency (Ver. B)	V _{FB} < 0.56 V		310		kHz
Maxduty	Oscillator Max. Duty Cycle		75	85	90	%
tmin	Minimum On Time			100		nsec
tss	Soft-start Time	V _{FB} = 0.72 V	0.2	0.4	0.6	ms
tdly	Delay Time for Latch Protection (Ver. A)		1	2	4	ms
R _{LXH}	Lx High Side Switch ON Resistance			0.35		Ω
ILXHOFF	Lx High Side Switch Leakage Current			0	5	μA
I _{LIMLXH}	Lx High Side Switch Limited Current			2.0		А
Vcel	CE "L" Input Voltage				0.3	V
Vсен	CE "H" Input Voltage		1.6			V
I _{FB}	VFB Input Current		-1.0		1.0	μA
ICEL	CE "L" Input Current		-1.0		1.0	μΑ
ICEH	CE "H" Input Current		-1.0		1.0	μΑ
T _{TSD}	Thermal Shutdown Detect Temperature	Hysteresis 30°C		160		°C
Istandby	Standby Current	V _{IN} = 30 V		0	5	μA

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

TYPICAL APPLICATION



R1240x Typical Application

External Parts

Cin	10 μF, KTS500B106M55N0T00 (Nippon Chemi-Con)			
Соит	Соυт 10 μF, GRM31CR71E106K (Murata)			
C _{BST}	0.1 μF, GRM21BB11H104KA01L (Murata)			
L	4.7 μH, SLF7045T-4R7M2R0-PF (TDK)			
D	MA24D60 (Panasonic)			

TECHNICAL NOTES

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed its voltage, current or power ratings. When designing a peripheral circuit, please be fully aware of the following points

- External components must be connected as close as possible to the ICs and make wiring as short as possible. Especially, the capacitor connected in between VIN and GND pin must be wiring the shortest. The operating may be unstable due to the change of the electric potential of internal ICs by the switching current when the impedance of the power supply line and GND line is high. Make the power supply and GND lines sufficient. It is also necessary to give careful consideration to design the wiring of the power supply, GND, Lx, VOUT and the inductor because of the large current by the function of switching is flowing into them. Besides, the wiring between the resistance (R1), which set the output voltage, and the wiring of the inductor must separate from the load wiring.
- The ceramic capacitors have low ESR (Equivalent Series Resistance) type are recommended for the ICs. The recommendation of C_{IN} capacitor between VIN and GND is more than 10 μ F, and C_{OUT} capacitor is more than 10 μ F in the case $V_{OUT} \ge 1.8$ V or more than 20 μ F in the case 1.8 V > V_{OUT} . Please check the bias dependence and the temperature variations of the ceramic capacitors.
- Normally, please select the inductor value in the range between 4.7 μH and 10μH in the case of V_{OUT} ≥ 5 V, 4.7 μH in the case of 5 V > V_{OUT} ≥ 1.8 V and 2.2 μH in the case of 1.8 V > V_{OUT}. The internal phase compensation of this IC is designed with the above-mentioned inductor value and C_{OUT} ceramic capacitor value. When the inductor value is small, there is a possibility to trigger the over-current protection circuit by the peak switching current. As the peak switching current might reach to the limited value when the load current increase a lot.
- Please note; the over-current protection circuit is influenced by the temperature shift caused by operation of the IC.
- For the diode, please use the Schottky diode, which parasitic capacitance is small as possible, as, there is a possibility that the operating of IC becomes unstable by the large switching current.
- Output voltage is set by $V_{OUT} = V_{FB} \times (R1 + R2) / R2$. If the values of R1 and R2 are large, the impedance of VFB pin increases, and pickup the noise may result. The recommendation value range of R2 is approximately between 1.2 k Ω to 16 k Ω . If the operation may be unstable, reduce the impedance of VFB pin.

Recommended Value for Each Output Voltage

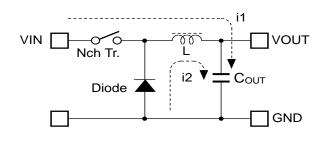
V _{OUT} (V)	0.8	1	1.2	1.3	1.5	1.8~6	6~15
R1 (kΩ)	0	= (V _{OUT} / 0.8 - 1) × 1.2					
R2 (kΩ)	open	1.20	1.20	1.20	1.20	1.20	1.20
C _{SPD} (pF)	open	3300	2200	1500	470	470	330
Соυт (μF)	22 × 2	10 × 2	10 × 2	10 × 2	10 × 2	10	10
L (μH)	2.2	2.2	2.2	2.2	2.2	4.7	10.0 (4.7)

Recommended External Components

Symbol	Condition	Value	Parts Name	MFR			
C _{IN}	Cin		UMK325BJ106MM-P	TAIYO YUDEN			
		10 μF/ 50 V	CGA6P3X7S1H106K	TDK			
		10 μF/ 50 V	KTS500B106M55N0T00	Nippon Chemi-Con			
Соит	V _{OUT} > 10 V	10 μF/ 50 V	UMK325BJ106MM-P	TAIYO YUDEN			
		10 μF/ 50 V	CGA6P3X7S1H106K	TDK			
		10 μF/ 50 V	KTS500B106M55N0T00	Nippon Chemi-Con			
	10 V > V _{OUT} > 1.8 V	10 μF/ 25 V	GRM31CR71E106K	Murata			
	V _{OUT} < 1.8 V	$22~\mu F/~10~V$	GRM31CR71A226M	Murata			
			NOTE: The value of C _{OUT}				
			depends on the setting output voltage.				
Свзт		0.1 μF/ 50 V	GRM21BB11H104KA01L	Murata			
R _{BST}		51.0 Ω					
L	40 V/ 2.0 A	10 μΗ	SLF6045T-100M1R6-3PF	TDK			
		4.7 μΗ	SLF7045T-4R7M2R0-PF	TDK			
		2.2 μΗ	VLCF4020T-2R2N1R7	TDK			
D	30 V/ 2.0 A	0.32 V	CMS06	TOSHIBA			
	40 V/ 2.0 A	0.49 V	CMS11	TOSHIBA			
	30 V/ 1.5 A	0.42 V	MA22D28	Panasonic			
	40 V/ 2.0 A	0.43 V	MA24D60	Panasonic			
			NOTE: Diode depends on the				
			input voltage and output				
Rce	Current. The diode is connected between the CE pin and the VIN pin as the ESD protection element.						
INCE		re is the possibility that the voltage of the CE pin becomes higher than the voltage					
	VIN pin, it is recomme	s recommended to connect the 5 kΩ resistance with the CE pin for preventing a					
	large current flows into the VIN pin from the CE pin.						

OPERATION OF STEP-DOWN DC/DC CONVERTER AND OUTPUT CURRENT

The step-down DC/DC converter charges energy in the inductor (L) when the LX transistor turns on, and discharges the energy from the inductor when LX transistor turns off and controls with less energy loss, so that a lower output voltage (V_{OUT}) than the input voltage (V_{IN}) can be obtained. The operation of the step-down DC/DC converter is explained in the following figures.



IL ILmax topen topen T=1/fosc

Basic Circuit

Inductor Current flowing through Inductor

- **Step1.** The Nch transistor turns on and the inductor current (i1) flows, L is charged with energy. At this moment, i1 increases from the minimum inductor current (ILmin), which is 0 A, and reaches the maximum inductor current (ILmax) in proportion to the on-time period (ton) of the Nch transistor.
- **Step2.** When the Nch transistor turns off, L tries to maintain IL at ILmax, so L turns the diode on and the inductor current (i2) flows into L.
- Step3. i2 decreases gradually and reaches ILmin after the open-time period (topen) of the Nch transistor, and then the diode turns off. This is called discontinuous current mode.
 As the output current (I_{OUT}) increases, the off-time period (toff) of the Nch transistor runs out before IL reaches ILmin. The next cycle starts, and the Nch transistor turns on and the diode turns off, which means IL starts increasing from ILmin. This is called continuous current mode.

In the case of PWM mode, V_{OUT} is maintained by controlling ton. During PWM mode, the oscillator frequency (fosc) is being maintained constant.

OUTPUT CURRENT AND SELECTION OF EXTERNAL COMPONENTS

The following equations explain the relationship between output current and peripheral components.

Ripple Current P-P value is described as I_{RP} , ON resistance of switch is described as R_{ONP} , forward drop voltage is described as V_F , and DC resistance of inductor is described as R_L .

First, when the switch is turned on, the following equation is satisfied.

 $V_{IN} = V_{OUT} + (R_{ONH} + R_L) \times I_{OUT} + L \times I_{RP} / ton$ Equation 1

Second, when the switch is turned off, the diode is turned on, the following equation is satisfied.

 $L \times I_{RP}$ / toff = V_F + V_{OUT} + $R_L \times I_{OUT}$ Equation 2

Put Equation 2 into Equation 1 to solve the ON duty of the switch (DoN = ton / (toff + ton)):

 $D_{ON} = \left(V_{OUT} + V_F + R_L \times I_{OUT}\right) / \left(V_{IN} + V_F - R_{ONH} \times I_{OUT}\right) \cdots Equation \ 3$

Ripple Current is described as follows:

 $I_{RP} = (V_{IN} - V_{OUT} - R_{ONH} \times I_{OUT} - R_{L} \times I_{OUT}) \times D_{ON} / fosc / L \dots Equation 4$

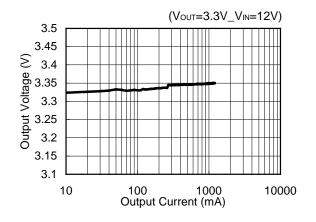
Peak current that flows through L and the switch is described as follows:

ILmax = Iout + IRP / 2 Equation 5

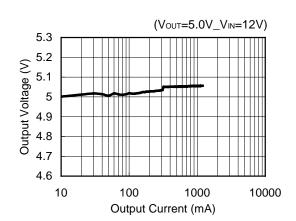
Notes: Please consider ILmax when setting conditions of input and output, as well as selecting the external components. The above calculation formulas are based on the ideal operation of the ICs in continuous mode.

TYPICAL CHARACTERISTICS

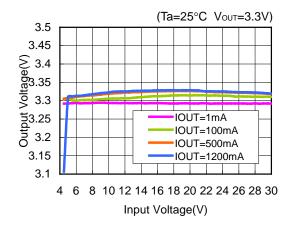
1) Output Voltage VS. Output Current R1240x00Xx



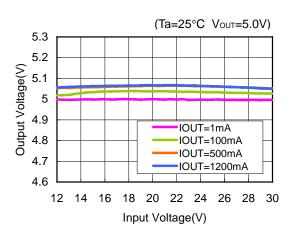
R1240x00Xx



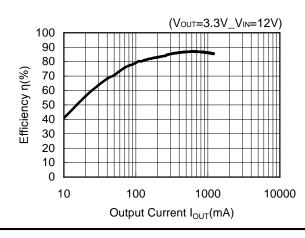
2) Output Voltage VS. Input Voltage R1240x00Xx



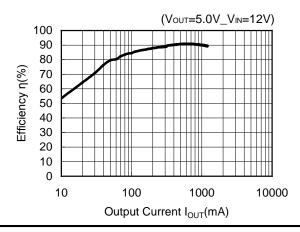
R1240x00Xx

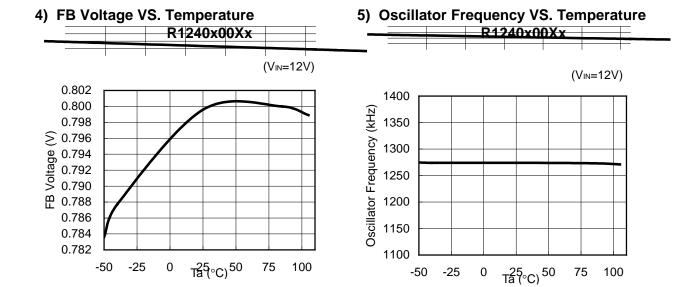


3) Efficiency VS. output Current R1240x00Xx

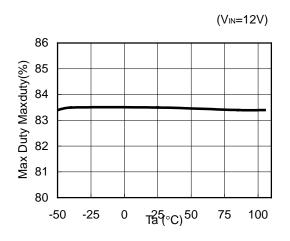


R1240x00Xx

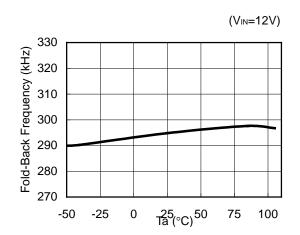




6) Maxduty VS. Temperature R1240x00Xx



7) Fold-Back Frequency VS. Temperature R1240x00XB





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