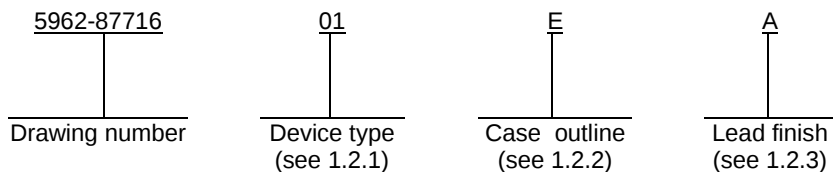


| REVISIONS                                                                                                                                                  |                                                                                      |  |  |                                   |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
|------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------|--|--|-----------------------------------|--|---|---|---|---|-------------------------------------------------------------------------------------------------------------------------------------|---------------------------|---|-------------------|---|-------------------|----|----|--|--|
| LTR                                                                                                                                                        | DESCRIPTION                                                                          |  |  |                                   |  |   |   |   |   |                                                                                                                                     | DATE (YR-MO-DA)           |   |                   |   | APPROVED          |    |    |  |  |
| A                                                                                                                                                          | Drawing updated to reflect current requirements. Editorial changes throughout. - drw |  |  |                                   |  |   |   |   |   |                                                                                                                                     | 00-12-13                  |   |                   |   | Raymond Monnin    |    |    |  |  |
| B                                                                                                                                                          | Redraw. Update drawing to current requirements. - drw                                |  |  |                                   |  |   |   |   |   |                                                                                                                                     | 11-06-08                  |   |                   |   | Charles F. Saffle |    |    |  |  |
|                                                                                                                                                            |                                                                                      |  |  |                                   |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
| THE ORIGINAL FIRST SHEET OF THIS DRAWING HAS BEEN REPLACED.                                                                                                |                                                                                      |  |  |                                   |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
| REV                                                                                                                                                        |                                                                                      |  |  |                                   |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
| SHEET                                                                                                                                                      |                                                                                      |  |  |                                   |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
| REV                                                                                                                                                        |                                                                                      |  |  |                                   |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
| SHEET                                                                                                                                                      |                                                                                      |  |  |                                   |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
| REV STATUS                                                                                                                                                 |                                                                                      |  |  | REV                               |  | B | B | B | B | B                                                                                                                                   | B                         | B | B                 | B | B                 | B  | B  |  |  |
| OF SHEETS                                                                                                                                                  |                                                                                      |  |  | SHEET                             |  | 1 | 2 | 3 | 4 | 5                                                                                                                                   | 6                         | 7 | 8                 | 9 | 10                | 11 | 12 |  |  |
| PMIC N/A                                                                                                                                                   |                                                                                      |  |  | PREPARED BY<br>Marcia B. Kelleher |  |   |   |   |   | <b>DLA LAND AND MARITIME</b><br><b>COLUMBUS, OHIO 43218-3990</b><br><a href="http://www.dsccl.dla.mil">http://www.dsccl.dla.mil</a> |                           |   |                   |   |                   |    |    |  |  |
| <b>STANDARD MICROCIRCUIT DRAWING</b><br><br>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE<br><br>AMSC N/A |                                                                                      |  |  | CHECKED BY<br>Wm. J. Johnson      |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
|                                                                                                                                                            |                                                                                      |  |  | APPROVED BY<br>Michael A. Frye    |  |   |   |   |   | <b>MICROCIRCUIT, LINEAR, 8-CHANNEL, JFET ANALOG MULTIPLEXER, MONOLITHIC SILICON</b>                                                 |                           |   |                   |   |                   |    |    |  |  |
|                                                                                                                                                            |                                                                                      |  |  | DRAWING APPROVAL DATE<br>88-12-08 |  |   |   |   |   |                                                                                                                                     |                           |   |                   |   |                   |    |    |  |  |
|                                                                                                                                                            |                                                                                      |  |  | REVISION LEVEL<br>B               |  |   |   |   |   | SIZE<br>A                                                                                                                           | CAGE CODE<br><b>67268</b> |   | <b>5962-87716</b> |   |                   |    |    |  |  |
|                                                                                                                                                            |                                                                                      |  |  |                                   |  |   |   |   |   | SHEET 1 OF 12                                                                                                                       |                           |   |                   |   |                   |    |    |  |  |

## 1. SCOPE

1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:



1.2.1 Device types. The device types identify the circuit function as follows:

| Device type | Generic number | Circuit function                  |
|-------------|----------------|-----------------------------------|
| 01          | MUX-08A        | 8-channel JFET analog multiplexer |
| 02          | MUX-08B        | 8-channel JFET analog multiplexer |

1.2.2 Case outlines. The case outlines are as designated in MIL-STD-1835 as follows:

| Outline letter | Descriptive designator | Terminals | Package style                |
|----------------|------------------------|-----------|------------------------------|
| E              | GDIP1-T16 or CDIP2-T16 | 16        | Dual-in-line                 |
| 2              | CQCC1-N20              | 20        | Square leadless chip carrier |

1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

## 1.3 Absolute maximum ratings.

|                                                         |                                    |
|---------------------------------------------------------|------------------------------------|
| Positive supply voltage ( $V_{CC}$ )                    | +18 V dc                           |
| Negative supply voltage ( $V_{EE}$ )                    | -18 V dc                           |
| Logic input voltage                                     | (-4 V or $V_{EE}$ ) to $V_{CC}$    |
| Analog input voltage                                    | $V_{EE} - 20$ V to $V_{CC} + 20$ V |
| Maximum current through any pin                         | 25 mA                              |
| Storage temperature range                               | -65°C to +150°C                    |
| Power dissipation ( $P_D$ ) $\frac{1}{I}$               | 500 mW                             |
| Lead temperature (soldering, 60 seconds)                | 300°C                              |
| Junction temperature ( $T_J$ )                          | 150°C                              |
| Thermal resistance, junction-to-case ( $\theta_{JC}$ )  | See MIL-STD-1835                   |
| Thermal resistance, junction-to-case ( $\theta_{JA}$ ): |                                    |
| Case E                                                  | 91°C/W                             |
| Case 2                                                  | 110°C/W                            |

## 1.4 Recommended operating conditions.

|                                               |                 |
|-----------------------------------------------|-----------------|
| Positive supply voltage ( $V_{CC}$ )          | +15 V dc        |
| Negative supply voltage ( $V_{EE}$ )          | -15 V dc        |
| Ambient operating temperature range ( $T_A$ ) | -55°C to +125°C |
| Digital "1" input voltage ( $V_{IH}$ )        | 2.0 V min       |
| Digital "0" input voltage ( $V_{IL}$ )        | 0.8 V max       |
| Analog voltage range ( $V_A$ )                | $\pm 10$ V max  |

$\frac{1}{I}$  Derate above 100°C, 10 mW/°C.

|                                                                                                |                  |                            |                   |
|------------------------------------------------------------------------------------------------|------------------|----------------------------|-------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING</b><br>DLA LAND AND MARITIME<br>COLUMBUS, OHIO 43218-3990 | SIZE<br><b>A</b> |                            | <b>5962-87716</b> |
|                                                                                                |                  | REVISION LEVEL<br><b>B</b> | SHEET<br><b>2</b> |

## 2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

### DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

### DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

### DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://assist.daps.dla.mil/quicksearch/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

## 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 Truth table. The truth table shall be as specified on figure 2.

3.2.4 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.5 Switching time waveforms. The switching time waveforms shall be as specified on figure 7.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

|                                                                                                |                  |                            |                   |
|------------------------------------------------------------------------------------------------|------------------|----------------------------|-------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING</b><br>DLA LAND AND MARITIME<br>COLUMBUS, OHIO 43218-3990 | SIZE<br><b>A</b> |                            | <b>5962-87716</b> |
|                                                                                                |                  | REVISION LEVEL<br><b>B</b> | SHEET<br><b>3</b> |

TABLE I. Electrical performance characteristics.

| Test                                                    | Symbol                                       | Conditions<br>-55°C ≤ T <sub>A</sub> ≤ +125°C<br>V <sub>CC</sub> = +15 V, V <sub>EE</sub> = -15 V<br>unless otherwise specified | Group A<br>subgroups | Device<br>type | Limits |     | Unit |
|---------------------------------------------------------|----------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------|----------------------|----------------|--------|-----|------|
|                                                         |                                              |                                                                                                                                 |                      |                | Min    | Max |      |
| Positive supply current                                 | I <sub>CC</sub>                              |                                                                                                                                 | 1                    | All            |        | 12  | mA   |
|                                                         |                                              |                                                                                                                                 | 2, 3                 |                |        | 15  |      |
| Negative supply current                                 | I <sub>EE</sub>                              |                                                                                                                                 | 1                    | All            | -3.8   |     | mA   |
|                                                         |                                              |                                                                                                                                 | 2, 3                 |                | -5     |     |      |
| "ON" resistance                                         | R <sub>ON</sub>                              | -10 V ≤ V <sub>SOURCE</sub> ≤ +10 V,<br>I <sub>SOURCE</sub> = 200 μA                                                            | 1                    | 01             |        | 300 | Ω    |
|                                                         |                                              |                                                                                                                                 |                      | 02             |        | 400 |      |
|                                                         |                                              |                                                                                                                                 | 2, 3                 | 01             |        | 400 |      |
|                                                         |                                              |                                                                                                                                 |                      | 02             |        | 500 |      |
| "ON" resistance change with<br>change in source voltage | Δ R <sub>ON</sub> /<br>Δ V <sub>SOURCE</sub> | -10 V ≤ V <sub>SOURCE</sub> ≤ +10 V,<br>I <sub>SOURCE</sub> = 200 μA <u>1</u> /                                                 | 1                    | 01             |        | 5   | %    |
|                                                         |                                              |                                                                                                                                 | 2                    |                |        | 7   |      |
|                                                         |                                              |                                                                                                                                 | 3                    |                |        | 6   |      |
|                                                         |                                              |                                                                                                                                 | 1                    | 02             |        | 7   |      |
|                                                         |                                              |                                                                                                                                 | 2                    |                |        | 8   |      |
|                                                         |                                              |                                                                                                                                 | 3                    |                |        | 8   |      |
| R <sub>ON</sub> MATCH<br>between switches               | R <sub>ON</sub><br>MATCH                     | V <sub>SOURCE</sub> = 0 V,<br>I <sub>SOURCE</sub> = 200 μA <u>1</u> /, <u>2</u> /                                               | 1                    | 01             |        | 15  | %    |
|                                                         |                                              |                                                                                                                                 | 2                    |                |        | 20  |      |
|                                                         |                                              |                                                                                                                                 | 3                    |                |        | 18  |      |
|                                                         |                                              |                                                                                                                                 | 1                    | 02             |        | 20  |      |
|                                                         |                                              |                                                                                                                                 | 2                    |                |        | 23  |      |
|                                                         |                                              |                                                                                                                                 | 3                    |                |        | 23  |      |
| Digital input current                                   | I <sub>IN</sub>                              | V <sub>IN</sub> = 0.4 V to 15 V                                                                                                 | 1                    | All            |        | ±10 | μA   |
|                                                         |                                              |                                                                                                                                 | 2, 3                 |                |        | ±20 |      |
| Digital "0" enable current                              | I <sub>IN(EN)</sub>                          | V <sub>IN(EN)</sub> = 0.4 V                                                                                                     | 1                    | All            |        | ±10 | μA   |
|                                                         |                                              |                                                                                                                                 | 2, 3                 |                |        | ±20 |      |

See footnotes at end of table.

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SHEET  
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TABLE I. Electrical performance characteristics - continued.

| Test                             | Symbol                                              | Conditions<br>-55°C ≤ T <sub>A</sub> ≤ +125°C<br>V <sub>CC</sub> = +15 V, V <sub>EE</sub> = -15 V<br>unless otherwise specified |                         | Group A<br>subgroups | Device<br>type | Limits |      | Unit |
|----------------------------------|-----------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------|-------------------------|----------------------|----------------|--------|------|------|
|                                  |                                                     |                                                                                                                                 |                         |                      |                | Min    | Max  |      |
| Source current<br>(Switch "OFF") | I <sub>SOURCE</sub><br>(OFF)                        | V <sub>SOURCE</sub> = 10 V<br>V <sub>DRAIN</sub> = -10 V                                                                        | V <sub>IL</sub> = 0.8 V | 1, 3                 | 01             |        | ±1   | nA   |
|                                  |                                                     |                                                                                                                                 | V <sub>IL</sub> = 0.7 V | 2                    |                |        | ±25  |      |
|                                  |                                                     | <u>3/</u>                                                                                                                       | V <sub>IL</sub> = 0.8V  | 1                    | 02             |        | ±2   |      |
|                                  |                                                     |                                                                                                                                 | V <sub>IL</sub> = 0.7 V | 2                    |                |        | ±50  |      |
|                                  |                                                     |                                                                                                                                 | V <sub>IL</sub> = 0.8 V | 3                    |                |        | ±25  |      |
| Drain current<br>(Switch "OFF")  | I <sub>DRAIN</sub><br>(OFF)                         | V <sub>SOURCE</sub> = 10 V<br>V <sub>DRAIN</sub> = -10 V                                                                        | V <sub>IL</sub> = 0.8V  | 1, 3                 | 01             |        | ±1   | nA   |
|                                  |                                                     |                                                                                                                                 | V <sub>IL</sub> = 0.7 V | 2                    |                |        | ±100 |      |
|                                  |                                                     | <u>3/</u>                                                                                                                       | V <sub>IL</sub> = 0.8 V | 1                    | 02             |        | ±2   |      |
|                                  |                                                     |                                                                                                                                 | V <sub>IL</sub> = 0.7 V | 2                    |                |        | ±500 |      |
|                                  |                                                     |                                                                                                                                 | V <sub>IL</sub> = 0.8 V | 3                    |                |        | ±100 |      |
| Leakage current<br>(Switch "ON") | I <sub>DRAIN(ON)</sub> +<br>I <sub>SOURCE(ON)</sub> | V <sub>IH</sub> = 2 V,<br>V <sub>SOURCE</sub> = V <sub>DRAIN</sub> = +10 V                                                      |                         | 1, 3                 | 01             |        | ±1   | nA   |
|                                  |                                                     |                                                                                                                                 |                         | 2                    |                |        | ±100 |      |
|                                  |                                                     | <u>3/</u>                                                                                                                       |                         | 1                    | 02             |        | ±2   |      |
|                                  |                                                     |                                                                                                                                 |                         | 2                    |                |        | ±500 |      |
|                                  |                                                     |                                                                                                                                 |                         | 3                    |                |        | ±100 |      |
| Analog voltage range             | V <sub>A</sub>                                      | <u>1/</u>                                                                                                                       |                         | 1, 2, 3              | All            | ±10    |      | V    |
| Digital "0" input voltage        | V <sub>IL</sub>                                     | <u>1/</u>                                                                                                                       |                         | 1, 3                 | All            |        | 0.8  | V    |
|                                  |                                                     |                                                                                                                                 |                         | 2                    |                |        | 0.7  |      |
| Digital "1" input voltage        | V <sub>IH</sub>                                     | <u>1/</u>                                                                                                                       |                         | 1, 2, 3              | All            | 2.0    |      | V    |
| Functional tests <u>4/</u>       |                                                     | See 4.3.1c                                                                                                                      |                         | 1, 2, 3              | All            |        |      |      |
| Switching time                   | t <sub>PHL</sub> , t <sub>PLH</sub>                 | V <sub>S1</sub> = +10 V, V <sub>S8</sub> = -10 V,<br>R <sub>L</sub> = 10 MΩ, C <sub>L</sub> = 10 pF<br>See figures 4 and 7      |                         | 9                    | All            |        | 2.1  | μs   |
|                                  |                                                     |                                                                                                                                 |                         | 10, 11 <u>1/</u>     |                |        | 3.5  |      |

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - continued.

| Test                    | Symbol               | Conditions<br>-55°C ≤ T <sub>A</sub> ≤ +125°C<br>V <sub>CC</sub> = +15 V, V <sub>EE</sub> = -15 V<br>unless otherwise specified | Group A<br>subgroups | Device<br>type | Limits |     | Unit |
|-------------------------|----------------------|---------------------------------------------------------------------------------------------------------------------------------|----------------------|----------------|--------|-----|------|
|                         |                      |                                                                                                                                 |                      |                | Min    | Max |      |
| Enable delay "ON"       | t <sub>ON(EN)</sub>  | V <sub>S1</sub> = -1.0 V, C <sub>L</sub> = 10 pF,<br>R <sub>L</sub> = 1 kΩ,                                                     | 9                    | All            |        | 2.0 | μs   |
|                         |                      |                                                                                                                                 | 10, 11 <u>1/</u>     |                |        | 3.0 |      |
| Enable delay "OFF"      | t <sub>OFF(EN)</sub> | See figures 5 and 7                                                                                                             | 9                    | All            |        | 0.4 | μs   |
|                         |                      |                                                                                                                                 | 10, 11 <u>1/</u>     |                |        | 1.0 |      |
| Break-before-make delay | t <sub>OPEN</sub>    | V <sub>S1</sub> = V <sub>S8</sub> = -1V, T <sub>A</sub> = 25°C<br>See figures 6 and 7                                           | 9                    | All            | 0.1    |     | μs   |

1/ Guaranteed, if not tested, to the specified limits.

2/ R<sub>ON</sub> match specified as a percentage of R<sub>AVERAGE</sub> where:

$$\frac{1}{N}$$

$$R_{AVERAGE} = N \sum_{i=1} R_i \text{ with } N = \text{number of channels, } R_i = \text{each channel's "ON" resistance.}$$

3/ Conditions applied to leakage tests insure worst case leakages.

4/ Verified by leakage tests.

3.5 Marking. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 Certification/compliance mark. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DLA Land and Maritime -VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DLA Land and Maritime -VA shall be required for any change that affects this drawing.

3.9 Verification and review. DLA Land and Maritime, DLA Land and Maritime's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

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| Device type     | 01 and 02       |                 |
|-----------------|-----------------|-----------------|
| Case outline    | E               | 2               |
| Terminal number | Terminal symbol |                 |
| 1               | A <sub>0</sub>  | NC              |
| 2               | ENABLE          | A <sub>0</sub>  |
| 3               | V <sub>EE</sub> | ENABLE          |
| 4               | S1              | V <sub>EE</sub> |
| 5               | S2              | S1              |
| 6               | S3              | NC              |
| 7               | S4              | S2              |
| 8               | DRAIN           | S3              |
| 9               | S8              | S4              |
| 10              | S7              | DRAIN           |
| 11              | S6              | NC              |
| 12              | S5              | S8              |
| 13              | V <sub>CC</sub> | S7              |
| 14              | GND             | S6              |
| 15              | A <sub>2</sub>  | S5              |
| 16              | A <sub>1</sub>  | NC              |
| 17              |                 | V <sub>CC</sub> |
| 18              |                 | GND             |
| 19              |                 | A <sub>2</sub>  |
| 20              |                 | A <sub>1</sub>  |

FIGURE 1. Terminal connections.

|                                                                                                |                  |                            |                   |
|------------------------------------------------------------------------------------------------|------------------|----------------------------|-------------------|
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|                                                                                                |                  | REVISION LEVEL<br><b>B</b> | SHEET<br><b>7</b> |

| A <sub>2</sub> | A <sub>1</sub> | A <sub>0</sub> | ENABLE | "ON" Channel |
|----------------|----------------|----------------|--------|--------------|
| X              | X              | X              | L      | NONE         |
| L              | L              | L              | H      | 1            |
| L              | L              | H              | H      | 2            |
| L              | H              | L              | H      | 3            |
| L              | H              | H              | H      | 4            |
| H              | L              | L              | H      | 5            |
| H              | L              | H              | H      | 6            |
| H              | H              | L              | H      | 7            |
| H              | H              | H              | H      | 8            |

FIGURE 2. Truth table.

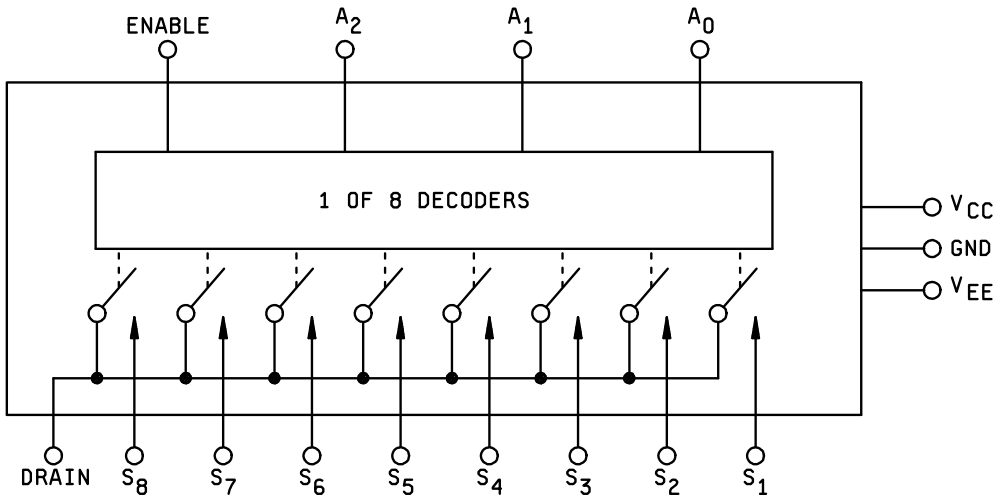


FIGURE 3. Logic diagram.

|                                                                                                |                  |                            |                   |
|------------------------------------------------------------------------------------------------|------------------|----------------------------|-------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING</b><br>DLA LAND AND MARITIME<br>COLUMBUS, OHIO 43218-3990 | SIZE<br><b>A</b> |                            | <b>5962-87716</b> |
|                                                                                                |                  | REVISION LEVEL<br><b>B</b> | SHEET<br><b>8</b> |

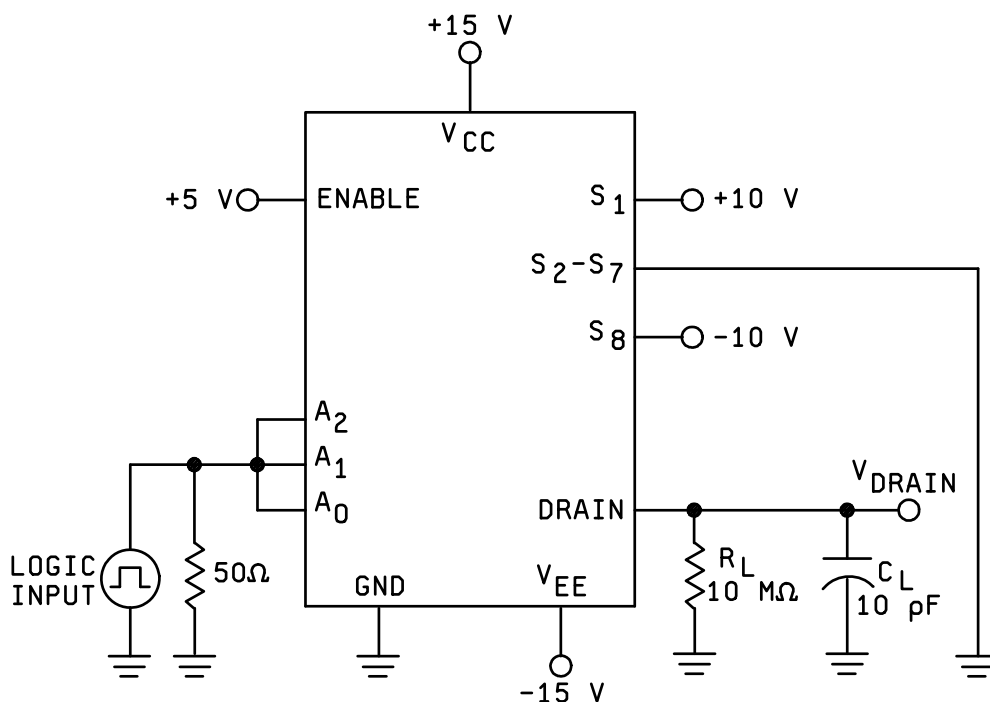


FIGURE 4. Transition time test circuit.

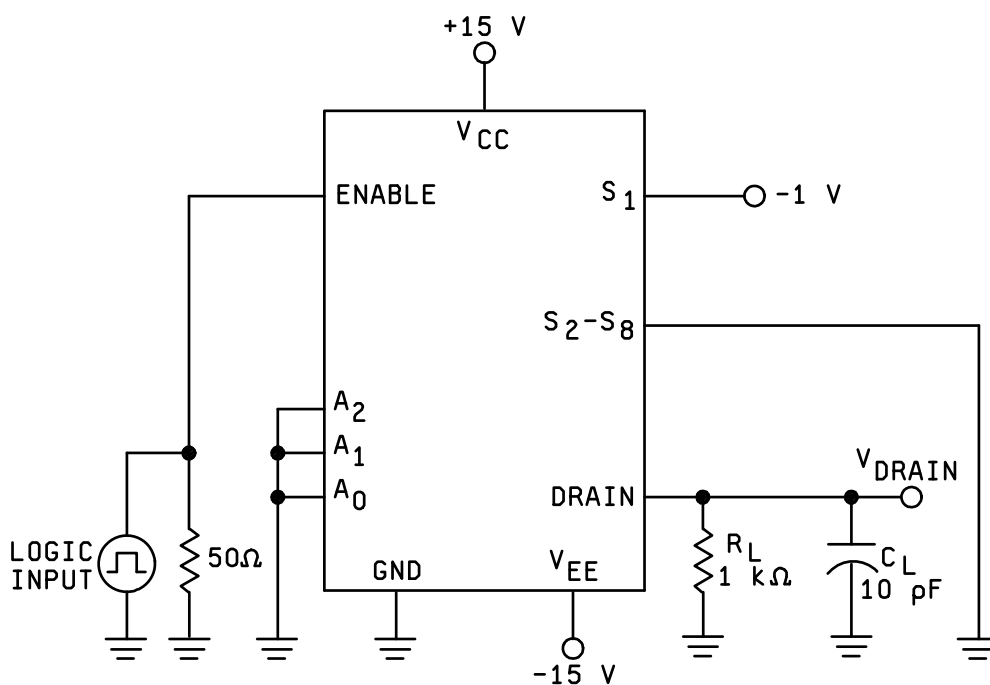


FIGURE 5. Enable delay time test circuit.

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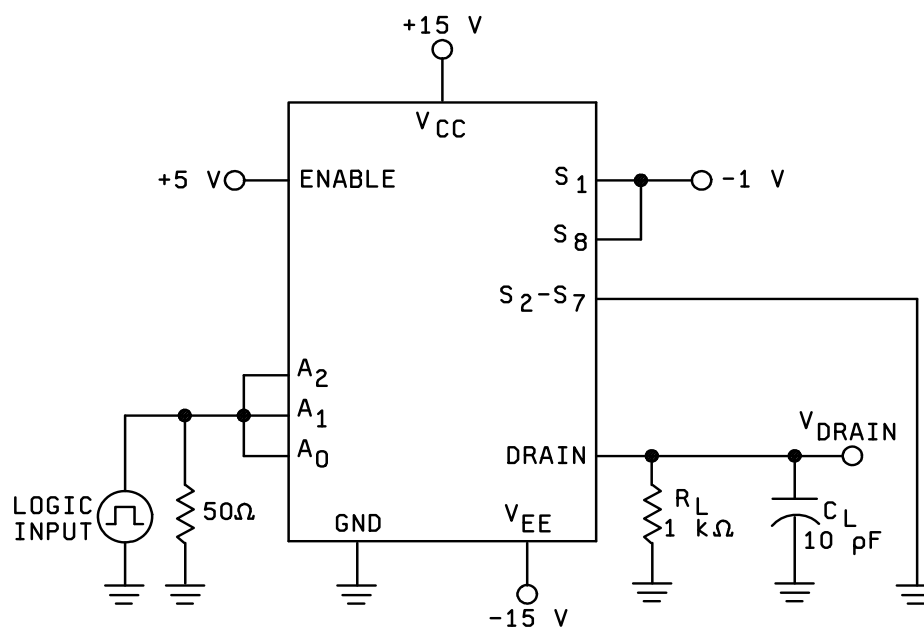


FIGURE 6. Break-before-make test circuit.

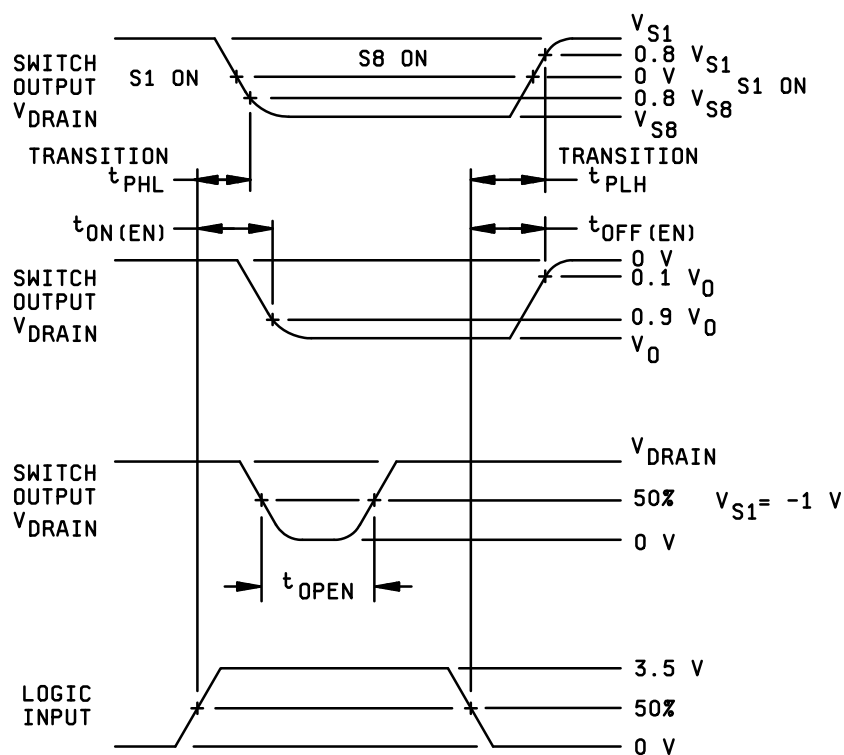


FIGURE 7. Switching time waveforms.

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#### 4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.

(2)  $T_A = +125^{\circ}\text{C}$ , minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

TABLE II. Electrical test requirements.

| MIL-STD-883 test requirements                                      | Subgroups<br>(in accordance with<br>MIL-STD-883, method 5005,<br>table I) |
|--------------------------------------------------------------------|---------------------------------------------------------------------------|
| Interim electrical parameters<br>(method 5004)                     | 1                                                                         |
| Final electrical test parameters<br>(method 5004)                  | 1*, 2, 3, 9                                                               |
| Group A test requirements<br>(method 5005)                         | 1, 2, 3, 9, (10, 11)**                                                    |
| Groups C and D end-point<br>electrical parameters<br>(method 5005) | 1                                                                         |

\* PDA applies to subgroup 1.

\*\* Subgroups 10 and 11 are guaranteed, if not tested, to the limits specified in table I.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

##### 4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, 7, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Leakage tests, performed on all channels, shall verify the truth table.

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#### 4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
  - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
  - (2)  $T_A = +125^{\circ}\text{C}$ , minimum.
  - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

### 5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

### 6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DLA Land and Maritime -VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime -VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DLA Land and Maritime -VA.

|                                                                                                |                   |                             |                     |
|------------------------------------------------------------------------------------------------|-------------------|-----------------------------|---------------------|
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## STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 11-06-08

Approved sources of supply for SMD 5962-87716 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.dscc.dla.mil/Programs/Smcr/>.

| Standard<br>microcircuit drawing<br>PIN <u>1/</u> | Vendor<br>CAGE<br>number | Vendor<br>similar<br>PIN <u>2/</u> |
|---------------------------------------------------|--------------------------|------------------------------------|
| 5962-8771601EA                                    | 24355                    | MUX-08AQ                           |
| 5962-8771602EA                                    | 24355                    | MUX-08BQ                           |
| 5962-87716022A                                    | 24355                    | MUX-08BRC                          |

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE  
number

24355

Vendor name  
and address

Analog Devices  
Rt 1 Industrial Park  
PO Box 9106  
Norwood, MA 02062  
Point of contact:  
804 Woburn Street  
Wilmington, MA 01887-3462

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

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- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
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- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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**Факс:** 8 (812) 320-02-42

**Электронная почта:** [org@eplast1.ru](mailto:org@eplast1.ru)

**Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.