

ZXMHC3A01N8 30V SO8 Complementary enhancement mode MOSFET H-Bridge

Summary

Device	V _{(BR)DSS}	Q_{G}	R _{DS(on)}	I _D T _A = 25°C
N-CH	30V 3.9nC -		125mΩ @ V _{GS} = 10V	2.7A
	30 v	3.9110	180mΩ @ V _{GS} = 4.5V	2.2A
P-CH	H -30V	E 2nC	210mΩ @ V _{GS} = -10V	-2.1A
P-CH		5.2nC	330mΩ @ V _{GS} = -4.5V	-1.6A



Description

This new generation complementary MOSFET H-Bridge features low on-resistance achievable with low gate drive.

Features

• 2 x N + 2 x P channels in a SOIC package

Applications

- DC Motor control
- DC-AC Inverters

Ordering information

Device	Reel size	Tape width	Quantity	
	(inches)	(mm)	per reel	
ZXMHC3A01N8TC	13	12	2,500	

Device marking

ZXMHC 3A01





Absolute maximum ratings

Parameter	Symbol	N- channel	P- channel	Unit
Drain-Source voltage	V _{DSS}	30	-30	V
Gate-Source voltage	V _{GS}	±20	±20	V
Continuous Drain current @ V_{GS} = 10V; T _A =25°C (b)	I _D	2.72	-2.06	А
@ V _{GS} = 10V; T _A =70°C ^(b)		2.18	-1.65	
@ V _{GS} = 10V; T _A =25°C ^(a)		2.17	-1.64	
@ V _{GS} = 10V; T _L =25°C ^(f)		2.21	-1.67	
Pulsed Drain current @ V_{GS} = 10V; T _A =25°C ^(C)	I _{DM}	11.7	-8.84	А
Continuous Source current (Body diode) at $T_A = 25^{\circ}C^{(b)}$	I _S	1.60	-1.60	А
Pulsed Source current (Body diode) at $T_A = 25^{\circ}C^{(C)}$	I _{SM}	11.7	-8.84	А
Power dissipation at $T_A = 25^{\circ}C^{(a)}$	PD	0.87		W
Linear derating factor		6.94		mW/°C
Power dissipation at $T_A = 25^{\circ}C^{(b)}$	PD	1.	36	W
Linear derating factor	_	10).9	mW/°C
Power dissipation at T _L =25°C ^(f)	PD	0.90		W
Linear derating factor	_	7.	19	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to	o 150	°C

Thermal resistance

Parameter	Symbol	Value	Unit
Junction to ambient ^(a)	$R_{\theta JA}$	144	°C/W
Junction to ambient ^(b)	$R_{\theta JA}$	92	°C/W
Junction to ambient ^(d)	$R_{\theta JA}$	106	°C/W
Junction to ambient ^(e)	$R_{\theta JA}$	254	°C/W
Junction to lead ^(f)	$R_{\theta JL}$	139	°C/W

NOTES:

(a) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions with the heat-sink split into two equal areas (one for each drain connection); the device is measured when operating in a steady-state condition with one active die.

(b) Same as note (a), except the device is measured at $t \le 10$ sec.

(c) Same as note (a), except the device is pulsed with D= 0.02 and pulse width 300 μs. The pulse current is limited by the maximum junction temperature.

(d) For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions with the heat-sink split into two equal areas (one for each drain connection); the device is measured when operating in a steady-state condition with one active die.

(e) For a device surface mounted on minimum copper 1.6mm FR4 PCB, in still air conditions; the device is measured when operating in a steady-state condition with one active die.

(f) Thermal resistance from junction to solder-point (at the end of the drain lead); the device is operating in a steady-state condition with one active die.

Thermal characteristics



Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Static							
Drain-Source breakdown voltage	V _{(BR)DSS}	30			V	$I_{D} = 250 \mu A, V_{GS} = 0 V$	
Zero Gate voltage Drain current	I _{DSS}			0.5	μA	V _{DS} = 30V, V _{GS} = 0V	
Gate-Body leakage	I _{GSS}			±100	nA	V_{GS} = ±20V, V_{DS} = 0V	
Gate-Source threshold voltage	V _{GS(th)}	1.0		3.0	V	I_D = 250µA, V_{DS} = V_{GS}	
Static Drain-Source on-state resistance ^(a)	R _{DS(on)}			0.125 0.180	Ω	V _{GS} = 10V, I _D = 2.5A V _{GS} = 4.5V, I _D = 2.0A	
Forward Transconductance ^{(a) (c)}	g fs		3.5		S	V _{DS} = 15V, I _D = 2.5A	
Dynamic							
Capacitance ^(c)							
Input capacitance	C _{iss}		190		pF		
Output capacitance	C _{oss}		38		pF	V_{DS} = 25V, V_{GS} = 0V	
Reverse transfer capacitance	C _{rss}		20		pF	f= 1MHz	
Switching ^{(b) (c)}							
Turn-on-delay time	t _{d(on)}		1.7		ns		
Rise time	t _r		2.3		ns	V _{DD} = 15V, V _{GS} = 10V	
Turn-off delay time	t _{d(off)}		6.6		ns ns	I _D = 2.5A R _G ≅ 6.0Ω,	
Fall time	t _f		2.9			$1 \times G = 0.022,$	
Gate charge ^(c)							
Total Gate charge	Qg		3.9		nC		
Gate-Source charge	Q _{gs}		0.6		nC	V _{DS} =15V, V _{GS} = 10V	
Gate-Drain charge	Q _{gd}				nC	I _D = 2.5A	
Source-Drain diode							
Diode forward voltage ^(a)	V _{SD}			0.95	V	I _S = 1.25A, V _{GS} = 0V	
Reverse recovery time (c)	t _{rr}		17.7		ns	I _S = 2.5A, di/dt= 100A/μs	
Reverse recovery charge ^(c)	Q _{rr}		13.0		nC	$15-2.5\pi$, $u/ul=100A/\mu S$	

N-channel electrical characteristics (at T_{amb} = 25°C unless otherwise stated)

NOTES:

(a) Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$. (b) Switching characteristics are independent of operating junction temperature. (c) For design aid only, not subject to production testing

N-channel typical characteristics





N-channel typical characteristics -continued

Test circuits



Basic gate charge waveform



Switching time waveforms



Gate charge test circuit



Switching time test circuit

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Static							
Drain-Source breakdown voltage	V _{(BR)DSS}	-30			V	I _D = -250μA, V _{GS} = 0V	
Zero Gate voltage Drain current	I _{DSS}			-0.5	μA	V _{DS} = -30V, V _{GS} = 0V	
Gate-Body leakage	I _{GSS}			±100	nA	V_{GS} = ±20V, V_{DS} = 0V	
Gate-Source threshold voltage	V _{GS(th)}	-1.0		-3.0	V	I_D = -250 μ A, V_{DS} = V_{GS}	
Static Drain-Source on-state resistance ^(a)	R _{DS(on)}			0.210 0.330	Ω	V _{GS} = -10V, I _D = -1.4A V _{GS} = -4.5V, I _D = -1.1A	
Forward Transconductance ^{(a) (c)}	g fs		2.5		S	V _{DS} = -15V, I _D = -1.4A	
Dynamic							
Capacitance ^(c)			•			· · · · · · · · · · · · · · · · · · ·	
Input capacitance	C _{iss}		204		pF		
Output capacitance	C _{oss}		39.8		pF	V _{DS} = -15V, V _{GS} = 0V	
Reverse transfer capacitance	C _{rss}		25.8		pF	f= 1MHz	
Switching ^{(b) (c)}			•				
Turn-on-delay time	t _{d(on)}		1.2		ns		
Rise time	t _r		2.3		ns	V _{DD} = -15V, V _{GS} = -10V	
Turn-off delay time	t _{d(off)}		12.1		ns	I _D = -1.0A R _G ≅ 6.0Ω	
Fall time	t _f		7.5		ns	-10G = 0.022	
Gate charge ^(c)			-		•		
Total Gate charge	Qg		5.2		nC		
Gate-Source charge	Q _{gs}		0.7		nC	V_{DS} = -15V, V_{GS} = -10V	
Gate-Drain charge Q _{gd}					nC	- I _D = -1.4A	
Source–Drain diode							
Diode forward voltage ^(a)	V _{SD}		-0.85	-0.95	V	I _S = -1.5A, V _{GS} = 0V	
Reverse recovery time ^(c) t _{rr}			19		ns	I _S = -0.95A,	
Reverse recovery charge ^(c)	Q _{rr}		15		nC	di/dt= 100A/µs	

P-channel electrical characteristics (at T_{amb} = 25°C unless otherwise stated)

NOTES:

(a) Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$. (b) Switching characteristics are independent of operating junction temperature. (c) For design aid only, not subject to production testing

P-channel typical characteristics





P-channel typical characteristics -continued

Test circuits



Basic gate charge waveform



Switching time waveforms



Gate charge test circuit



Switching time test circuit

Packaging details - SO8



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
А	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	с	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°
E	0.150	0.157	3.80	4.00	-	-	-	-	-
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Note: Controlling dimensions are in inches. Approximate dimensions are provided in millimeters

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