



74LV05A

### HEX INVERTERS WITH OPEN DRAIN OUTPUTS

## Description

The 74LV05A provides provides six independent inverters with open drain outputs. The device is designed for operation with a power supply range of 2.0V to 5.5V.

The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down.

The gates perform the Boolean function:

 $Y = \overline{A}$ 

### Features

- Wide Supply Voltage Range from 2.0V to 5.5V
- Sinks 12mA at V<sub>CC</sub> = 4.5V
- CMOS low power consumption
- I<sub>OFF</sub> Supports Partial -Power Down Operation
- Inputs or Outputs accept up to 5.5V
- Inputs can be driven by 3.3V or 5V allowing for voltage translation applications.
- Schmitt Trigger Action at All Inputs
- ESD Protection Tested per JESD 22
- Exceeds 200-V Machine Model (A115)
- Exceeds 2000-V Human Body Model (A114)
- Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Pin Assignments**



## Applications

- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
  - PCs, networking, notebooks, ultrabooks, netbooks
  - Computer peripherals, hard drives, CD/DVD ROM
  - TV, DVD, DVR, set top box

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

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## **Pin Descriptions**

Pin Number	Pin Name	Description
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	Vcc	Supply Voltage





## **Function Table**

Input	Output
Α	Y
Н	L
L	Z

# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 4)	-0.5 to +7.0	V
l <sub>IK</sub>	Input Clamp Current VI < 0V	-20	mA
I <sub>OK</sub>	Output Clamp Current V <sub>O</sub> < 0V	-50	mA
Ι <sub>Ο</sub>	Continuous Output Current - 0.5V < V <sub>O</sub> Vcc +0.5V	- 25	mA
lcc	Continuous Current Through Vcc	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



# Recommended Operating Conditions (Note 5) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		2.0	5.5	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	5.5	V
		2.0V		50	μA
		2.3V to 2.7V		2	mA
I <sub>OL</sub>	Low-Level Output Current	3.0V to 3.6V		6	mA
		4.5V to 5.5V		12	mA
		2.3V to 2.7V		200	
Δt/ΔV	Input Transition Rise or Fall	3.0V to 3.6V		100	ns/V
	T die	4.5V to 5.5V		20	
T <sub>A</sub>	Operating Free-Air Temperature		-40	+125	°C

Note: 5. Unused inputs should be held at Vcc or Ground.

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	N/	T <sub>A</sub> = -40°0	C to +85°C	T <sub>A</sub> = -40°C	to +125°C	l lmit
Symbol	Farameter		Vcc	Min	Max	Min	Max	Unit
			2.0V	1.5		1.5		
.,	High-Level Input		2.3V to 2.7V	V <sub>CC</sub> X 0.7		V <sub>CC</sub> X 0.7		V
VIH	Voltage		3.0V to 3.6V	V <sub>CC</sub> X 0.7		V <sub>CC</sub> X 0.7		
			4.5V to 5.5V	V <sub>CC</sub> X 0.7		V <sub>CC</sub> X 0.7		
	V <sub>IL</sub> Low-Level Input Voltage		2.0V		0.5		0.5	
. <i>i</i>			2.3V to 2.7V		V <sub>CC</sub> X 0.3		V <sub>CC</sub> X 0.3	V
VIL			3.0V to 3.6V		V <sub>CC</sub> X 0.3		V <sub>CC</sub> X 0.3	
			4.5V to 5.5V		V <sub>CC</sub> X 0.3		V <sub>CC</sub> X 0.3	
		I <sub>OL</sub> = 50μA	2.0V to 5.5V		0.1		0.1	
.,	Low-Level	I <sub>OL</sub> = 2mA	2.3V		0.4		0.4	. /
V <sub>OL</sub>	Output Voltage	I <sub>OL</sub> = 6mA	3.0V		0.44		0.44	V
		I <sub>OL</sub> = 12mA	4.5V		0.55		0.55	
I <sub>OFF</sub>	Power Down Leakage Current	$V_1$ or $V_0$ = 0 to 5.5V	0V		5		5	μA
l <sub>l</sub>	Input Current	V <sub>I</sub> =GND or 5.5V	0 to 5.5V		±1		±1	μA
I <sub>CC</sub>	Supply Current	$V_{I} = GND \text{ or } V_{CC}$ $I_{O}=0$	5.5V		20		20	μA



# **Switching Characteristics**

$V_{\rm CC} = 2.5 \text{V} \pm 0.2 \text{V}$										
Symbol	Parameter	Test Conditions	-	T <sub>A</sub> = +25°C		-40°C to +85 °C		-40°C to +125°C		Unit
Symbol	Falalletei	Test conditions	Min	Тур	Max	Min	Max	Min	Max	Onic
t <sub>PLZ</sub>		Figure 1	_	3.6	10.4	1	13	1	13	20
t <sub>PZL</sub>	Propagation Delay A <sub>N</sub>	C <sub>L</sub> = 15pF	—	5.8	12.2	1	15	1	15	ns
t <sub>PLZ</sub>	to Y <sub>N</sub>	Figure 1	—	6.1	15.2	1	18	1	18	20
t <sub>PZL</sub>		C <sub>L</sub> = 50pF	_	8.1	16.6	1	19.5	1	19.5	ns

## $V_{CC}$ =3.3V ± 0.3V

Symbol	Parameter	Test Conditions		T <sub>A</sub> = +25°C		-40°C to +85 °C		-40°C to +125°C		Unit
Symbol Parameter		Test conditions	Min	Тур	Max	Min	Max	Min	Max	Onit
t <sub>PLZ</sub>		Figure 1	-	2.9	7.1	1	8.5	1	8.5	20
t <sub>PZL</sub>	Propagation Delay A <sub>N</sub> to Y <sub>N</sub>	C <sub>L</sub> = 15pF	_	4	7.1	1	8.5	1	8.5	ns
t <sub>PLZ</sub>		Figure 1	_	4.7	10.6	1	12	1	12	20
t <sub>PZL</sub>		C <sub>L</sub> = 50pF	_	5.8	10.6	1	12	1	12	ns

### $V_{CC}$ =5.0V $\pm$ 0.5V

Symbol	Parameter	Test Conditions	Test Conditions $T_A = +25^{\circ}C$		-40°C to +85 °C		-40°C to +125°C		Unit	
Symbol Parameter		Test conditions	Min	Тур	Max	Min	Max	Min	Max	Unit
t <sub>PLZ</sub>		Figure 1	—	2.2	5.5	1	6.5	1	6.5	20
t <sub>PZL</sub>	Propagation Delay A <sub>N</sub> to Y <sub>N</sub>	C <sub>L</sub> = 15pF	—	2.9	5.5	1	6.5	1	6.5	ns
t <sub>PLZ</sub>		Figure 1	—	3.4	7.5	1	8.5	1	8.5	
t <sub>PZL</sub>		C <sub>L</sub> = 50pF	—	4.2	7.5	1	8.5	1	8.5	ns

# **Operating Characteristics**

T <sub>A</sub> = +25°C									
	Parameter	Test Conditions	V <sub>cc</sub>	Тур	Unit				
6	Power Dissipation	f = 10MHz	3.3V	2.5	pF				
C <sub>pd</sub>	Capacitance per Gate	C <sub>L</sub> = 50pF	5.0V	3.0	PΓ				

# **Noise Characteristics**

$V_{CC} = 3V, C_L = 8$	$V_{\rm CC} = 3V, C_{\rm L} = 50 pF T_{\rm A} = +25^{\circ} C$									
Symbol	Parameter	Min	Тур	Max	Unit					
V <sub>OL(p)</sub>	Quiet output, maximum dynamic VOL	—	0.2	0.8	V					
V <sub>OL(V)</sub>	Quiet output, minimum dynamic V <sub>OL</sub>	—	-0.1	-0.8	V					
V <sub>OH(V)</sub>	Quiet output, minimum dynamic V <sub>OH</sub>	—	3.1	_	V					
V <sub>IH(D)</sub>	High Level dynamic input voltage	2.31	—	—	V					
VIL(D)	Low Level dynamic input voltage	_	—	0.99	V					

## Package Characteristics

Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Unit
Ci	Input Capacitance	$V_i = V_{CC} - or GND$	2.0V to 5.5V	_	3.3	10	pF







- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz D. For the open drain device  $t_{PLZ}$  and  $t_{PZL}$  are the same as  $t_{PD}$
- E. t<sub>PZL</sub> is measured at V<sub>M</sub>.
- D.  $t_{PLZ}$  is measured at V\_OL +V\_ $\Delta$  where V\_ $\Delta$  = 0.3V

Figure 1 Load Circuit and Voltage Waveforms



## **Ordering Information**



Part Number	Package Code	Packaging	13" Tape	and Reel
Fart Nulliber	Fackage Code	(Note 6)	Quantity	Part Number Suffix
74LV05AS14-13	S14	SO-14	2500/Tape & Reel	-13
74LV05AT14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Notes:

6. The taping orientation and tape details can be found at http://www.diodes.com/datasheets/ap02007.pdf

## **Marking Information**

### (1) SO14, TSSOP14



Part Number	Package
74LV05AS14	SO-14
74LV05AT14	TSSOP-14



## Package Outline Dimensions (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



SO-14		
Dim	Min	Max
Α	1.47	1.73
A1	0.10	0.25
A2	1.45 Typ	
В	0.33	0.51
D	8.53	8.74
Е	3.80	3.99
е	1.27 Typ	
Н	5.80	6.20
L	0.38	1.27
θ	0°	8°
All Dimensions in mm		

## Package Type: TSSOP-14



TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С		1.2
D	0.8	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Тур	
All Dimensions in mm		



## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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