

54150/DM54150/DM74150, 54151A/DM54151A/DM74151A Data Selectors/Multiplexers

General Description

These data selectors/multiplexers contain full on-chip decoding to select the desired data source. The 150 selects one-of-sixteen data sources; the 151A selects one-of-eight data sources. The 150 and 151A have a strobe input which must be at a low logic level to enable these devices. A high level at the strobe forces the W output high and the Y output (as applicable) low.

The 151A features complementary W and Y outputs, whereas the 150 has an inverted (W) output only.

The 151A incorporates address buffers which have symmetrical propagation delay times through the complementary paths. This reduces the possibility of transients occurring at the output(s) due to changes made at the select inputs, even when the 151A outputs are enabled (i.e., strobe low).

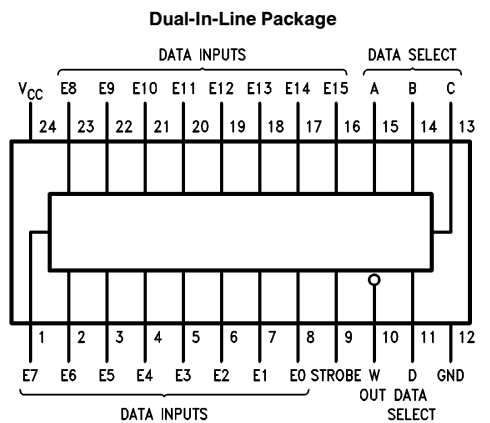
Features

- 150 selects one-of-sixteen data lines
- 151A selects one-of-eight data lines
- Performs parallel-to-serial conversion
- Permits multiplexing from N lines to one line
- Also for use as Boolean function generator
- Typical average propagation delay time, data input to W output

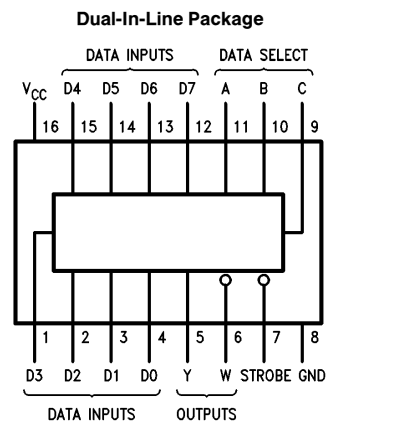
150	11 ns
151A	9 ns
- Typical power dissipation

150	200 mW
151A	135 mW
- Alternate Military/Aerospace device (54150, 54151A) is available. Contact a National Semiconductor Sales Office/Distributor for specifications.

Connection Diagrams



Order Number 54150DQMB, 54150FMQB,
DM54150J or DM74150N
See NS Package Number J24A, N24A or W24C



Order Number 54151ADMQB, 54151AFMQB,
DM54151AJ, DM54151AW or DM74151AN
See NS Package Number J16A, N16E or W16A

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
DM54 and 54	−55°C to +125°C
DM74	0°C to +70°C
Storage Temperature Range	−65°C to +150°C

Note: The “Absolute Maximum Ratings” are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the “Electrical Characteristics” table are not guaranteed at the absolute maximum ratings. The “Recommended Operating Conditions” table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54150			DM74150			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
I _{OH}	High Level Output Current			−0.8			−0.8	mA
I _{OL}	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	−55		125	0		70	°C

150 Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = −12 mA			−1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max V _{IL} = Max, V _{IH} = Min	2.4			V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max V _{IH} = Min, V _{IL} = Max			0.4	V
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 5.5V			1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.4V			40	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V			−1.6	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	DM54 −20 DM74 −18		−55 −55	mA
I _{CC}	Supply Current	V _{CC} = Max, (Note 3)		40	68	mA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time.

Note 3: I_{CC} is measured with the strobe and data select inputs at 4.5V, all other inputs and outputs open.

'150 Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^\circ C$ (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	From (Input) To (Output)	$R_L = 400\Omega, C_L = 15 pF$		Units
			Min	Max	
t_{PLH}	Propagation Delay Time Low to High Level Output	Select to W		35	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	Select to W		33	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	Strobe to W		24	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	Strobe to W		30	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	E0-E15 to W		20	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	E0-E15 to W		14	ns

Recommended Operating Conditions

Symbol	Parameter	DM54151A			DM74151A			Units
		Min	Nom	Max	Min	Nom	Max	
V_{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V_{IH}	High Level Input Voltage	2			2			V
V_{IL}	Low Level Input Voltage			0.8			0.8	V
I_{OH}	High Level Output Current			-0.8			-0.8	mA
I_{OL}	Low Level Output Current			16			16	mA
T_A	Free Air Operating Temperature	-55		125	0		70	$^\circ C$

'151A Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V_I	Input Clamp Voltage	$V_{CC} = \text{Min}, I_I = -12 \text{ mA}$			-1.5	V
V_{OH}	High Level Output Voltage	$V_{CC} = \text{Min}, I_{OH} = \text{Max}$ $V_{IL} = \text{Max}, V_{IH} = \text{Min}$	2.4			V
V_{OL}	Low Level Output Voltage	$V_{CC} = \text{Min}, I_{OL} = \text{Max}$ $V_{IH} = \text{Min}, V_{IL} = \text{Max}$			0.4	V
I_I	Input Current @ Max Input Voltage	$V_{CC} = \text{Max}, V_I = 5.5 \text{ V}$			1	mA
I_{IH}	High Level Input Current	$V_{CC} = \text{Max}, V_I = 2.4 \text{ V}$			40	μA
I_{IL}	Low Level Input Current	$V_{CC} = \text{Max}, V_I = 0.4 \text{ V}$			-1.6	mA
I_{OS}	Short Circuit Output Current	$V_{CC} = \text{Max}$ (Note 2)	DM54	-20	-55	mA
			DM74	-18	-55	
I_{CC}	Supply Current	$V_{CC} = \text{Max}, (\text{Note 3})$		27	48	mA

Note 1: All typicals are at $V_{CC} = 5 \text{ V}$, $T_A = 25^\circ\text{C}$.

Note 2: Not more than one output should be shorted at a time.

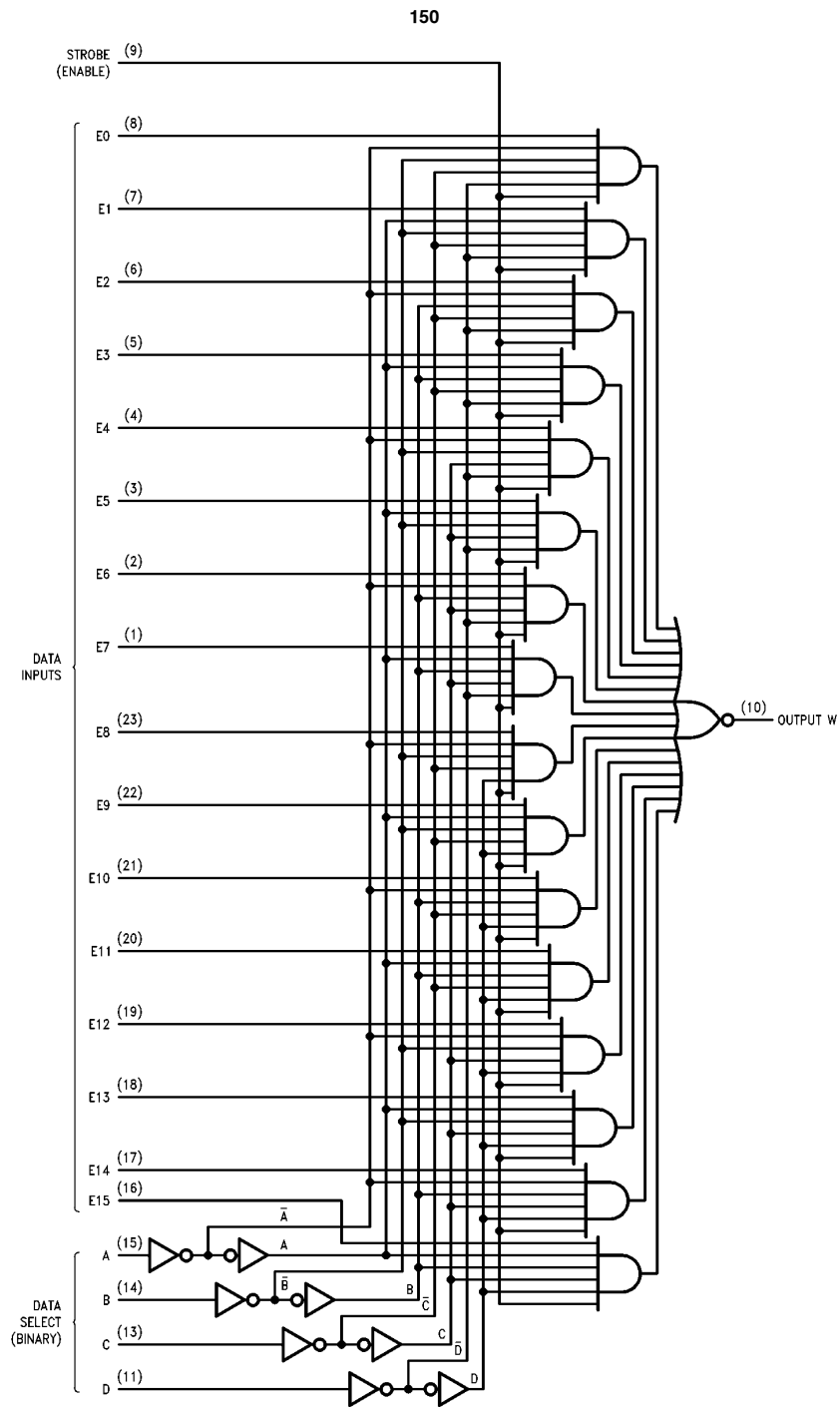
Note 3: I_{CC} is measured with the strobe and data select inputs at 4.5V, all other inputs and outputs open.

'151A Switching Characteristics

at $V_{CC} = 5 \text{ V}$ and $T_A = 25^\circ\text{C}$ (See Section 1 for Test Waveforms and Output Load)

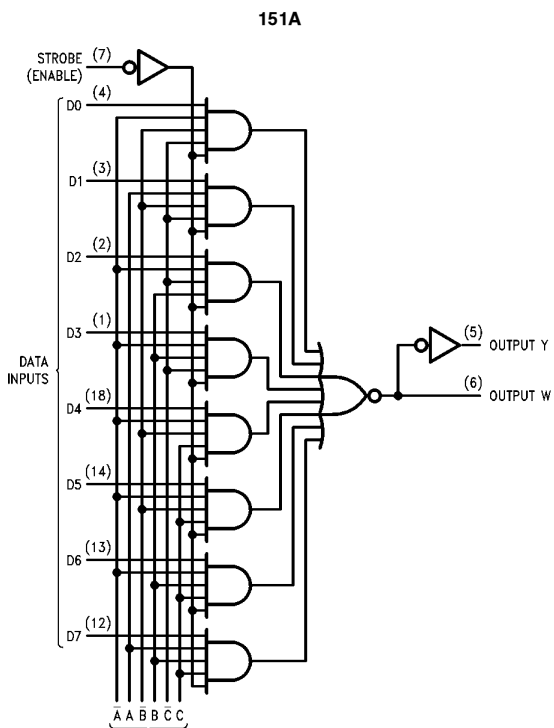
Symbol	Parameter	From (Input) To (Output)	$R_L = 400\Omega, C_L = 15 \text{ pF}$		Units
			Min	Max	
t_{PLH}	Propagation Delay Time Low to High Level Output	Select (4 Levels) to Y		38	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	Select (4 Levels) to Y		30	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	Select (3 Levels) to W		26	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	Select (3 Levels) to W		30	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	Strobe to Y		33	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	Strobe to Y		30	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	Strobe to W		21	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	Strobe to W		25	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	D0-D7 to Y		24	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	D0-D7 to Y		24	ns
t_{PLH}	Propagation Delay Time Low to High Level Output	D0-D7 to W		14	ns
t_{PHL}	Propagation Delay Time High to Low Level Output	D0-D7 to W		14	ns

Logic Diagrams

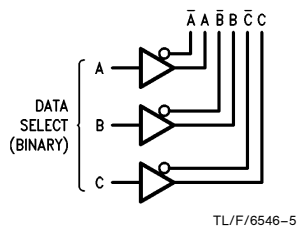


TL/F/6546-3

Logic Diagrams



Address Buffers for 54151A/74151A



See Address Buffers Below

TL/F/6546-4

Function Tables

54150/74150

Inputs					Outputs W
Select				Strobe S	
D	C	B	A		
X	X	X	X	H	H
L	L	L	L	L	$\overline{E0}$
L	L	L	H	L	$\overline{E1}$
L	L	H	L	L	$\overline{E2}$
L	L	H	H	L	$\overline{E3}$
L	H	L	L	L	$\overline{E4}$
L	H	L	H	L	$\overline{E5}$
L	H	H	L	L	$\overline{E6}$
L	H	H	H	L	$\overline{E7}$
H	L	L	L	L	$\overline{E8}$
H	L	L	H	L	$\overline{E9}$
H	L	H	L	L	$\overline{E10}$
H	L	H	H	L	$\overline{E11}$
H	H	L	L	L	$\overline{E12}$
H	H	L	H	L	$\overline{E13}$
H	H	H	L	L	$\overline{E14}$
H	H	H	H	L	$\overline{E15}$

H = High Level, L = Low Level, X = Don't Care

$\overline{E0}, \overline{E1} \dots \overline{E15}$ = the complement of the level of the respective E input

54151A/75151A

Inputs			Outputs		
Select			Strobe S	Y	W
C	B	A			
X	X	X	H	L	H
L	L	L	L	D0	$\overline{D0}$
L	L	H	L	D1	$\overline{D1}$
L	H	L	L	D2	$\overline{D2}$
L	H	H	L	D3	$\overline{D3}$
H	L	L	L	D4	$\overline{D4}$
H	L	H	L	D5	$\overline{D5}$
H	H	L	L	D6	$\overline{D6}$
H	H	H	L	D7	$\overline{D7}$

H = High Level, L = Low Level, X = Don't Care

D0, D1 ... D7 = the level of the respective D input

The image contains three mechanical drawings of the J16A package:

- Top View:** Shows a rectangular package with 16 pins. Dimensions include a total width of $0.785 [19.94]$ MAX, a pin pitch of $0.037 \pm 0.005 [0.94 \pm 0.13]$ TYP, and a pin diameter of $0.020-0.030 [5.59-7.87]$. A corner radius is specified as $R 0.025 [0.64]$.
- Side View:** Shows the package height and pin dimensions. The maximum height is $0.200 [5.08]$ MAX TYP. The pin height is $0.125-0.200 [3.18-5.08]$ TYP. The pin diameter is $0.080 [2.03]$ MAX. The pin angle is $90^\circ \pm 4^\circ$ TYP. The pin spacing is $0.100 \pm 0.010 [2.54 \pm 0.25]$ TYP.
- Cross-sectional View:** Shows the internal structure, including the glass sealant and the package body. The glass sealant thickness is $0.010 \pm 0.002 [0.25 \pm 0.05]$ TYP. The package body thickness is $0.180 [4.57]$ MAX. The package body width is $0.290-0.320 [7.37-8.13]$. The package body angle is $95^\circ \pm 5^\circ$ TYP. The package body diameter is $0.310-0.410 [7.87-10.41]$.

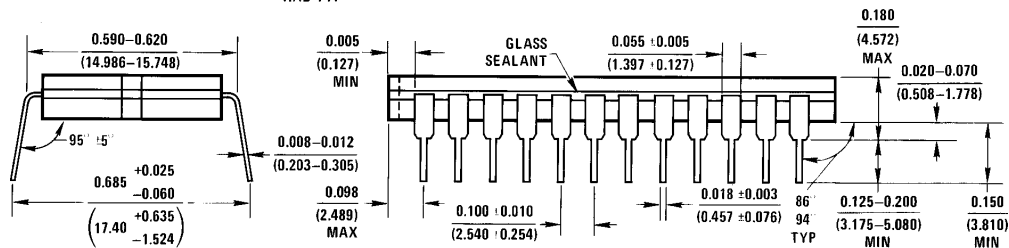
Technical drawing of a rectangular plate with rounded corners and a central hole. The drawing includes dimensions in inches and millimeters, and a list of 24 numbered points along the top and bottom edges.

Dimensions:

- Overall width: 1.290 (32.766) MAX
- Overall height: 0.600 (15.240) MAX GLASS
- Central hole radius: 0.025 (0.635) RAD
- Corner radius: 0.030-0.055 (0.762-1.397) RAD TYP
- Distance from bottom edge to hole: 0.514-0.526 (13.06-13.36)

Numbered Points:

- Top edge: 24, 23, 22, 21, 20, 19, 18, 17, 16, 15, 14, 13
- Bottom edge: 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12



J24A (REV H)

Physical Dimensions inches (millimeters) (Continued)

The image contains two sets of technical drawings for integrated circuit packages. The top set is for a 16-Lead Molded Dual-In-Line Package (N), showing top, side, and end views with dimensions in inches and millimeters. It includes options for pin 1 identification and lead angle. The bottom set is for a 24-Lead Molded Dual-In-Line Package (N), showing top, side, and end views with dimensions in inches and millimeters. It includes a note about dotted outlines reflecting an alternate molded body configuration.

16-Lead Molded Dual-In-Line Package (N)
Order Number DM74151AN
NS Package Number N16E

24-Lead Molded Dual-In-Line Package (N)
Order Number DM74150N
NS Package Number N24A

Physical Dimensions inches (millimeters) (Continued)

0.050-0.080
(1.270-2.032)

0.004-0.006
(0.102-0.152) TYP

0.007-0.018
(0.178-0.457) TYP

0.371-0.390
(9.423-9.906)

0.050 ± 0.005
(1.270 ± 0.127) TYP

0.000 MIN TYP

0.250-0.370
(6.350-9.398)

0.245-0.275
(6.223-6.985)

0.015-0.019
(0.381-0.482) TYP

0.026-0.040
(0.660-1.016) TYP

0.008-0.012
(0.203-0.305)

DETAIL A

16 15 14 13 12 11 10 9

2 3 4 5 6 7 8

PIN NO. 1
IDENT

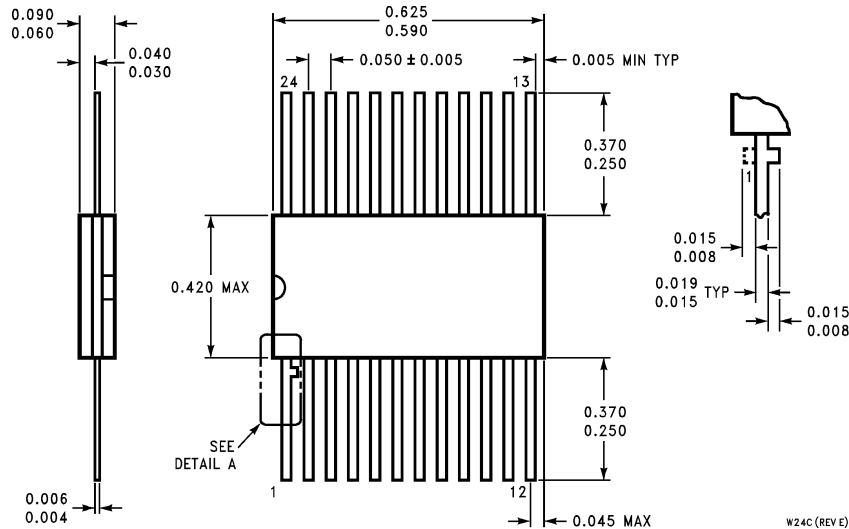
MAX GLASS

0.300
(7.620)

W16A (REV. H)

16-Lead Ceramic Flat Package (W)
Order Number 54151AFMQB or DM54151AW
NS Package Number W16A

16-Lead Ceramic Flat Package (W)
Order Number 54151AFMQB or DM54151AW
NS Package Number W16A

Physical Dimensions inches (millimeters) (Continued)

24-Lead Ceramic Flat Package (W)
Order Number 54150FMQB
NS Package Number W24C

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National Semiconductor Corporation
 1111 West Bardin Road
 Arlington, TX 76017
 Tel: 1(800) 272-9959
 Fax: 1(800) 737-7018

National Semiconductor Europe
 Fax: (+49) 0-180-530 85 86
 Email: cnjwge@tevm2.nsc.com
 Deutsch Tel: (+49) 0-180-530 85 85
 English Tel: (+49) 0-180-532 78 32
 Français Tel: (+49) 0-180-532 93 58
 Italiano Tel: (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd.
 19th Floor, Straight Block,
 Ocean Centre, 5 Canton Rd.
 Tsimshatsui, Kowloon
 Hong Kong
 Tel: (852) 2737-1600
 Fax: (852) 2736-9960

National Semiconductor Japan Ltd.
 Tel: 81-043-299-2309
 Fax: 81-043-299-2408

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Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.