TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# TA8050P

# **1.5A MOTOR DRIVER WITH BRAKE FUNCTION**

The TA8050P is a 1.5A motor driver which directly drives a bidirectional DC motor. Inputs DI1 and DI2 are combined to select one of forward, reverse, stop, and brake modes. Since the inputs are TTL-compatible, this IC can be controlled directly from a CPU or other control system. The IC also has various protective functions.

#### **FEATURES**

- Bidirectional DC motor driver
- Current capacity : 1.5A
- Four modes : Forward, Reverse, Stop, and Brake
- Protective functions : Thermal Shutdown, Short Circuit Protection, and Overvoltage Shutdown
- Built-in diode for counteracting counter electromotive force
- Plastic HSIP-7 pin

#### **BLOCK DIAGRAM AND PIN LAYOUT**



#### 961001FBA2

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Weight : 1.9g (Typ.)

#### **PIN DESCRIPTION**

PIN No.	SYMBOL	DESCRIPTION				
1	DI1	Dutput status control pin.				
2	DI2	Connects to a PNP-type voltage comparator.				
3	M (+)	Connects to the DC motor. Both the sink and the source have a current capacity of 1.5A. Diodes for absorbing counter electromotive force are contained on the $V_{CC}$ and GND sides.				
4	GND	Grounded				
5	M (-)	Connects to the DC motor together with pin 3 and has the same function as pin 3. This pin is controlled by the inputs from pins 1 and 2.				
6	(N.C)	Not connected				
7	V <sub>CC</sub>	Power supply pin. This pin has a function to turn off the output when the applied voltage exceeds 27.5V, thus protecting the IC and the load.				

#### TRUTH TABLE

Inp	out	Out		
DI1	DI2	M(+)	M (-)	
Н	Н	L	L	(Note)
L	Н	L	Н	
Н	L	Н	L	
L	L	OFF (high i	(Note)	

Note : Brake mode comes into effect when both M(+) and M(-) go low ; stop mode comes into effect when both M(+) and M(-) turn OFF.

#### DESCRIPTION OF MULTI-PROTECTIVE OPERATION

The TA8050P has functions for protection from overvoltage ( $V_{SD}$ ), overcurent ( $I_{SD}$ ), and overheat ( $T_{SD}$ ). These functions protect the IC (and the motor load in some cases) from deterioration or destruction due to power-related overstress.

The three functions work independently.

Each function is explained below.



- 1. Overvoltage protection (V<sub>SD</sub>)
  - Basic operation

When the voltage supplied to the  $V_{CC}$  pin is up to the  $V_{SD}$  detection voltage, the output is controlled by the input signals. However, when the  $V_{CC}$  voltage exceeds the detection voltage, the output enters high-impedance state regardless of the input signals.

• Detailed explanation

The V<sub>SD</sub> voltage is detected by comparing the Zener voltage with the voltage obtained by dividing  $V_{CC}$  with a resistor. When the center voltage of the resistor is higher than the Zener voltage, a transistor-off instruction is issued to the control logic. When it is lower than the Zener voltage, the logic is controlled by the input signals from pins 1 and 2.

- 2. Overheat protection (T<sub>SD</sub>)
  - Basic operation

When the junction (chip) temperature is up to the  $T_{SD}$  detection temperature, the output is controlled by the input signals. When it exceeds the  $T_{SD}$  detection temperature, the output enters high-impedance state regardless of the input signals.

• Detailed explanation

The temperature is detected by monitoring  $V_F$  of a diode on the chip. When the diode  $V_F$  is lower than the internal reference voltage, an output transistor-off instruction is issued to the control logic. When it is higher than the internal reference voltage, the logic is controlled by the input signals from pins 1 and 2.

- 3. Overcurrent protections (ISD)
  - Basic operation

When the output current (pin 3 or 5, I sink or I source) is up to the I<sub>SD</sub> detection current, the output is controlled by the input signals. When it exceeds the detection current, the output assumes a switching waveform as shown in Fig.1.



Fig.1 Basic Operation

• Detailed explanation

The output current is detected by monitoring the V<sub>BE</sub> from each output transistor. One detection circuit connects to one of the output transistors and leads to the short-circuit protection circuit. When a current exceeding the I<sub>SD</sub> detection current flows through one of the four output transistors, the short-circuit protection circuit is activated. This circuit contains a timer. When overcurrent condition continues for  $20\mu s$  (typically), the protection circuit places the output in high-impedance mode and,  $80\mu s$  (typically) later, returns the IC to ON mode. The switching-waveform output is repeated until overcurrent condition is no longer present.

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	Vcc	30	V	
supply voltage	Vcc	60 (1s)	v	
Input Voltage	VIN	-0.3 to V <sub>CC</sub>	V	
Output Current	IO.AVE	1.5	А	
Operation Temperature	T <sub>opr</sub>	– 40 to 110	°C	
Storage Temperature	T <sub>stg</sub>	– 55 to 150	°C	
Power Dissipation	PD	12.5	W	
Lead Temperature-time	T <sub>sol</sub>	260 (10s)	°C	

## **ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 6$ to 16V, $T_c = -40$ to 110°C)

CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	ICC1	Vcc	—	Stop	—	8	15	mA
Current Consumption	ICC2			Forward / Reverse	—	27	50	
	ICC3			Brake	—	16	30	
Input Voltage	VIL	DI1		—	—	—	0.8	v
mput voltage	VIH	/ DI2	—	—	2.0	—	—	
Input Current	ΙL	DI1	_	V <sub>IN</sub> = 0.4V	_	—	– 100	μΑ
Input Current	Чн	/ DI2		$V_{IN} = V_{CC}$		—	100	
Output Saturation	V <sub>sat</sub>	M(+)		I <sub>O</sub> = 1.5A, Tc = 25°C	—	2.2	2.9	v
Voltage	(total)	/M(-)	—	I <sub>O</sub> = 1.5A, Tc = 110°C	—	2.2	2.8	v
Output Leakage Current	ILEAK-U	M(+)		V <sub>O</sub> = 0V		—	– 100	
Output Leakage Current	ILEAK·L	/ M ( – )	—	V <sub>O</sub> = V <sub>CC</sub>	—	—	100	μΑ
Diode Forward Voltage	V <sub>F</sub> .U	M(+)	_	IF = 1.5A	_	2.6	_	v
Didde Forward Voltage	VF.L	/ M ( – )	_		—	1.5	_	
<b>Overcurrent Detection</b>	I <sub>SD</sub>			—	1.8	3	4	Α
Shutdown Temperature	T <sub>SD</sub>		—	—	—	150	—	°C
Overvoltage Detection	V <sub>SD</sub>	_	_	—	25	27.5	30	V
Thermal Resistance	R <sub><i>θ</i>j-c</sub>	_	_	—	_	4	—	°C/W
Transfer Delay Time	t <sub>pLH</sub>	_	—	—		1	10	
Transier Deidy Time	t <sub>pHL</sub>	_			_	1	10	$\mu$ s



#### I/O EQUIVALENT CIRCUIT



#### **EXAMPLE OF APPLICATION CIRCUIT**



st Connect this capacitor as close to the IC as possible.

OUTLINE DRAWING HSIP7-P-2.54

Unit : mm



Weight : 1.9g (Typ.)



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#### Как с нами связаться

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