Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RJK6014DPK

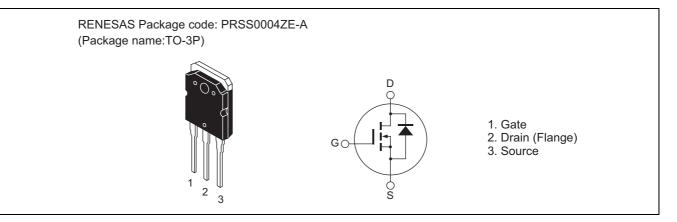
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G1517-0200 Rev.2.00 Jun 04, 2007

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	16	А
Drain peak current	Note1 I _{D (pulse)}	32	А
Body-drain diode reverse drain current	I _{DR}	16	А
Body-drain diode reverse drain peak current	Note1 I _{DR (pulse)}	32	А
Avalanche current	I _{AP} ^{Note3}	5	А
Avalanche energy	E _{AR} ^{Note3}	1.3	mJ
Channel dissipation	Pch ^{Note2}	150	W
Channel to case thermal impedance	θch-c	0.833	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at $Tc = 25^{\circ}C$

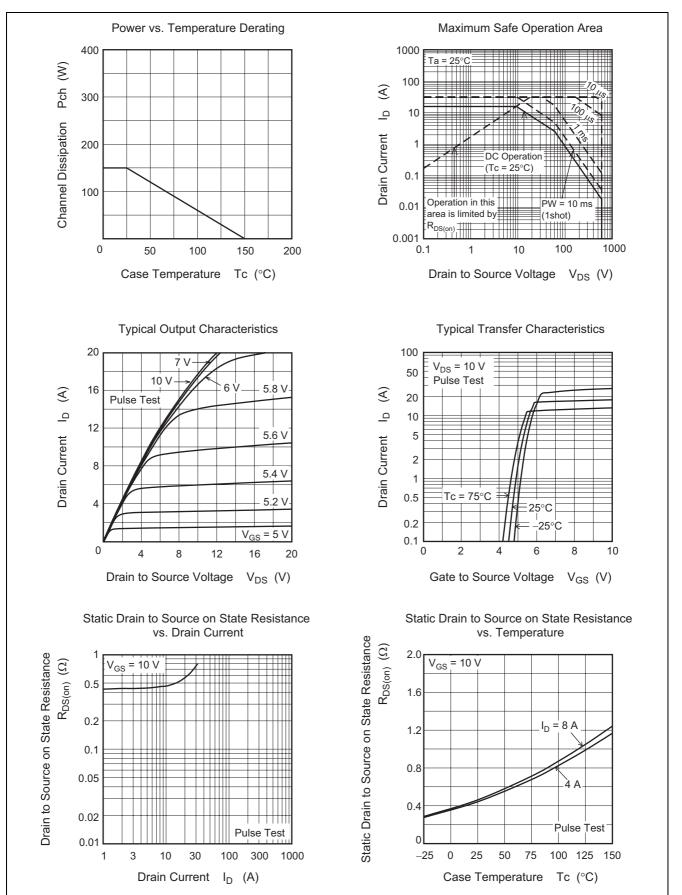
3. STch = 25° C, Tch $\leq 150^{\circ}$ C

Electrical Characteristics

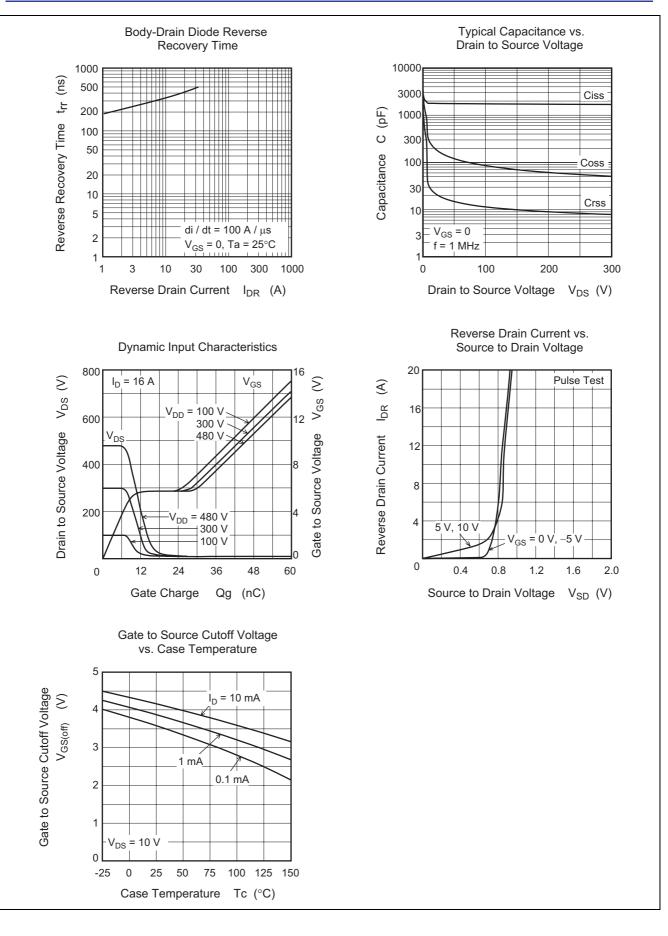
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	600	—		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	1	μΑ	$V_{DS} = 600 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30$ V, $V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}		0.475	0.575	Ω	$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	1800	_	pF	V _{DS} = 25 V
Output capacitance	Coss	_	170	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	20		pF	
Turn-on delay time	t _{d(on)}	_	36		ns	I _D = 8 A
Rise time	tr	_	29		ns	$V_{GS} = 10 V$ $R_L = 37.5 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t _{d(off)}	_	93		ns	
Fall time	t _f	_	20		ns	
Total gate charge	Qg	_	45		nC	V _{DD} = 480 V
Gate to source charge	Qgs	_	9	_	nC	V _{GS} = 10 V I _D = 16 A
Gate to drain charge	Qgd	_	20		nC	
Body-drain diode forward voltage	V _{DF}	_	0.91	1.50	V	$I_F = 16 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t _{rr}	_	390	—	ns	$I_F = 16 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

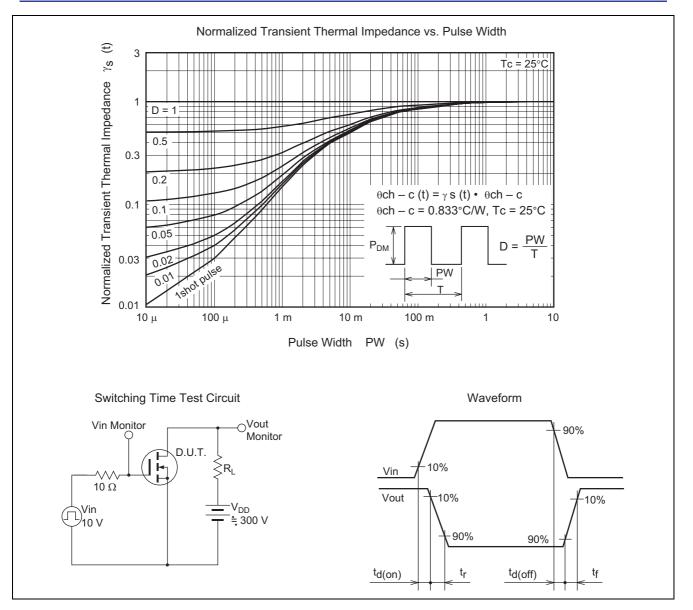
Main Characteristics



RENESAS



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Package Dimensions

Package Name TO-3P	JEITA Package Code SC-65	RENESAS Code	Previous Code	MASS[Typ.]	
10-3P	SC-65	PRSS0004ZE-A 15.6 ± 0.3	TO-3P/TO-3PV	5.0g 4.8 ± 0.2 1.5 2.8 0.6 ± 0.2	Unit: mm
	<u>5.45 ± 0</u>		<u>.0</u> <u>.0</u> <u>.5.45 ± 0.5</u>		

Ordering Information

Part No.	Quantity	Shipping Container
RJK6014DPK-00-T0	360 pcs	Box (Tube)

RenesasTechnology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan



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