TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

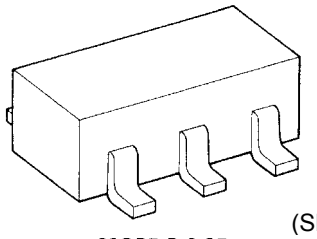
TC7SET17F, TC7SET17FU

Schmitt Buffer

Features

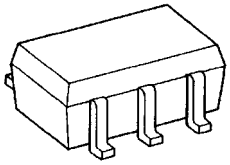
- High speed t_{pd} = 5.0 ns (typ.) at V_{CC} = 5 V, C_L = 15pF
- Low power dissipation I_{CC} = 2 μA (max) at Ta = 25°C
- Compatible with TTL outputs.
- 5.5V tolerant input.

TC7SET17F



SSOP5-P-0.95 (SMV)

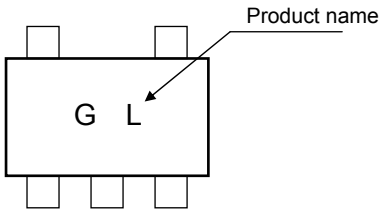
TC7SET17FU



SSOP5-P-0.65A (USV)

Weight
SSOP5-P-0.95 : 0.016 g (typ.)
SSOP5-P-0.65A : 0.006 g (typ.)

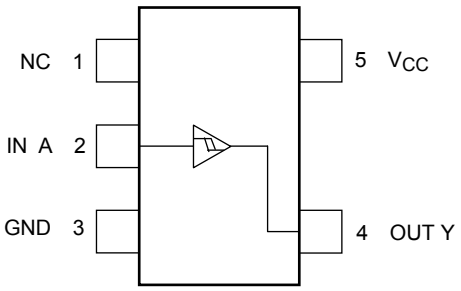
Marking



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	−0.5 to 7.0	V
DC input voltage	V_{IN}	−0.5 to 7.0	V
DC output voltage	V_{OUT}	−0.5 to V_{CC} + 0.5	V
Input diode current	I_{IK}	−20	mA
Output diode current	I_{OK}	±20 (Note 1)	mA
DC output current	I_{OUT}	±25	mA
DC V_{CC}/ground current	I_{CC}	±50	mA
Power dissipation	P_D	200	mW
Storage temperature	T_{stg}	−65 to 150	°C
Lead temperature (10 s)	T_L	260	°C

Pin Assignment (top view)

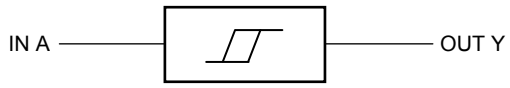


Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production
2004-02

IEC Logic Symbol



Truth Table

INPUT	OUTPUT
A	Y
L	L
H	H

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	4.5 to 5.5	V
Input voltage	V_{IN}	0 to 5.5	V
Output voltage	V_{OUT}	0 to V_{CC}	V
Operating temperature	T_{opr}	−40 to 85	°C

Electrical Characteristics
DC Characteristics

Characteristics	Symbol	Test Condition		V_{CC} (V)	Ta = 25°C			Ta = −40 to 85°C		Unit
					Min	Typ.	Max	Min	Max	
Positive Threshold Voltage	V_P	—		4.5	—	—	1.90	—	1.90	V
				5.5	—		2.10	—	2.10	
Negative Threshold Voltage	V_N	—		4.5	0.50	—	—	0.50	—	
				5.5	0.60	—	—	0.60	—	
Hysteresis Voltage	V_H	—		4.5	0.40	—	1.40	0.40	1.40	
				5.5	0.40	—	1.50	0.40	1.50	
High-level output voltage	V_{OH}	$V_{IN} = V_{IL}$	I_{OH} = −50 μA	4.5	4.4	4.5	—	4.4	—	V
			I_{OH} = −8 mA	4.5	3.94	—	—	3.80	—	
Low-level output voltage	V_{OL}	$V_{IN} = V_{IH}$	I_{OL} = 50 μA	4.5	—	0.0	0.10	—	0.10	
			I_{OL} = 8 mA	4.5	—	—	0.36	—	0.44	
Input leakage current	I_{IN}	V_{IN} = 5.5 V or GND		0 to 5.5	—	—	±0.1	—	±1.0	μA
Quiescent supply current	I_{CC}	$V_{IN} = V_{CC}$ or GND		5.5	—	—	2.0	—	20.0	μA
	I_{CCT}	Per Input :V_{IN} = 3.4 V Other Input :V_{CC} or GND		5.5	—	—	1.35	—	1.50	mA

AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
			V_{CC} (V)	C_L (pF)	Min	Typ.	Max	Min	Max	
Propagation delay time	t_{pLH} t_{pHL}		5.0 ± 0.5	15	—	5.0	7.6	1.0	9.0	ns
				50	—	6.5	10.8	1.0	12.4	
Input capacitance	C_{IN}				—	4	10	—	10	pF
Power dissipation capacitance	C_{PD}	(Note2)			—	18	—	—	—	pF

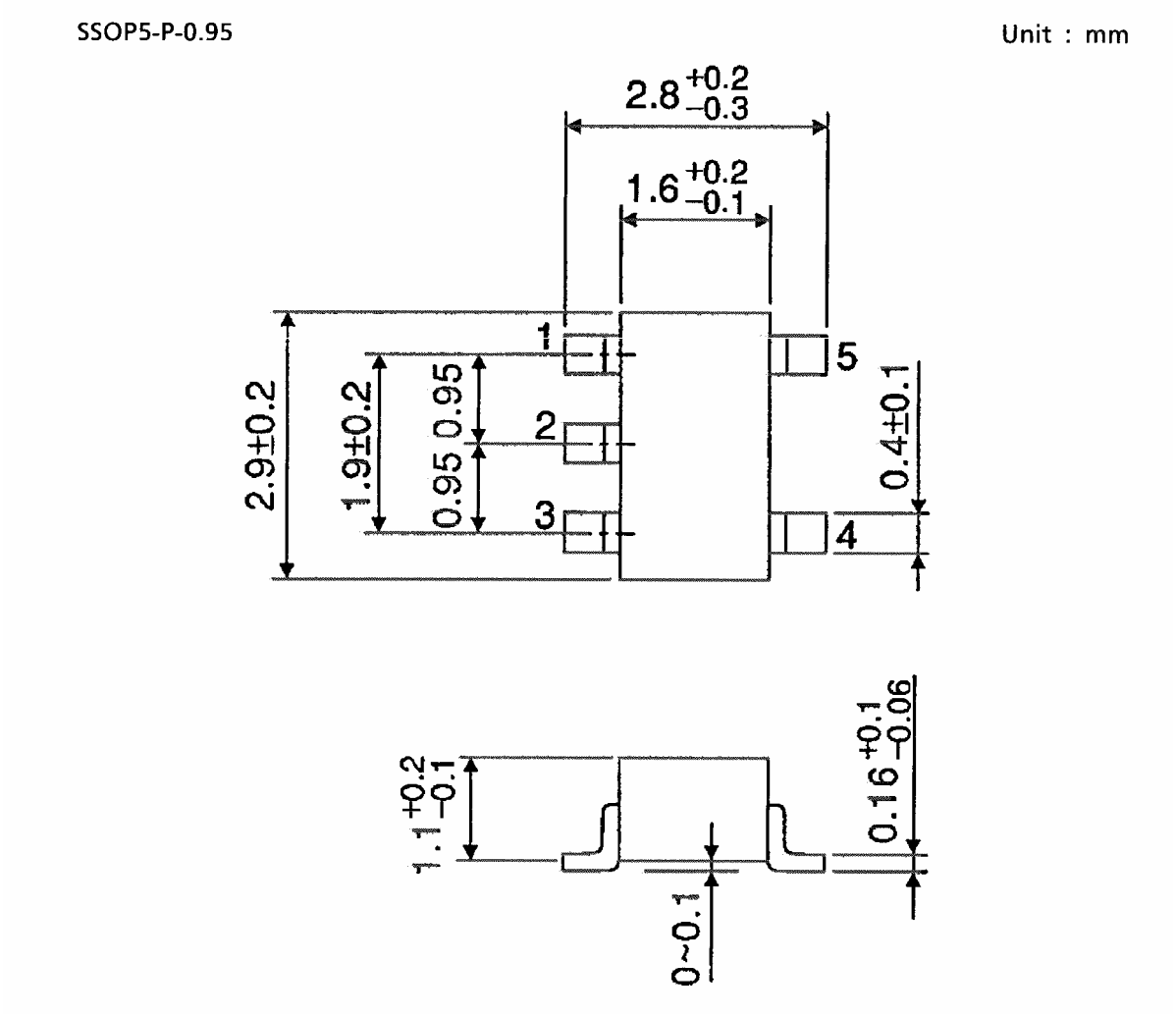
Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC\ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

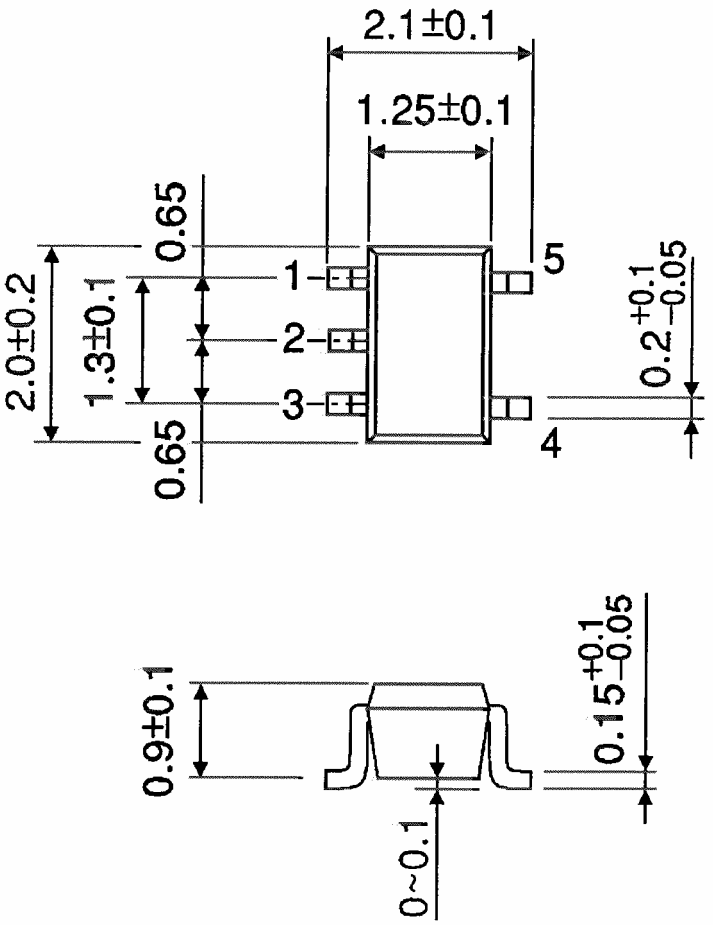
SSOP5-P-0.95 Unit : mm



Weight: 0.016 g (typ.)

Package Dimensions

SSOP5-P-0.65A Unit : mm



Weight: 0.006 g (typ.)

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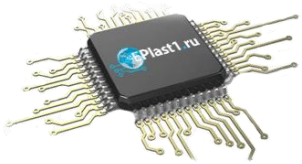
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)
Факс: 8 (812) 320-02-42
Электронная почта: org@eplast1.ru
Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.