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# LIQUID CRYSTAL DISPLAY MODULE MODEL: MTF-TQ35SP741-AV Customer's No.:



Microtips Technology Inc. 12F. No.31 Lane 169, Kang Ning St., His-Chih, Taipei Hsien, Taiwan FAX: 886-2-26958625

Approved and Checked by

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2008/01/16	2008/01/16	2008/01/16	2008/01/16
李剛	連俊傑	蔡宜夢	陳雅靖



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# **Revise Records**

Rev.	Date	Contents	Written	Approved
А	2007/11/22	Initial Release	Sherry Chen	Steele Lee
В	2007/12/03	See Note 1 and 2 below.	Sherry Chen	Steele Lee
С	2008/01/16	See Note 3	Sherry Chen	Steele Lee

# Special Notes

Note1.	Update Block Diagram(Page.7);
-	
Note2.	Update the relation of SPI interface between Sync mode and DE mode.
Note3.	Update DIMENSIONAL OUTLINES (Page 31)
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#### **GENERAL DESCRIPTION AND FEATURES** 1.

MTF-TQ35SP741-AV is a TM (Transmissive) type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT devices. This model is composed of a TFT-LCD Panel, driver IC, FPC , Touch panel and a back-light unit. The resolution of a 3.5" contains 320RGBx240 dots and can display up to 16.7M colors. The following table described the features of MTF-TQ35SP741-AV.

#### 1.1 Features

Support 24-bit data (RGB). \_

Item	Specification	Unit	Note
Screen Size	3.5″ diagonal	inch	-
Display Resolution	320 x RGB x 240	Dot	-
Dot Pitch	0.073 (W) x 0.219 (H)	mm	-
Active Area	70.08 (W) x 52.56 (H)	mm	-
Outline Dimension	77.8 (W) x 64.5 (H) x 4.12 (T),	122.122	
	Not including FPCB	mm	-
Display Mode	Normally white/Transmissive	-	-
Pixel Arrangement	RGB-Strip	-	-
Surface Treatment	Anti-glare (AG)	-	-
weight	128	g	-
Viewing Direction	6 o'clock	-	-
Input Interface	Digital 24-bits parallel RGB	-	-
Driver IC	Himax HX8238A	-	-

#### **General Specifications** 1.2



**Pixel Arrangement** 



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#### 2. **BLOCK DIAGRAM**

2.1 TFT-LCD Module (Interface System Structure) with Back Light Unit





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#### 2.2 LCM Driver IC Block





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# 3. INPUT TERMINAL PIN ASSIGNMENT

## 3.1 CN1 Pin Assignment (LCD)

Pin No.	Symbol	I/O	Function	Remark
1	LED K1	I	Backlight LED Ground	
2	LED K2	I	Backlight LED Ground	
3	LED A1	I	Backlight LED Power (10.2V/20mA)	
4	LED A2	I	Backlight LED Power (10.2V/20mA)	
5	N/C	-	Not Connection	
6	/REST	I	Hardware Reset	
7	N/C		Not Connection	
8	N/C or Y1 (Top)	I	No connection (for MTF-TQ35SN741-AV) or Y1 (Top) (for MTF-TQ35SP741-AV)	
9	N/C or X1 (Right)	I	No connection (for MTF-TQ35SN741-AV) or X1 (Right) (for MTF-TQ35SP741-AV)	
10	N/C or Y2 (Bottom)	I	No connection (for MTF-TQ35SN741-AV) or Y2 (Bottom) (for MTF-TQ35SP741-AV)	
11	N/C or X2 (Left)	I	No connection (for MTF-TQ35SN741-AV) or X2 (Left) (for MTF-TQ35SP741-AV)	
12	BO	I	Blue Data Bit 0	
13	B1	I	Blue Data Bit 1	
14	B2	I	Blue Data Bit 2	
15	B3	I	Blue Data Bit 3	
16	B4	Ι	Blue Data Bit 4	
17	B5	I	Blue Data Bit 5	
18	B6	I	Blue Data Bit 6	
19	Β7	Ι	Blue Data Bit 7	
20	G0	I	Green Data Bit0	
21	G1	I	Green Data Bit1	
22	G2	I	Green Data Bit2	
23	G3	I	Green Data Bit3	
24	G4	I	Green Data Bit4	
25	G5	I	Green Data Bit5	
26	G6	I	Green Data Bit6	
27	G7	I	Green Data Bit7	



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28	RO	I	Red Data Bit0	
29	R1	I	Red Data Bit1	
30	R2	I	Red Data Bit2	
31	R3	I	Red Data Bit3	
32	R4	I	Red Data Bit4	
33	R5	I	Red Data Bit5	
34	R6	I	Red Data Bit6	
35	R7	1	Red Data Bit7	
36	H <sub>SYNC</sub>	I	Horizontal Sync Input	Note 2
37	V <sub>SYNC</sub>	I	Vertical Sync Input	Note 2
38	D <sub>CLK</sub>	I	Dot Data Clock	
39	N/C	-	Not Connection	
40	N/C	-	Not Connection	
41	V <sub>CC</sub>	I	Digital Power	3.3V
42	V <sub>CC</sub>	I	Digital Power	3.3V
42	CCD		SPI Interface Data En	
43	CSB	I	Leave it open when not used!	Note 1
44	N/C	-	Not Connection	
45	N/C	-	Not Connection	
46	N/C	-	Not Connection	
47	N/C	-	Not Connection	
48	SDO		SPI Interface Data output	Note 1
40	300		Leave it open when not used!	
49	SP <sub>CLK</sub>	1	SPI Interface Data Clock	Note 1
			Leave it open when not used!	
50	SDI	I	SPI Interface Data input	Note 1
<b></b>			Leave it open when not used!	
51	N/C	-	Not Connection	
52	DEN		Data Enable Input	Note 3
53	GND		Ground	
54	GND	I	Ground	

Note 1: SPI Interface is only to set up the initial code in LCM driver IC register.

Note 2: There had been default initial code stored in LCD driver IC at Sync Mode operation, and if customer needs to revise the default initial code to change gamma or Vcom voltage, then SPI interface is needed.

Note 3: Different from Sync mode, there is no default initial code in driver IC in DE mode, so initial code has to be setup via SPI interface at DE mode.



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#### 3.2 Touch Panel Pin Assignment

Pin No.	Designation
1	Ү1 (Тор)
2	X1 (RIGHT)
3	Y2 (BOTTOM)
4	X2 (LEFT)

# 4. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (1).

ltem		Symbol	Condition	Min	Туре	Max	Unit	Note
Response	Rise	T <sub>R</sub>			15	30	ms	
time	Fall	Τ <sub>F</sub>			35	50	ms	
Brightness		L		160	200	-	$cd/m^2$	
Contrast ratio		CR		200	300	-	-	All left side
	Pod	R <sub>x</sub>	Viewing	1	0.591	-		data are based
	Red	R <sub>Y</sub>	normal	1	0.373	-	-	on CMO's following condition-T6 NTSC:60% LC:5091 Light: C light (Machine:BM5A)
	Green	G <sub>x</sub>	Angle θx=θy=0°	-	0.331	-		
Color		G <sub>Y</sub>		-	0.599	-		
Chromaticity	Blue	B <sub>x</sub>		-	0.134	-		
	Diue	B <sub>Y</sub>			0.171	-		
	White	Wx		1	0.295	-		Normal Polarizer without DBEF
	vvnite	Wy		-	0.311	-	-	Simulation Data
	Hor.	$\theta_{X^+}$		50	60	-		Reference only
Viewing	1101.	θ <sub>x-</sub>	Center	50	60	-	Degree	
Angle	Ver.	$\theta_{Y^{+}}$	CR≥10	40	50	-	Degree	
	vei.	θ <sub>γ-</sub>		50	60	-		

Note 1: Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression. Contrast Ratio (CR)= L63/L0 L63: Luminance of gray level 63 L0: Luminance of gray level 0 CR=CR (10) CR(X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5)



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Note 2: Definition of Response Time  $(T_R T_F)$ :



Note 3: Definition of Viewing Angle





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- (4) Measurement Set-Up:
  - The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature Change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



(5) Measurement Set-Up:





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#### 5. ABSOLUTE MAXIMUM RATINGS

#### 5.1 Absolute Ratings of Environment

If the operating condition exceeds the following absolute maximum ratings, the TFT LCD module may be damaged permanently.

(Ta=25(2°C, VSS=GND							
Item	Symbol	Min.	Max.	Unit	Note		
Storage temperature	TSTG	-30	80	°C	(1)		
Operating temperature (Ambient temperature)	TOPR	-20	70	°C	(1), (2)		

Note (1) 95 % RH Max. (  $40 \text{ °C} \ge \text{Ta}$  )

Maximum wet-bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.



(2) In case of below 0°, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of LC's character

5.2	Maximum Ratings (Voltage Referenced to VS	S)
-----	---	----

ltem	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VCC	VSS=0	-0.3	6.0	V	
Input voltage	V <sub>-in</sub> -		-0.3	VCC+0.3	V	Note 1

Note1:Hsync, Vsync, DEN, DCLK, R0~R7, G0~G7, B0~B7



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# 6. ELECTRICAL CHARACTERISTICS

#### 6.1 DC Electrical Characteristics

ltem		Symbol		Unit			
		Symbol	Min.	Тур.	Max.	Unit	
Power supply		VCC	3.0	3.3	3.6	V	
Input Voltage for logic	H Level	V <sub>-IH</sub> -	0.7 VCC	-	VCC	V	Note 1
	L Level	V. <sub>II</sub> .	0	-	0.3 VCC	V	NOLE I
Power Supply current		ICC	-	34.36	_	mA	Note 2

Note1: Hsync, Vsync, DEN, DCLK, R0~R7, G0~G7, B0~B7

Note2: fV =60Hz , Ta=25 $^\circ\!\mathrm{C}$  , Display pattern : All Black



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# 7. AC CHARACTERISTICS

# 7.1 Pixel timing

(Unless otherwise specified, Voltage Referenced to V\_{SS}, V\_{CCIO}=3.3V, Ta=25°C)



PARAMETER	Currele el	Sumh al		Тур.		Max.		Unit
PAKAMETEK	Symbol	24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	Unit
DOTCLK Frequency	fDOTCLK			6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-	-	ns
Vertical Sync Setup Time	tvsys	20	10		I	1	I	ns
Vertical Sync Hold Time	tvsyyh	20	10		1	1	1	ns
Horizontal Sync Setup Time	thsys	20	10		-	-	-	ns
Horizontal Sync Hold Time	thsyh	20	10		-	-	-	ns
Phase difference of Sync Signal Falling Edge	thv	1		-		240		tDOTCLK
DOTCLK Low Period	tCKL	50	15	-	-	-	-	ns
DOTCLK High Period	tCKH	50	15	-		-	-	ns
Data Setup Time	tds	12	10	-	-	-	-	ns
Data hold Time	tdh	12	10	-		-	-	ns
Reset Pulse Width	tRES	1	0	-		-	-	us

Note: External clock source must be provided to DOTCLK pin of HX8238-A. The driver will not operate if absent of the clocking signal.



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# 7.2 Data transaction timing in parallel RGB (24 bit) interface (SYNC mode)







<li>b) vertical</li>	Data I	ransaction	Liming	

Data transaction timing in parallel RGE	3 (24 bit) interface (SYNC mode)
---	----------------------------------

PARAMETER	Symbol	Min.		Тур.		Max.		Unit	
	Symbol	24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	Unit	
DOTCLK Frequency	fDOTCLK	ł	-	6.5	19.5	10	30	MHz	
DOTCLK Period	tDOTCL	100	33.3	154	51.3		I	Ns	
Horizontal Frequency (Line)	fH	-	-	14	.9	22.	35	KHz	
Vertical Frequency (Refresh)	fV		-	6	0 90		0	Hz	
Horizontal Back Porch	tHBP		-	68	204	-	-	tDOTCLK	
Horizontal Front Porch	tHFP		-	20	60		-	tDOTCLK	
Horizontal Data Start Point	tHBP		-	68	204	-	-	tDOTCLK	
Horizontal Blanking Period	tHBP+tHFP		-	88	264		-	tDOTCLK	
Horizontal Display Area	HDISP		-	320	960	-	1	tDOTCLK	
Horizontal Cycle	Hcycle			408	1224	450	1350	tDOTCLK	



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#### 7.3 Signal timing in DE mode





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#### 8. **BACKLIGHT SPECIFICATIONS**

#### 8.1 Absolute Maximum Ratings

			Ta = 25°C
Parameter	Symbol	Maximun Rating	Units
Peak Forward Current	I <sub>FM</sub>	40	mA
Reverse Voltage	V <sub>R</sub>	20	V
Power Dissipation	Pd	456	mW
Operating Temperature	T <sub>OPR</sub>	-20~+70	°C
Storage Temperature	T <sub>stg</sub>	-30~+80	°C

#### 8.2 Electrical/Operating Characteristics

Ta = 25°C

Parameter	Symbol	Min.	Тур.	Max.	Units	Test Condition
Forward Voltage(VLED1\VLED2)	V <sub>F</sub>	9.0	10.2	11.4	V	Ta=25℃
LED (1+2) Current	Ι <sub>L</sub>	-	20+20		mA	IF=40mA
Uniformity	-	75		-	%	IF=40mA
Chromaticity Coordinates	Х	0.27	0.30	0.32	-	
Chromaticity Coordinates	Y	0.27	0.31	0.33	-	

\*Uniformity = (Min./Max.) x 100%

#### Electrical Circuit of Backlight 8.3





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# 9. BASIC DISPLAY COLOR AND GRAY SCALE

											Ir	put	Сс	lor (	Dat	a									
					R€	ed							Gre	een							BI	ue			
	Color	MS	SB					Ľ	SB	1	VISB					LSE	3	MSB LSB							
		R7	R6	R5	R4	R3	R2	R1	RO	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	В5	B4	B3	B2	B1	BO
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	ĭ	1	1	1	ĭ	1	1	ŏ	ŏ	ŏ	ŏ	õ	ŏ	ŏ	ŏ	õ	ŏ	ŏ	ŏ	ŏ	ŏ	ŏ	õ
	Green(255)	0	0	0	0	0	0	0	0	ĭ	1	1	1	1	1	ĭ	ĭ	õ	ŏ	ŏ	ŏ	ŏ	ŏ	ŏ	õ
Basic	Blue(255)	Ő	ŏ	õ	õ	õ	õ	õ	ŏ	0	0	0	0	0	o	o	0	1	ĭ	ĭ	ĭ	ĭ	ĭ	1	1
Colors	Cyan	0	õ	0	0	õ	ŏ	ŏ	ŏ	ĭ	ĭ	1	1	1	ĭ	ĭ	ĩ	1	1	1	1	1	1	1	1
001010	Magenta	1	1	1	1	1	ĭ	1	1	0	0	ò	ò	0	0	o	ò	1	ì	i	1	1	1	1	1
	Yellow	1	1	i	1	1	i	i	1	ĩ	1	1	1	1	1	ĩ	1	0	0	0	0	0	0	0	ò
	White	1	í	1	1	1	i	1	1	1	1	1	1	1	1	1	1	ĩ	ĩ	ĩ	ĩ	1	1	1	1
	Red(0) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	Ő	Ő	0	ŏ	õ	õ	ŏ	1	ŏ	ŏ	õ	õ	õ	ŏ	ŏ	ŏ	Ő	õ	õ	õ	ŏ	ŏ	ŏ	õ
	Red(2)	0	ŏ	0	ŏ	õ	õ	1	0	ŏ	ŏ	õ	õ	õ	ŏ	ŏ	ŏ	õ	õ	ŏ	ŏ	ŏ	ŏ	ŏ	õ
Red			Ĭ.				Ĭ.		:	:	:	:	:	,	:		:			Ĭ.	Ĩ.	:	:	:	
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	õ	õ	õ	õ	õ	õ	õ	õ	0	Ő	õ	õ	Ő	õ	õ	0
	Red (255) Bright	1	1	1	1	1	1	1	1	õ	õ	õ	õ	õ	ŏ	ŏ	ŏ	0	õ	õ	õ	õ	õ	õ	0
	Green(0) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	õ	0	0	0	0	0	0	0	Õ	0	0	0	Õ	õ	1	0	0	0	0	0	0	0	0
	Green(2)	õ	õ	0	0	0	Õ	0	0	õ	õ	õ	0	0	õ	1	Ó	0	0	0	0	0	0	0	0
Green	:	:	:	;	;	;	;	;	;	:	:	:	:	:	:	:	:	:	:	:	:	;	;	;	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)Bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(0) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255) Bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



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# **10. QUALITY STANDARD FOR LCD**

### 10.1 Objective

This specification book is the standard for LCD module general inspection. And also this book will be re fer to customer approval specification.

### 10.2 Scope

This specification book is applicable to general LCD module. If supplier has any doubt or requirement, then it can be discussed.

### 10.2.1 Acceptable Quality Level

Inspection	Sampling Procedures	A.Q.L
Major	MIL-STD-105E Inspection Level II Normal Inspection Single sample inspection	1
Minor	MIL-STD-105E Inspection Level II Normal Inspection Single sample inspection	1.5

### Major defect :

A major defect is a defect that could result in failure or extremely reduction on the usability of the product for its intended purpose.

Minor defect :

A minor defect is one that does not materially reduce the usability of the product for its intended purple or is a departure from established standards giving no significant bearing on the effective use or operation of the unit.

10.2.2 Inspection Conditions

10.2.2.1 The environmental conditions for inspection shall be as follows

- Room Temperature : 25±10°C
- Humidity Temperature : 45±20%RH
- 10.2.3 The external visual inspection
  - The inspection shall be performed by using 40Watts fluorescent lamp for illumination and the distance between LCD and eyes of the inspector shall be 30cm or more.



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### 10.2.4 Inspection Item

Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder. The phenomenon dose not change with voltage.
Contrast variation	The color of a small area is different from the remainder. The phenomenon change with voltage.
Glass defect	Glass crack, Chip
Operating	Function, Contrast, Uniformity, Components

# 10.2.5 Definition of the Area



A area: Viewing Area B area: Out of Viewing Area



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## 10.3 Inspection specification

10.3.1 Non-operating inspection specification

Class of					Ac	ceptab	e Q'ty	
defects	No.	Inspection Item	C	riteria of defects	Zone	A	Zone B	
Major	1	Circuits	1. Ci	rcuit short	0		0	
,			2. Ci	rcuit open				
		Black spot, White spot,	А	$\phi \le 0.1$	*			
		Bright spot, Foreign particle	В	$0.1 < \phi < 0.2$	3			
		$\frown$	С	$0.2 \le \phi < 0.3$	1		Ignore	
	2	b	D	$0.3 \le \phi$	0			
		$\leftarrow$	Total	defect point (B,C)	3			
		$\phi = (a+b)/2$		ject when 5 or m thin 5mm circle.	iore spo	ots are	gathered	
		Black line, White line	А	$W \le 0.02$	-	*		
			В	$0.02 < W \le 0.05$	L ≤ 5	2		
			С	$0.05 < W \le 0.1$	$L \le 3$	2	Ignore	
	3		D	0.1 < W	-	0		
		W	٦	Total defect point (B,	,C)	3		
				ject when 5 or m thin 5mm circle.	iore spo	ots are	gathered	
Minor		Contrast variation	А	$\phi \le 0.3$	Igno	ore		
			В	$0.3 < \phi \le 0.4$	2			
	4	b	С	$0.4 < \phi \leq 0.5$	1		Ignore	
		$  \longleftrightarrow   \longleftrightarrow  $	D	$0.5 < \phi$	0			
		$a \\ \phi = (a+b)/2$	Total	defect point (B,C)	3			
		Pattern deformity	1. Pir	n hole				
			А	<i>ф</i> ≤0.15	Igno	ore		
		a ⊆	В	0.15< <i>φ</i> ≤0.2	2 (*	°)	Ignore	
				$0.2 < \phi$	0			
	5	$ \bigcirc \textcircled{b} \\ \hline $	* Two	o pin hole shall not f	ormed i	n the si	ngle dot	
			2. Ex	cess, void				
		$\phi = (a+b)/2$	А	a≤0.2and b≤0.2	Igno	ore	lan cur	
			В	0.2 < a or 0.2 < b	0		lgnore	



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	1						
			А	Bright dot	N≤2		
			В	Dark dot	N≤3	Ignore	
	6	Dot defect	С	Total defect dot	N≤4		
			* This LCI	s inspection item do D	es not apply t	o B/W	
			А	<i>¢</i> ≤0.3	Ignore		
	7	Bubble between Polarizer and panel	В	$0.3 < \phi \le 0.5$	2	Ignore	
		'	С	0.5< Ø	0		
	8	Polarizer scratch and particle	Circu No.2	lar : Same as inspec	tion item	Ignore	
	0		Linea No.3	r : Same as inspectio	on item	ignore	
			А	$\phi \leq 0.2$	Ignore		
		Polarizer Dent	В	$0.2 < \phi \le 0.3$	2	Ignore	
	9		С	$0.3 < \phi \le 0.4$	1	ignore	
Minor			D	$0.4 < \phi$	0		
_			Total	defect point (B,C)	3		
	10	Bubble in the Cell	Any size 0 0				
	11	Dirt on polarizer	Dirt which can be wiped easily accepted.			should be	
	12	Protection film	-	pped up to should not			
	13	Polarizer shift	<ol> <li>Shifting in position should not exceed the glass outline dimension.</li> <li>Incomplete covering of the viewing area due to shifting is not allowed.</li> <li>Shifting in position should be within the tolerance (refer to module dimensiona drawing)</li> </ol>				
	14	Silicon	1. Sil 2. Sil	<ol> <li>Silicon must cover all circuits.</li> <li>Silicon thickness should be within s (refer to module dimensional drawi</li> </ol>			
	15	Таре		cation: refer to spec oufficient adhesive.	ification.		
Major	16	TCP, FPC defect	Film	or Pattern should no	ot have crack.		
iviajui	17	Components	Missi	ng components not	allowed.		
* Condition of		•	1				

\* Condition of item 2~9

1. Distance between defects must be more than 10mm with light on, more than 15mm with light off.

2. Total acceptable defect number

- Defects with light on : 6 points



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Class of defects	No.	Inspection Item	Criteria of defects
	1	No display	-
	2	Abnormal operation	
	3	Contrast defect	Judge according to module specification. Establish boundary sample if required.
4 Viewing angle defect Judge acc Establish B	Judge according to module specification. Establish boundary sample if required.		
Major	5	Excess power consumption	Judge according to module specification.
	6	Back-light, LED defect	<ol> <li>No lit-on</li> <li>Different color</li> <li>Low brightness</li> </ol>
	7	Speaker, Vibrator defect	<ol> <li>No operation</li> <li>Abnormal operation</li> </ol>
	8	Cross-talk defect	No noticeable crosstalk. Establish boundary sample if required.
	9	Uneven brightness	No noticeable unevenness allowed. Establish boundary sample if required.
Minor	10	Uneven color	No noticeable unevenness allowed. Establish boundary sample if required.
	11	Spot, Pinhole, Foreign particle, Line	Same as in Chapter 7.1



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# **11. RELIABILITY CONDITION**

- 11.1 LCM Reliability Test
  - 11.1.1 Reliability Test Condition

No.	TFT	ltem	Condition	Test time	Note
1	V	High temp. operating	70°C	240 Hrs	-
2	V	Low temp. operating	-20°C	240 Hrs	-
3	V	High temp. storage	80°C	240 Hrs	-
4	V	Low temp. storage	-30°C	240 Hrs	
5	V	High Temp / High Humidity Storage	T = 60°C /90%. For (But no condensation dew)	240 Hrs	-
6	V	High Temp/ High Humidity Operating	T = 40°C /90% For (But no condensation dew)	240 Hrs	-
7	V	Thermal Shock	$-30^{\circ}C \rightarrow +25^{\circ}C \rightarrow +80^{\circ}C$ ,50 cycle 30min 5min 30min		-

11.1.2 Operating Test Pattern

No.	ltems	Test Pattern
		1. Full Red
		2. Full Green
		3. Full Blue
		4. Gradation (horizontal)
		5. Gradation (vertical)
		6. Character (111111)
1	Test Pattern in Driving Condition	7. Full White
		8. Full Black
		9. Black Line (horizontal)
		10. Black Line (vertical)
		11. Mosaic (1X1)
		The Test Pattern is changed 1sec.
		The same Pattern are repeated.
2	Black Square	Black Window and White Background



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### 11.1.3 Test Method

The method of visual inspection is equal to the appearance standard. Evaluation and assessment made two hours after return to room temperature (25  $\pm$  5°C). The LCDs subjected to the test must not have dew condensation.

The test pattern is gray scale and the operating voltage sweep from Vth to Vsat variable. The non-uniformity and other appearance are checked in LCD.

11.1.4 Result Evaluation Criteria

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

### 11.1.5 Life time

Life time expectancy of LCD Panel is approximately 50,000 hours under the room environment. D efinition on the termination of life time is deterioration of contrast ratio by one fifth against initial value.

11.1.6 Basic rule for Reliability test

- \* Place all the samples under room temperature & humidity for 24 hours after reliability stressing.
- \* Room environment means 25+/-10°C, 45+/-20%RH
- \* There should be no condensation during the test.
- \* One LCD module shall be used for one test item only and once.

11.1.7 Judgment Criteria for reliability test No. 1-2

- \* Contrast (or Brightness) ratio variation is within 50% of the initial value.
- \* No abnormal function
- \* No extreme decay on appearance

11.1.8 Life time

Display (LCD module) : Life time expectancy of LCD Panel is approximately 50,000 hours under the room environment.

No.	Items	Min.	Тур.	Max.	Unit	Remark
1	Activation Force	100	130	150	g	<ol> <li>within active area.</li> <li>R8.0mm polyacetal pen or finger.</li> </ol>
2	Surface Hardness	3			Н	Judgment ref. JIS-K5600
3	Durability (Writing Life)	100,000	-		characters	<ol> <li>within active area.</li> <li>R0.8mm polyacetal pen.</li> <li>Load: 150g</li> <li>Speed: 60mm/sec</li> </ol>
4	Durability (Hitting Life)	1,000,000	-	-	touches	<ol> <li>within active area.</li> <li>R0.8mm polyacetal pen.</li> <li>Load: 250g</li> <li>Frequency: 3 times/sec</li> </ol>

### 11.2 Touch panel Reliability



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# **12. PRECAUTIONS**

12.1 Operation

Burn-in sometimes happens when the same character was displayed at along time. Therefore, to prevent Burn-in, it is recommended to set up a Screen-saver function.

12.2 Safety

The liquid crystal in the LCD is poisonous, DO NOT put it in your mouth. If the liquid crystal to uches your skin or clothes, wash it off immediately using soap and water.

12.3 Handling





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			2,		it covering gla d) to keep app	from external ss (acrylic board or propriate space	
				damage to j	e change. Con polarizer or ele fter fading col	on at sudden densation makes ectrical contacted ndensation, smear	
	*			i. Property of semiconductor devices may be affected when they are exposed to light possibly resulting in malfunctioning of the ICs. To prevent such malfunctioning of the ICs, your design and mounting layout done are so that the IC is not exposed to light in actual use.			
	2		000		exposure caus t may not reco	ses degradation of over	
		22	2	corrosion. I. When it is n turned off o changed by same patter brightness c	ot in use, the r the pattern r a screen save n for a long pe	nust be frequently rr. If it displays the eriod of time, icking may develop	5
	le le	60	2° 7	circumstanc users assem disassemblii	es. If unqualifies ble the produ	ot function or its	



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# 12.4 Static electricity

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge.

CONTRACTOR	a.	The LCD module shall be installed flat, without twisting or bending. Ground soldering iron tips, tools and testers when they operate.
	b.	Ground your body when handling the products.
	C.	DO NOT apply voltage to the input terminal without applying power supply.
	d.	DO NOT apply voltage that exceeds the absolute maximum rating.
	e.	Store the products in an anti-electrostatic container.
	f.	Peel off protect tape, attached to polarizer, slowly to minimize ESD damage.

12.5 Storage



Store the products in a dark place at  $+5 \sim +25$ degree C, low humidity (50%RH or less). DO NOT store the products in an atmosphere containing organic solvents or corrosive gases.

12.6 Cleaning



12.7 Waste



When dispose of LCD module, manage it at the production waste according to the relevant laws and regulations.



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# 13. WARRANTY

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1 We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- 2 We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3 We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4 We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product. Microtips-origin longer than one year from Microtips production.

# **14. DIMENSIONAL OUTLINES**

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Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



### Как с нами связаться

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