



# PSMN4R2-60PL

N-channel 60 V, 3.9 mΩ logic level MOSFET in SOT78

5 February 2013

Product data sheet

## 1. General description

Logic level N-channel MOSFET in SOT78 using TrenchMOS technology. Product design and manufacture has been optimized for use in battery operated power tools.

## 2. Features and benefits

- High efficiency due to low switching & conduction losses
- Robust construction for demanding applications
- Logic level gate

## 3. Applications

- Battery-powered tools
- Load switching
- Motor control
- Uninterruptible power supplies

## 4. Quick reference data

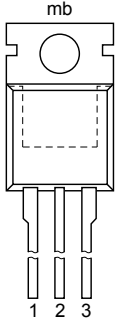
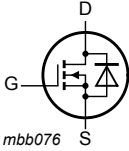
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>J</sub> ≥ 25 °C; T <sub>J</sub> ≤ 175 °C		-	-	60	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C; <a href="#">Fig. 1</a>	[1]	-	-	130	A
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <a href="#">Fig. 2</a>		-	-	263	W
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>J</sub> = 25 °C; <a href="#">Fig. 11</a>		-	3.17	3.9	mΩ
Dynamic characteristics							
Q <sub>G(tot)</sub>	total gate charge	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; V <sub>DS</sub> = 48 V;		-	151	-	nC
Q <sub>GD</sub>	gate-drain charge	<a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>		-	27	-	nC
Avalanche ruggedness							
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	I <sub>D</sub> = 130 A; V <sub>sup</sub> ≤ 60 V; R <sub>GS</sub> = 50 Ω; V <sub>GS</sub> = 10 V; T <sub>J(init)</sub> = 25 °C; unclamped; <a href="#">Fig. 3</a>		-	-	283	mJ

[1] Continuous current is limited by package.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 TO-220AB (SOT78)	 mbb076
2	D	drain		
3	S	source		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN4R2-60PL	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

7. Marking

Table 4. Marking codes

Type number	Marking code
PSMN4R2-60PL	PSMN4R2-60PL

8. Limiting values

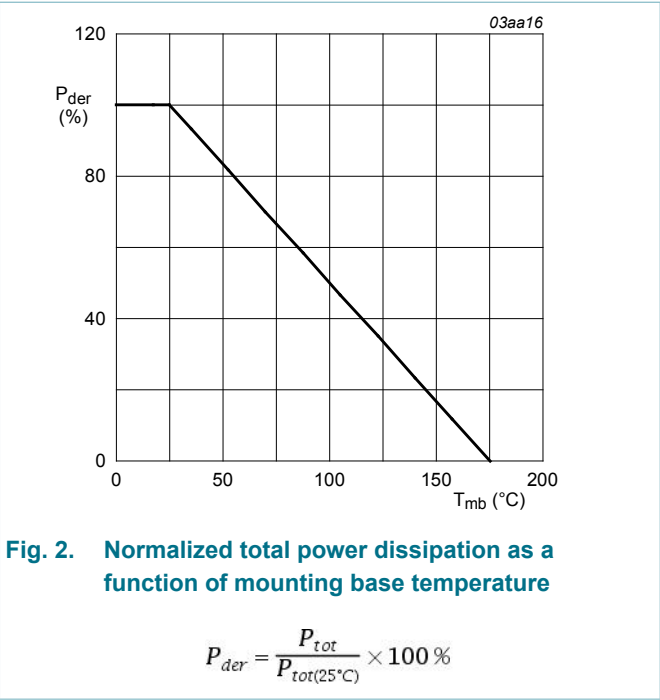
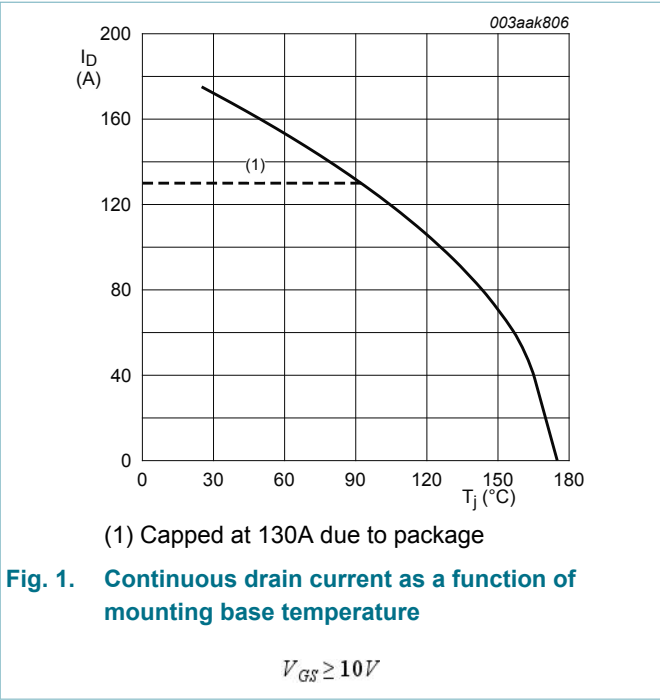
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{DS}$	drain-source voltage	$T_j \geq 25\text{ °C}$ ; $T_j \leq 175\text{ °C}$		-	60	V
$V_{DGR}$	drain-gate voltage	$R_{GS} = 20\text{ k}\Omega$		-	60	V
$V_{GS}$	gate-source voltage			-20	20	V
$I_D$	drain current	$T_{mb} = 25\text{ °C}$ ; $V_{GS} = 10\text{ V}$ ; Fig. 1	[1]	-	130	A
		$T_{mb} = 100\text{ °C}$ ; $V_{GS} = 10\text{ V}$ ; Fig. 1		-	124	A
$I_{DM}$	peak drain current	$T_{mb} = 25\text{ °C}$ ; pulsed; $t_p \leq 10\text{ }\mu\text{s}$ ; Fig. 4		-	701	A

Symbol	Parameter	Conditions		Min	Max	Unit
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <a href="#">Fig. 2</a>		-	263	W
T <sub>stg</sub>	storage temperature			-55	175	°C
T <sub>j</sub>	junction temperature			-55	175	°C
T <sub>sld(M)</sub>	peak soldering temperature			-	260	°C
Source-drain diode						
I <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C	<a href="#">[1]</a>	-	130	A
I <sub>SM</sub>	peak source current	pulsed; t <sub>p</sub> ≤ 10 μs; T <sub>mb</sub> = 25 °C		-	701	A
Avalanche ruggedness						
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	I <sub>D</sub> = 130 A; V <sub>sup</sub> ≤ 60 V; R <sub>GS</sub> = 50 Ω; V <sub>GS</sub> = 10 V; T <sub>j(init)</sub> = 25 °C; unclamped; <a href="#">Fig. 3</a>		-	283	mJ

[1] Continuous current is limited by package.



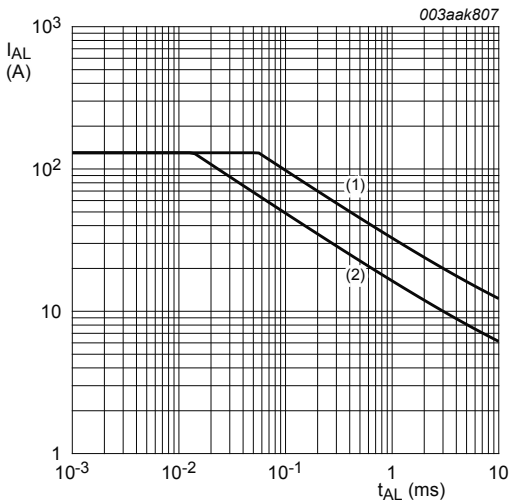


Fig. 3. Avalanche rating; avalanche current as a function of avalanche time

(1)  $T_{j(jnt)} = 25^{\circ}\text{C}$ ; (2)  $T_{j(jnt)} = 100^{\circ}\text{C}$

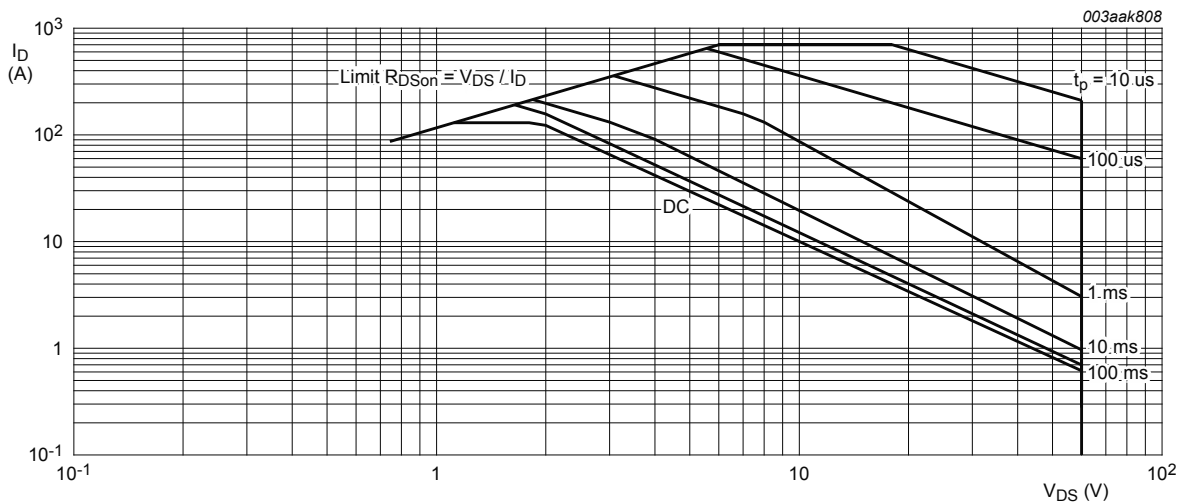


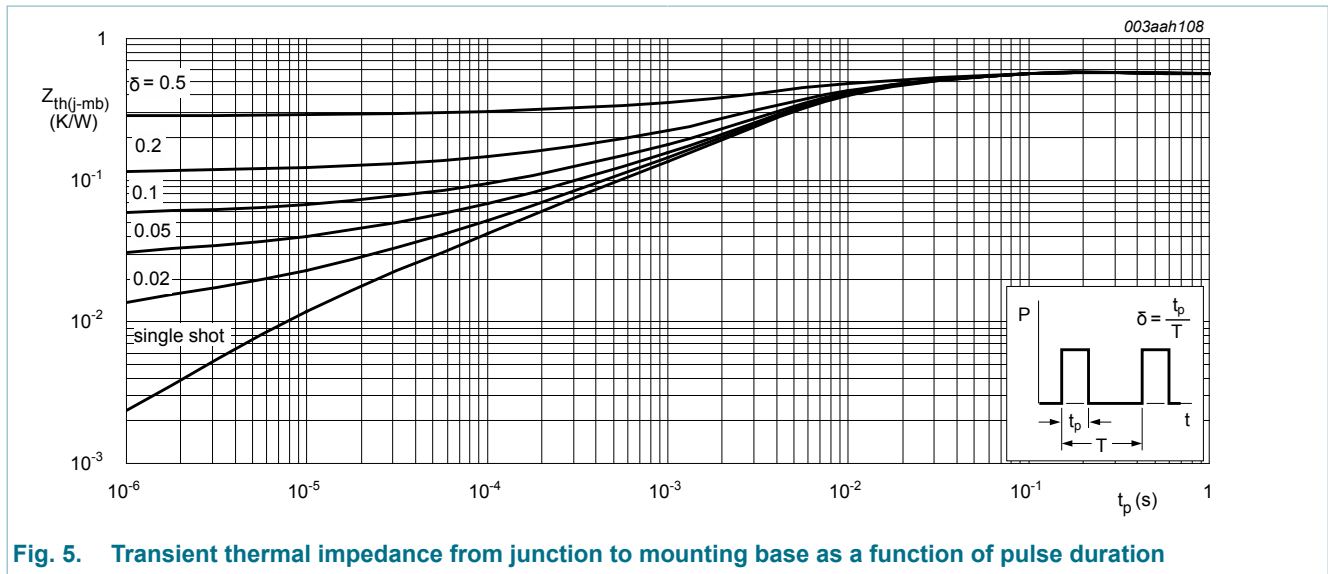
Fig. 4. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$T_{mb} = 25^{\circ}\text{C}$ ;  $I_{DM}$  is a single pulse

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 5	-	0.49	0.57	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	vertical in still air	-	60	-	K/W

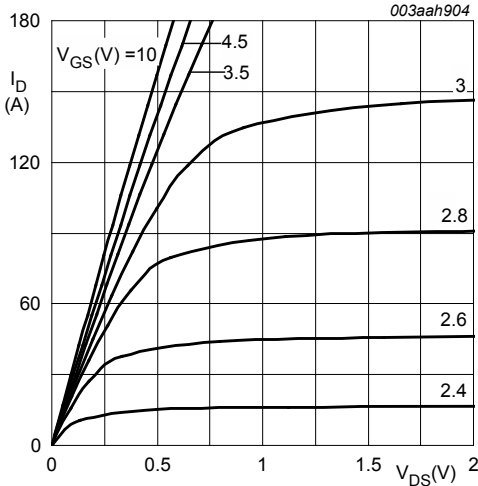


## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A$ ; $V_{GS} = 0 V$ ; $T_J = 25 ^\circ C$	60	-	-	V
		$I_D = 250 \mu A$ ; $V_{GS} = 0 V$ ; $T_J = -55 ^\circ C$	54	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 mA$ ; $V_{DS} = V_{GS}$ ; $T_J = 25 ^\circ C$ ; <a href="#">Fig. 9</a> ; <a href="#">Fig. 10</a>	1.4	1.7	2.1	V
		$I_D = 1 mA$ ; $V_{DS} = V_{GS}$ ; $T_J = -55 ^\circ C$ ; <a href="#">Fig. 9</a>	-	-	2.45	V
		$I_D = 1 mA$ ; $V_{DS} = V_{GS}$ ; $T_J = 175 ^\circ C$ ; <a href="#">Fig. 9</a>	0.5	-	-	V
$I_{DSS}$	drain leakage current	$V_{DS} = 60 V$ ; $V_{GS} = 0 V$ ; $T_J = 25 ^\circ C$	-	0.06	1	$\mu A$
		$V_{DS} = 60 V$ ; $V_{GS} = 0 V$ ; $T_J = 175 ^\circ C$	-	-	500	$\mu A$
$I_{GSS}$	gate leakage current	$V_{GS} = 16 V$ ; $V_{DS} = 0 V$ ; $T_J = 25 ^\circ C$	-	2	100	nA
		$V_{GS} = -16 V$ ; $V_{DS} = 0 V$ ; $T_J = 25 ^\circ C$	-	2	100	nA
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 4.5 V$ ; $I_D = 25 A$ ; $T_J = 25 ^\circ C$ ; <a href="#">Fig. 11</a>	-	3.6	4.3	mΩ
		$V_{GS} = 10 V$ ; $I_D = 25 A$ ; $T_J = 25 ^\circ C$ ; <a href="#">Fig. 11</a>	-	3.17	3.9	mΩ
		$V_{GS} = 10 V$ ; $I_D = 25 A$ ; $T_J = 175 ^\circ C$ ; <a href="#">Fig. 12</a> ; <a href="#">Fig. 11</a>	-	-	8.6	mΩ
$R_G$	gate resistance	$f = 1 MHz$	0.35	0.7	1.4	Ω

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Dynamic characteristics							
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 25 A; V <sub>DS</sub> = 48 V; V <sub>GS</sub> = 10 V; <a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>		-	151	-	nC
		I <sub>D</sub> = 25 A; V <sub>DS</sub> = 48 V; V <sub>GS</sub> = 5 V; <a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>		-	72	-	nC
Q <sub>GS</sub>	gate-source charge	I <sub>D</sub> = 25 A; V <sub>DS</sub> = 48 V; V <sub>GS</sub> = 10 V; <a href="#">Fig. 13</a> ; <a href="#">Fig. 14</a>		-	20	-	nC
Q <sub>GD</sub>	gate-drain charge			-	27	-	nC
C <sub>iss</sub>	input capacitance	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 25 V; f = 1 MHz; T <sub>j</sub> = 25 °C; <a href="#">Fig. 15</a>		-	8533	-	pF
C <sub>oss</sub>	output capacitance			-	703	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	357	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 45 V; R <sub>L</sub> = 1.8 Ω; V <sub>GS</sub> = 5 V; R <sub>G(ext)</sub> = 5 Ω		-	47	-	ns
t <sub>r</sub>	rise time			-	97	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	84	-	ns
t <sub>f</sub>	fall time			-	73	-	ns
Source-drain diode							
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 25 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C; <a href="#">Fig. 16</a>		-	0.8	1.2	V
t <sub>rr</sub>	reverse recovery time	I <sub>S</sub> = 20 A; dI <sub>S</sub> /dt = -100 A/μs; V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 25 V		-	40	-	ns
Q <sub>r</sub>	recovered charge			-	59	-	nC



T<sub>j</sub> = 25 °C; t<sub>p</sub> = 300 μs

Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

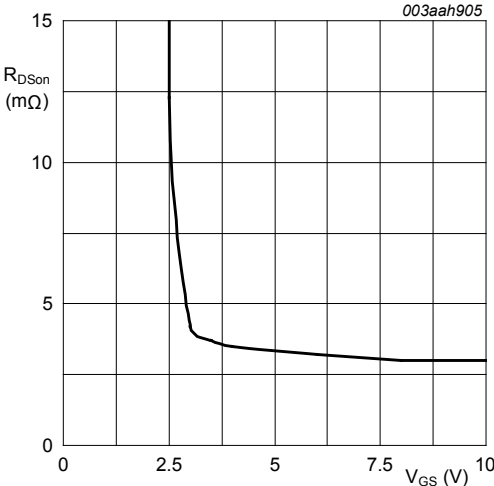


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

T<sub>j</sub> = 25 °C; I<sub>D</sub> = 25 A

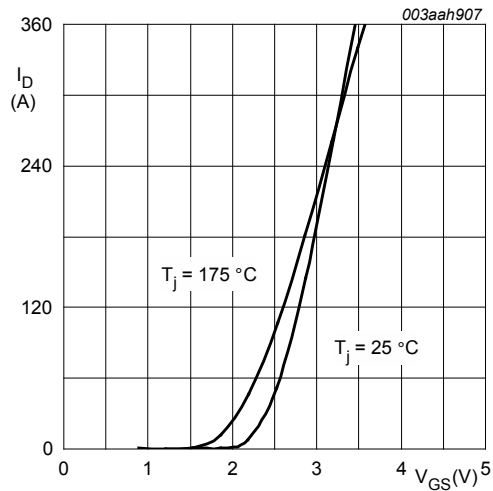


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

$V_{DS} = 10V$

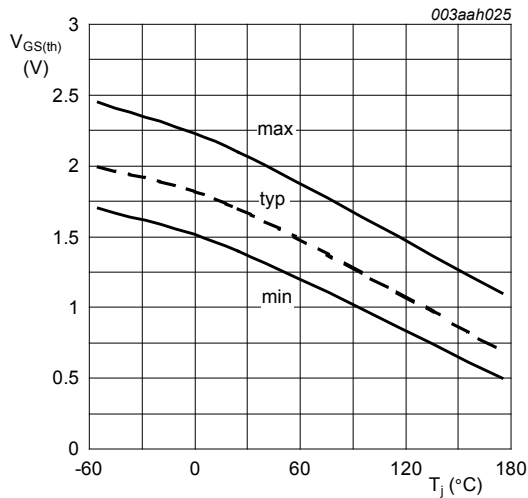


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$I_D = 1\text{ mA}; V_{DS} = V_{GS}$

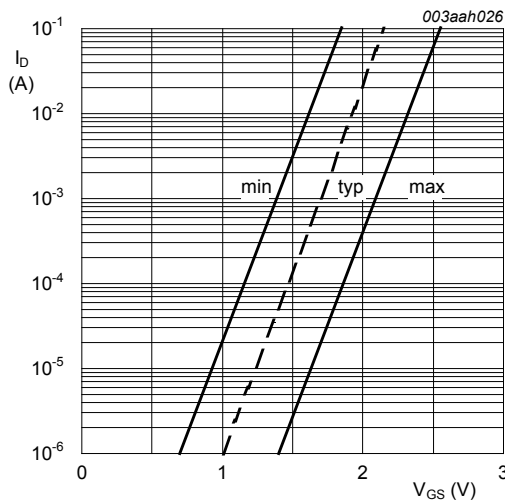


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$T_J = 25^\circ\text{C}; V_{DS} = 5V$

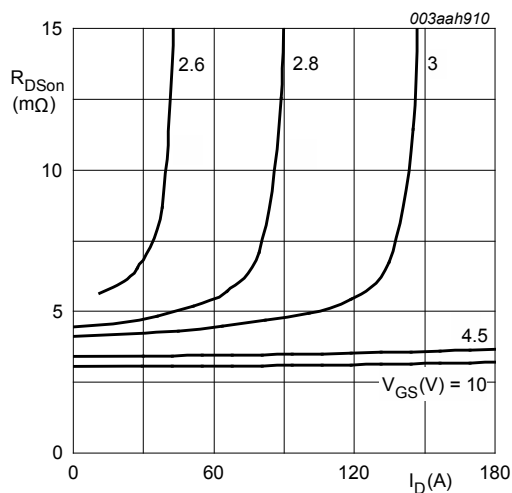


Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

$T_J = 25^\circ\text{C}; t_p = 300\text{ }\mu\text{s}$

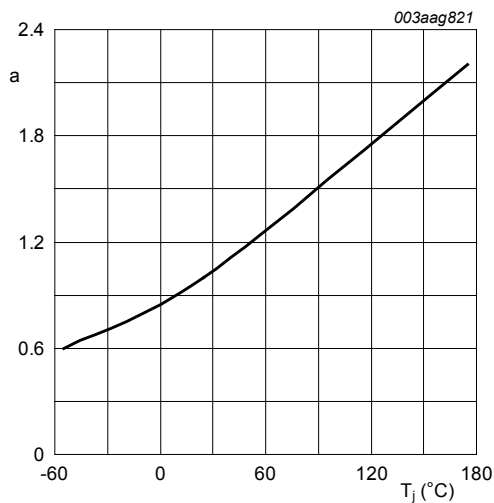


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon(25\text{ °C})}}$$

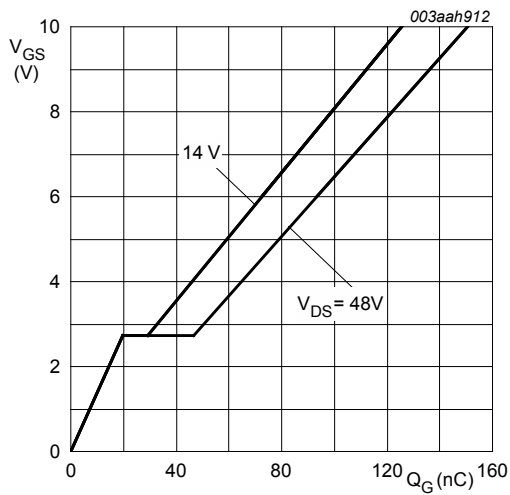


Fig. 14. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25\text{ °C}; I_D = 25\text{ A}$$

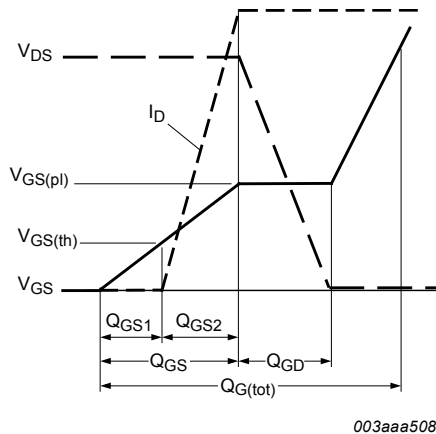


Fig. 13. Gate charge waveform definitions

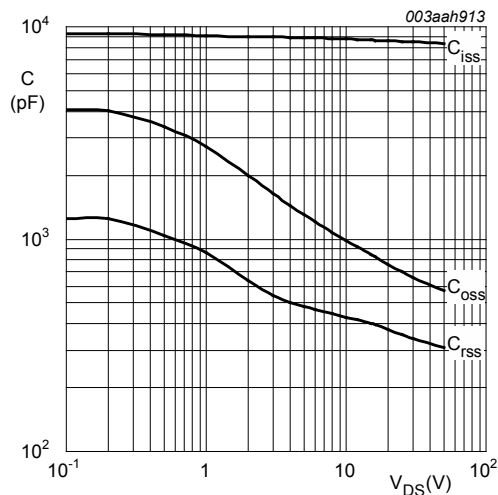


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = 0\text{ V}; f = 1\text{ MHz}$$



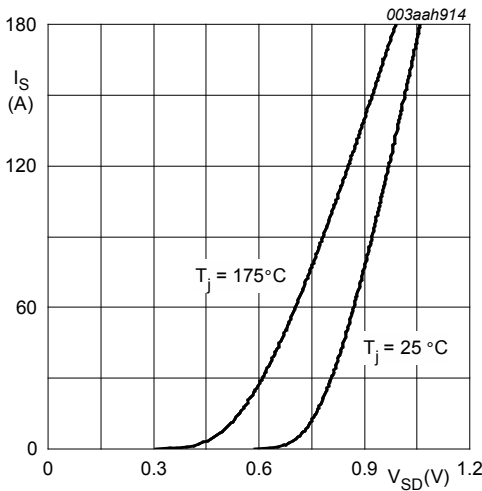


Fig. 16. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

$V_{GS} = 0V$

11. Package outline

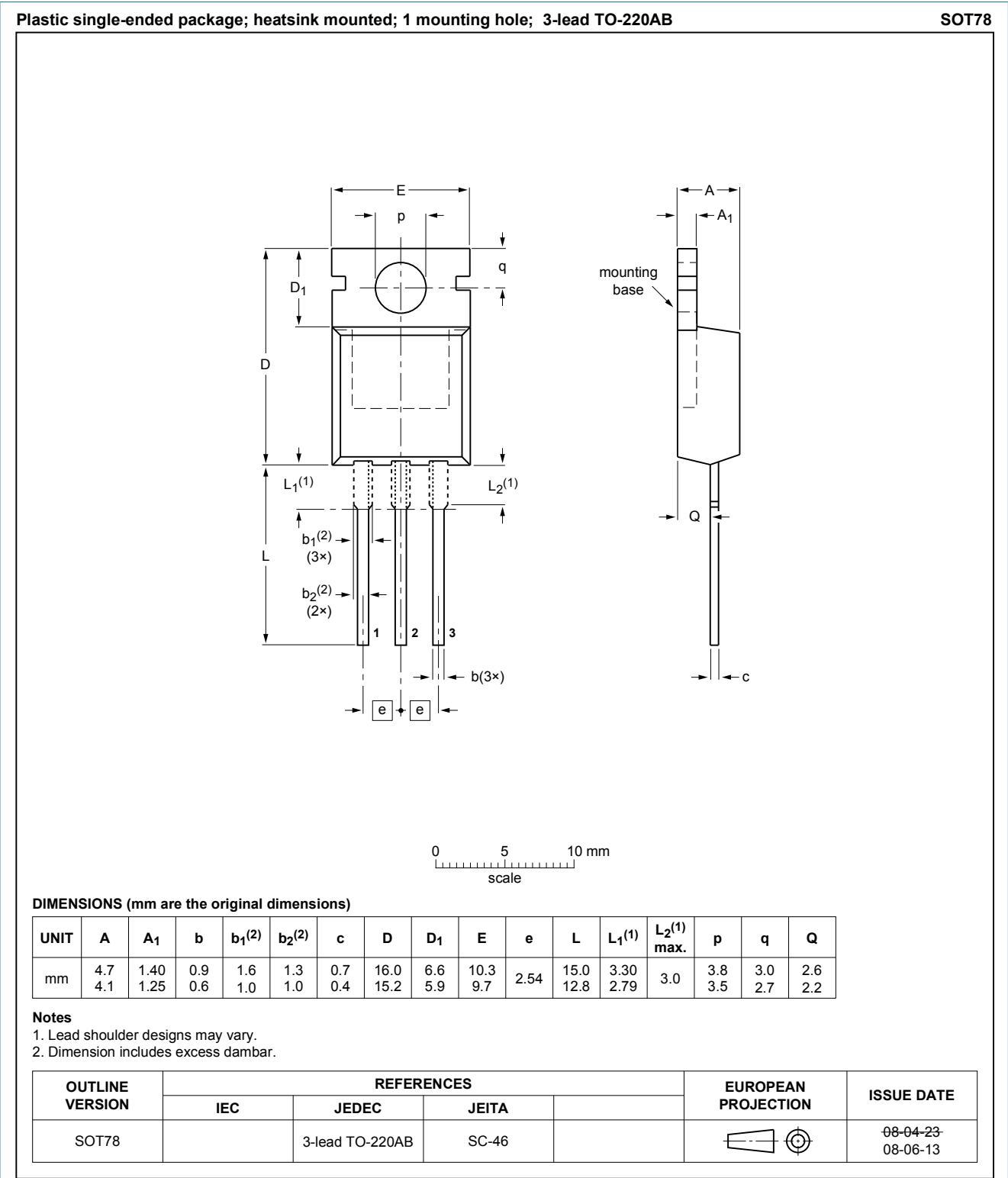


Fig. 17. Package outline TO-220AB (SOT78)

## 12. Legal information

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Document status [1][2]	Product status [3]	Definition
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Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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