

# EMRB85B-32.768K

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## REGULATORY COMPLIANCE (Data Sheet downloaded on Jun 12, 2020)


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## ITEM DESCRIPTION

MEMS Clock Oscillators LVCMOS (CMOS) 3.0Vdc 4 Pad 0.8mm x 1.5mm Chip Scale Package (CSP) 32.768KHz  $\pm 75$ ppm over  $-10^{\circ}\text{C}$  to  $+70^{\circ}\text{C}$

## ELECTRICAL SPECIFICATIONS

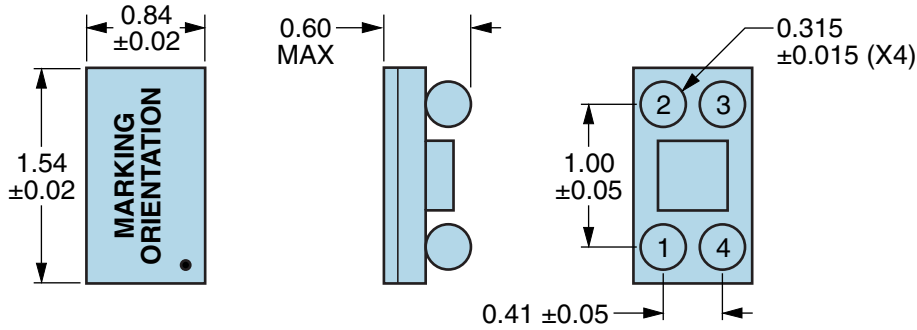
Nominal Frequency	32.768KHz
Frequency Tolerance/Stability	$\pm 75$ ppm Maximum over $-10^{\circ}\text{C}$ to $+70^{\circ}\text{C}$ (Inclusive of all conditions: Calibration Tolerance at $25^{\circ}\text{C}$ , Frequency Stability over the Operating Temperature Range, Supply Voltage Change, and Output Load Change)
Frequency Tolerance	$\pm 20$ ppm Maximum (Measured at $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$ , at $V_{\text{dd}}=3.0\text{Vdc}$ , Post Reflow, with board level underfill)
Aging at $25^{\circ}\text{C}$	$\pm 1$ ppm Maximum First Year
Supply Voltage	3.0Vdc $\pm 10\%$
Core Operating Current	0.9 $\mu\text{A}$ Typical (at $25^{\circ}\text{C}$ ), 1.3 $\mu\text{A}$ Maximum
Output Stage Operating Current	0.065 $\mu\text{A}/\text{Vpp}$ Typical, 0.125 $\mu\text{A}/\text{Vpp}$ Maximum
Input Current	1.1 $\mu\text{A}$ Typical (at $25^{\circ}\text{C}$ ), 1.7 $\mu\text{A}$ Maximum (No Load, Nominal Vdd)
Output Voltage Logic High (Voh)	90% of Vdd Minimum (IOH = -10 $\mu\text{A}$ )
Output Voltage Logic Low (Vol)	10% of Vdd Maximum (IOL = +10 $\mu\text{A}$ )
Rise/Fall Time	100nSec Typical, 200nSec Maximum (Measured from 10% to 90% of waveform)
Duty Cycle	50 $\pm 2$ (%) (Measured at 50% of waveform)
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Period Jitter (RMS)	35nSec Typical (Measured at $25^{\circ}\text{C}$ )
Power Supply Ramp	100mSec Maximum (Measured at 0Vdc to 90% of Vdd)
Start Up Time	180mSec Typical, 300mSec Maximum (at $25^{\circ}\text{C}$ ) 450mSec Maximum (over Operating Temperature Range) (Measured at Nominal Vdd)
Storage Temperature Range	$-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$

## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	JESD22-A114, HBM, 3000V
Flammability	UL94-V0
Mechanical Shock	MIL-STD-883, Method 2002, Condition E, 10,000G
Moisture Sensitivity	J-STD-020, MSL 1
Solderability	MIL-STD-883, Method 2003
Temperature Cycling	JESD22-A104, Condition G
Vibration	MIL-STD-883, Method 2007, Condition C, 70G

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### MECHANICAL DIMENSIONS (all dimensions in millimeters)

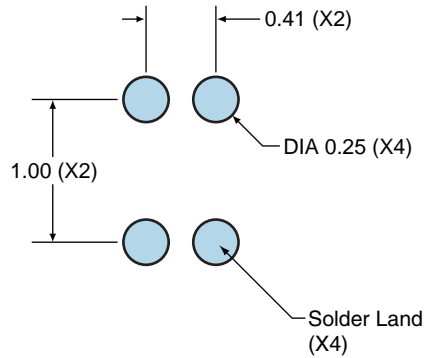


PIN	CONNECTION
1	Ground
2	Output
3	Supply Voltage
4	Ground

LINE	MARKING
1	<b>XX</b> XX=Ecliptek Manufacturing Identifier
2	<b>XXX</b> XXX=Ecliptek Manufacturing Identifier (continued)

### Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are  $\pm 0.1$

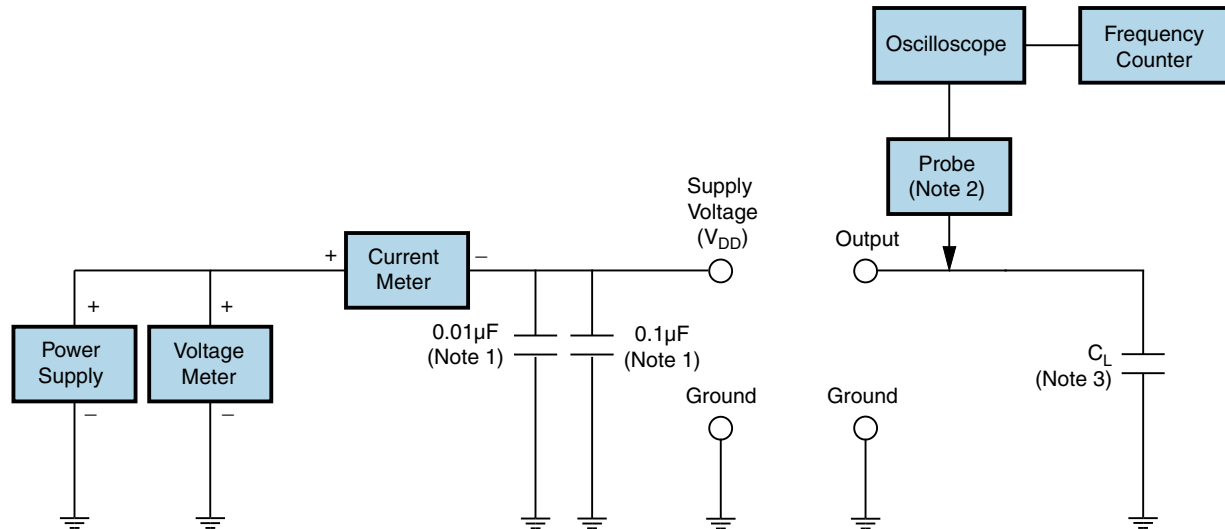
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## OUTPUT WAVEFORM



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## Test Circuit for CMOS Output



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is recommended.

Note 2: A low input capacitance (<12pF), 10X Attenuation Factor, High Impedance (>10Mohms), and High bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance. See applicable specification sheet for 'Load Drive Capability'.

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## Recommended Solder Reflow Methods



### High Temperature Infrared/Convection

<b><math>T_s</math> MAX to <math>T_L</math> (Ramp-up Rate)</b>	3°C/Second Maximum
<b>Preheat</b>	
- Temperature Minimum ( $T_s$ MIN)	150°C
- Temperature Typical ( $T_s$ TYP)	175°C
- Temperature Maximum ( $T_s$ MAX)	200°C
- Time ( $t_s$ MIN)	60 - 180 Seconds
<b>Ramp-up Rate (<math>T_L</math> to <math>T_P</math>)</b>	3°C/Second Maximum
<b>Time Maintained Above:</b>	
- Temperature ( $T_L$ )	217°C
- Time ( $t_L$ )	60 - 150 Seconds
<b>Peak Temperature (<math>T_P</math>)</b>	260°C Maximum for 10 Seconds Maximum
<b>Target Peak Temperature (<math>T_P</math> Target)</b>	250°C +0/-5°C
<b>Time within 5°C of actual peak (<math>t_p</math>)</b>	20 - 40 Seconds
<b>Ramp-down Rate</b>	6°C/Second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	8 Minutes Maximum
<b>Moisture Sensitivity Level</b>	Level 1
<b>Additional Notes</b>	Temperature shown are applied to body of device.

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## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

Ts MAX to TL (Ramp-up Rate)	5°C/Second Maximum
<b>Preheat</b>	
- Temperature Minimum (Ts MIN)	N/A
- Temperature Typical (Ts TYP)	150°C
- Temperature Maximum (Ts MAX)	N/A
- Time (ts MIN)	60 - 120 Seconds
<b>Ramp-up Rate (TL to TP)</b>	5°C/Second Maximum
<b>Time Maintained Above:</b>	
- Temperature (TL)	150°C
- Time (tL)	200 Seconds Maximum
<b>Peak Temperature (TP)</b>	240°C Maximum
<b>Target Peak Temperature (TP Target)</b>	240°C Maximum 2 Times / 230°C Maximum 1 Time
<b>Time within 5°C of actual peak (tp)</b>	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
<b>Ramp-down Rate</b>	5°C/Second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	N/A
<b>Moisture Sensitivity Level</b>	Level 1
<b>Additional Notes</b>	Temperature shown are applied to body of device.

### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperature shown are applied to body of device.)

### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperature shown are applied to body of device.)

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#### Как с нами связаться

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