Low Noise 150mA LDO Regulator for High Temperature Applications

NO.EA-336-150821

OUTLINE

The RP130x is a CMOS-based positive voltage regulator IC with high ripple rejection, low dropout voltage, high output voltage accuracy and extremely low supply current. The RP130x consists of a voltage reference unit, an error amplifier, a resistor network for voltage setting, a short current limit circuit, and a chip enable circuit.

The RP130x has low supply current characteristics in the CMOS process. In addition, the RP130x can supply a low dropout voltage, which becomes the smallest difference between the input voltage and output voltage by having a low on-resistance and also can achieve the battery's long life by a chip enable function.

When compared with the conventional products of high-speed type, the RP130x achieves low consumption current of 38μA (Typ.) while improving the input transient response, the load transient response, and the ripple rejection.

The RP130x supports two package types: DFN(PLP)1010-4 and SOT-23-5. By the adoption of the ultra-compact DFN(PLP)1010-4, the RP130x can achieve a higher density mounting than ever.

FEATURES

- Input Voltage Range (Max. Rating)··········1.7V to 6.5V (7.0V)
- Operating Temperature Range ··········–40°C to 105°C (※)
- Supply Current··········Typ. 38μA
- Supply Current (Standby Mode)··········Typ. 0.1μA
- Ripple Rejection··········Typ. 80dB (f = 1kHz)
- Output Voltage Range ··········1.2V, 1.5V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V, 3.4V, 3.6V, and 5.0V
  Contact Ricoh sales representatives for other voltages.
- Output Voltage Accuracy··········±1.0% (V_{SET} > 2.0V, Ta = 25°C)
- Temperature-Drift Coefficient of Output Voltage··········Typ. ±20 ppm/°C
- Dropout Voltage ··········Typ. 0.32V (I_{OUT} = 150mA, V_{SET} = 2.8V)
- Line Regulation··········Typ. 0.02%/V
- Packages ··········DFN(PLP)1010-4, SOT-23-5
- Built-in Fold Back Protection Circuit··········Typ. 40mA
- Recommended Ceramic Capacitors ··········0.47μF or more
- Output Noise Voltage ··········Typ. 20 × V_{SET} μVrms
  (BW = 10Hz to 100kHz, I_{OUT} = 30mA)

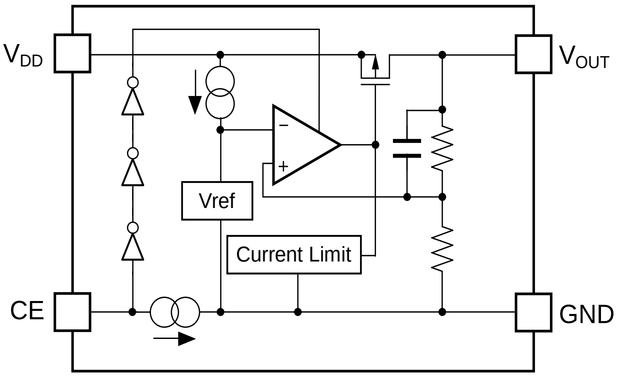
※ This product is usable for the high-temperature applications since have passed a test at the high temperature. In addition, this product has a high-reliability since having passed Ricoh's rigorous quality standards. To distinguish from the consumer products, "-Yx" is added at the end of the product name.

APPLICATIONS

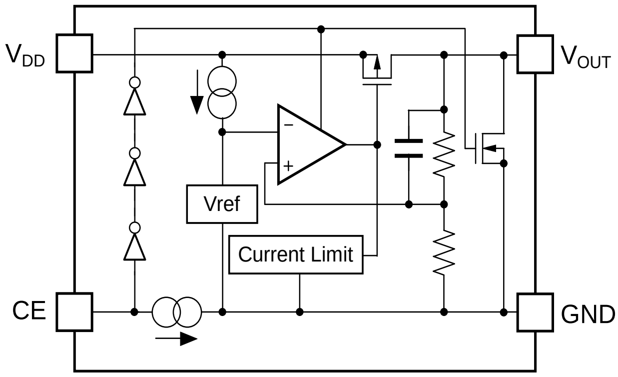
- Industrial equipments such as FAs and smart meters
- Equipments used under high-temperature conditions
- Equipments accompanied by self-heating

BLOCK DIAGRAMS

RP130xxx1B



RP130xxx1D



SELECTION GUIDE

The output voltage, chip-enable polarity, auto-discharge function, and package type for this device can be selected at the user’s request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
RP130Kxx1∗-TR-Y	DFN(PLP)1010-4	10,000pcs	Yes	Yes
RP130Nxx1∗-TR-YE	SOT-23-5	3,000pcs	Yes	Yes

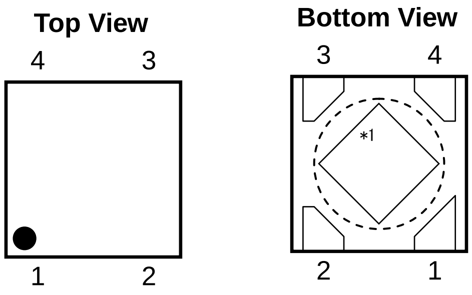
xx : Specify the set output voltage (V_{SET})
1.2 V (12) / 1.5 V (15) / 1.8 V (18) / 2.5 V (25) / 2.8 V (28) / 3.0 V (30) / 3.3 V (33) / 3.4 V (34) / 3.6 V (36) / 5.0 V (50)
Note: Contact Ricoh sales representatives for other voltages.

∗ : Specify the desired functions for chip-enable polarity and auto-discharge
B: "H" active / No auto-discharge function
D: "H" active / Auto-discharge function

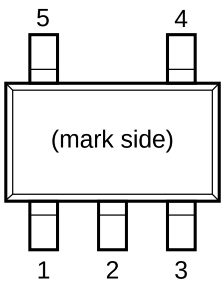
Auto-Discharge function quickly lowers the output voltage to 0 V by releasing the electrical charge in the external capacitor when the chip enable signal is switched from the active mode to the standby mode.

PIN DESCRIPTION

- **DFN(PLP)1010-4**



- **SOT-23-5**



DFN(PLP)1010-4

Pin No.	Symbol	Description
1	V_{OUT}	Output Pin
2	GND	Ground Pin
3	CE	Chip Enable Pin ("H" Active)
4	V_{DD}	Input Pin

[*1] The tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). It is recommended that the tab be connected to the ground plane on the board, or otherwise be left floating.

SOT-23-5

Pin No.	Symbol	Description
1	V_{DD}	Output Pin
2	GND	Ground Pin
3	CE	Chip Enable Pin ("H" Active)
4	NC	No Connection
5	V_{OUT}	Output Pin

ABSOLUTE MAXIMUM RATINGS

Symbol	Item		Rating	Unit
V_{IN}	Input Voltage		–0.3 to 7.0	V
V_{CE}	Input Voltage (CE Pin)		–0.3 to 7.0	V
V_{OUT}	Output Voltage		–0.3 to V_{IN}+0.3	V
I_{OUT}	Output Current		200	mA
P_D	Power Dissipation (DFN(PLP)1010-4) *1	Standard Land Pattern	400	mW
	Power Dissipation (SOT-23-5) *1	Standard Land Pattern	420	
Tj	Junction Temperature		–40 to 125	°C
Tstg	Strong Temperature Range		–55 to 125	°C

*1 Refer to *PACKAGE INFORMATION* for detailed information.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.
The functional operation at or over these absolute maximum rating is not assured.

RECOMMENDED OPERATING RATINGS

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	1.7 to 6.5	V
Ta	Operating Temperature Range	–40 to 105	°C

RECOMMENDED OPERATING RATINGS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating ratings. The semiconductor devices cannot operate normally over the recommended operating ratings, even if when they are used over such ratings by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating ratings.

ELECTRICAL CHARACTERISTICS

V_{IN} = V_{SET}+1.0V (V_{SET} > 1.5V), V_{IN} = 2.5V (V_{SET} ≤ 1.5V), I_{OUT} = 1mA, C_{IN} = C_{OUT} = 0.47µF, unless otherwise noted.
The specifications surrounded by [box] are guaranteed by design engineering at - 40ºC ≤ Ta ≤ 105ºC.

RP130xxx1B/D **(Ta = 25°C)**

Symbol	Item	Conditions		Min.	Typ.	Max.	Unit
V_{OUT}	Output Voltage	Ta = 25°C	V_{SET} > 2.0V	×0.99		×1.01	V
			V_{SET} ≤ 2.0V	–20		20	mV
		-40°C ≤ Ta ≤ 105°C	V_{SET} > 2.0V	×0.985		×1.015	V
			V_{SET} ≤ 2.0V	-30		30	mV
I_{OUT}	Output Current			150			mA
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	1mA ≤ I_{OUT} ≤ 150mA			10	30	mV
V_{DIF}	Dropout Voltage	I_{OUT} = 150mA	1.2V ≤ V_{SET} < 1.5V		0.67	1.03	V
			1.5V ≤ V_{SET} < 1.7V		0.54	0.84	
			1.7V ≤ V_{SET} < 2.0V		0.46	0.75	
			2.0V ≤ V_{SET} < 2.5V		0.41	0.63	
			2.5V ≤ V_{SET} < 4.0V		0.32	0.51	
			V_{SET} = 5V		0.24	0.31	
I_{SS}	Supply Current	I_{OUT} = 0mA			38	58	µA
Istandby	Supply Current (at Standby)	V_{CE} = 0			0.1	1.0	µA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	V_{SET} + 0.5V ≤ V_{IN} ≤ 6.5V			0.02	0.10	%/V
I_{SC}	Short Current Limit	V_{OUT} = 0V			40		mA
I_{PD}	CE Pull-down Current				0.4		µA
V_{CEH}	CE Input Voltage "H"			1.0			V
V_{CEL}	CE Input Voltage "L"					0.36	V
R_{LOW}	Nch ON Resistance for Auto Discharge (D Version Only)	V_{IN} = 4.0V, V_{CE} = 0V			30		Ω

All test items listed under Electrical Characteristics are done under the pulse load condition (Tj ≈ Ta = 25°C)

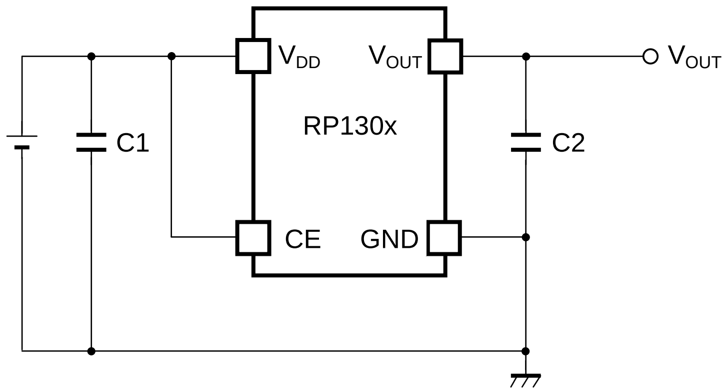
Product-specific Electrical Characteristics

The specifications surrounded by [ ] are guaranteed by design engineering at - 40ºC ≤ Ta ≤ 105ºC.

(Ta = 25°C)

Product Name	V_{OUT} [V] (Ta = 25°C)			V_{OUT} [V] (Ta = −40 ~ 105°C)			V_{DIF} [V]	
	Min.	Typ.	Max.	Min.	Typ.	Max.	Typ.	Max.
RP130x121x	1.180	1.200	1.220	1.170	1.200	1.230	0.67	1.03
RP130x151x	1.480	1.500	1.520	1.470	1.500	1.530	0.54	0.84
RP130x181x	1.780	1.800	1.820	1.770	1.800	1.830	0.46	0.75
RP130x251x	2.475	2.500	2.525	2.463	2.500	2.538	0.32	0.51
RP130x281x	2.772	2.800	2.828	2.758	2.800	2.842		
RP130x301x	2.970	3.000	3.030	2.955	3.000	3.045		
RP130x331x	3.267	3.300	3.333	3.251	3.300	3.350		
RP130x341x	3.366	3.400	3.434	3.349	3.400	3.451		
RP130x361x	3.564	3.600	3.636	3.546	3.600	3.654		
RP130x501x	4.950	5.000	5.050	4.925	5.000	5.075	0.24	0.31

TYPICAL APPLICATION



External Components：

Symbol	Description
C2	0.47μF (Ceramic)

TECHNICAL NOTES

Phase Compensation

In the ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, use a capacitor C2 with 0.47μF or more.

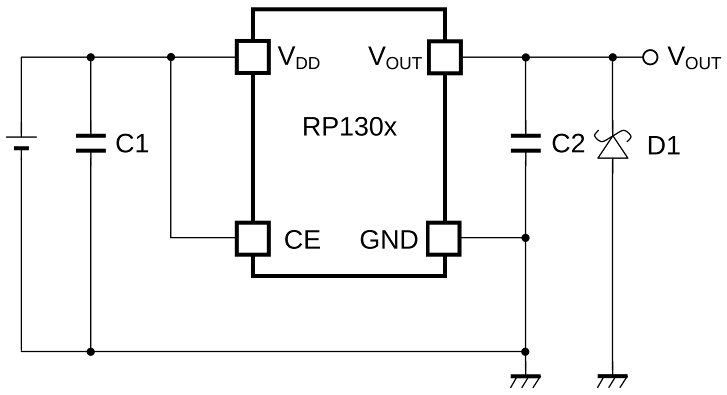
If a tantalum capacitor is used, and its ESR (Equivalent Series Resistance) of C2 is large, the loop oscillation may result. Because of this, select C2 carefully considering its frequency characteristics.

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is too high, noise pickup or unstable operation may result. Connect 0.47 μF or more of the capacitor C1 between the V_{DD} and GND, and as close as possible to the pins.

In addition, connect the capacitor C2 between V_{OUT} and GND, and as close as possible to the pins.

TYPICAL APPLICATION FOR IC CHIP BREAKDOWN PREVENTION



When a sudden surge of electrical current travels along the V_{OUT} pin and GND due to a short-circuit, electrical resonance of a circuit involving an output capacitor (C2) and a short circuit inductor generates a negative voltage and may damage the device or the load devices. Connecting a schottky diode (D1) between the V_{OUT} pin and GND has the effect of preventing damage to them.

PACKAGE INFORMATION

Power Dissipation (DFN(PLP)1010-4)

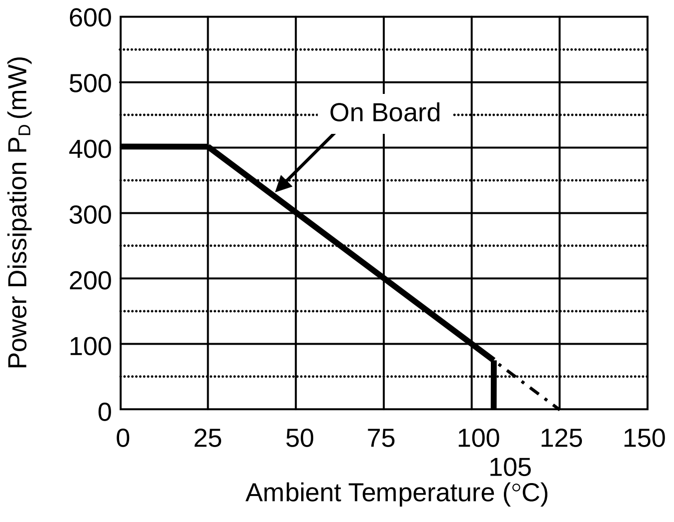
Power Dissipation (P_D) depends on conditions of mounting on board. This specification is based on the measurement at the condition below:

Measurement Conditions

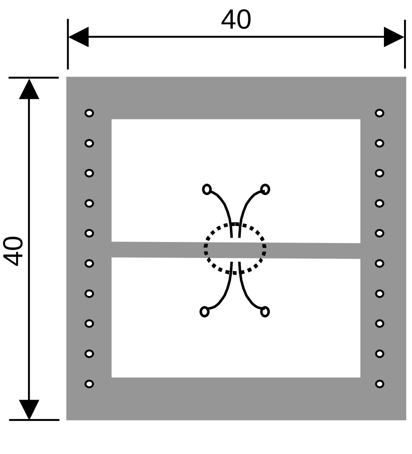
	Standard Test Land Pattern
Environment	Mounting on Board (Wind velocity=0m/s)
Board Material	Glass cloth epoxy plastic (Double sided)
Board Dimensions	40mm x 40mm x 1.6mm
Copper Ratio	Top side: Approx. 50%, Back side: Approx. 50%
Through-holes	ϕ 0.54mm x 24pcs

Measurement Result (Ta=25°C, Tjmax=125°C)

	Standard Test Land Pattern
Power Dissipation	400mW
Thermal Resistance	θja = (125-25°C)/0.4W = 250°C/W
	θjc = 67 °C/W



Power Dissipation

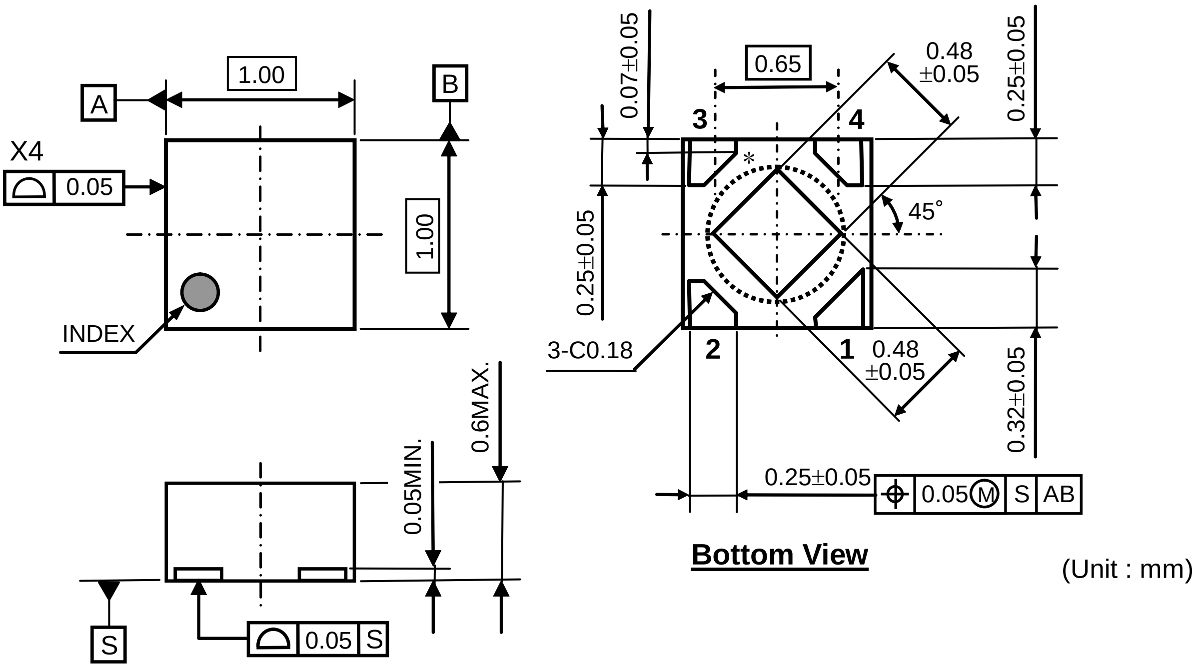


Measurement Board Pattern

IC Mount Area (Unit : mm)

RICOH

Package Dimensions (DFN(PLP)1010-4)



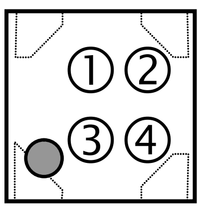
*) The tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). It is recommended that the tab be connected to the ground plane on the board, or otherwise be left floating.

DFN(PLP)1010-4 Package Dimensions

Mark Specifications (DFN(PLP)1010-4)

①②: Product Code … **Refer to "*RP130K Mark Specification Table*"**

③④: Lot Number …Alphanumeric Serial Number



DFN(PLP)1010-4 Mark Specifications

RP130K Mark Specification Table (DFN(PLP)1010-4)

RP130Kxx1B

Product Name	① ②	V_{SET}
RP130K121B	T A	1.2V
RP130K151B	T D	1.5V
RP130K181B	T G	1.8V
RP130K251B	T Q	2.5V
RP130K281B	T T	2.8V
RP130K301B	T W	3.0V
RP130K331B	T Z	3.3V
RP130K341B	U A	3.4V
RP130K361B	U C	3.6V
RP130K501B	U S	5.0V

RP130Kxx1D

Product	① ②	V_{SET}
RP130K121D	V A	1.2V
RP130K151D	V D	1.5V
RP130K181D	V G	1.8V
RP130K251D	V Q	2.5V
RP130K281D	V T	2.8V
RP130K301D	V W	3.0V
RP130K331D	V Z	3.3V
RP130K341D	W A	3.4V
RP130K361D	W C	3.6V
RP130K501D	W S	5.0V

Power Dissipation (SOT-23-5)

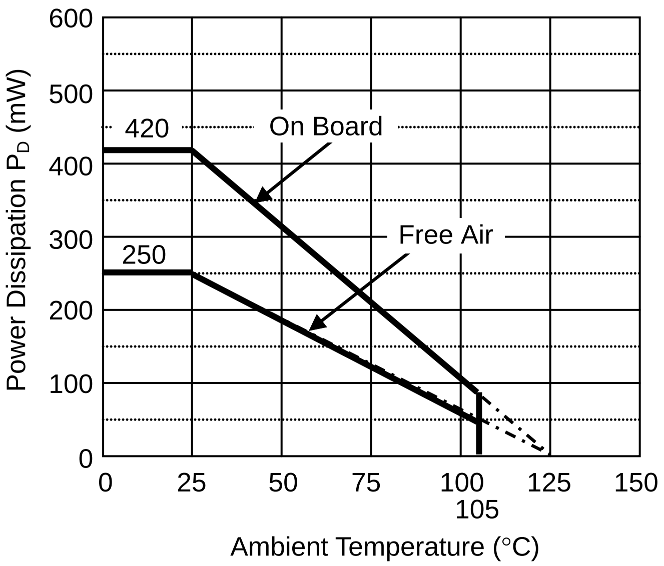
Power Dissipation (P_D), which indicates the P_D of SOT-23-6 package as a substitute, depends on conditions of mounting on board. This specification is based on the measurement at the condition below:

Measurement Conditions

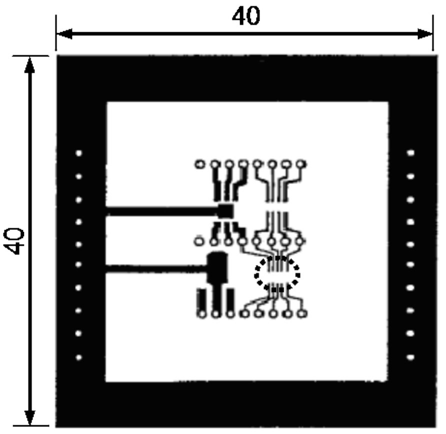
	Standard Test Land Pattern
Environment	Mounting on Board (Wind velocity=0m/s)
Board Material	Glass cloth epoxy plastic (Double sided)
Board Dimensions	40mm x 40mm x 1.6mm
Copper Ratio	Top side: Approx. 50%, Back side: Approx. 50%
Through-holes	ϕ 0.5mm x 44pcs

Measurement Result (Ta=25°C, Tjmax=125°C)

	Standard Land Pattern	Free Air
Power Dissipation	420mW	250mW
Thermal Resistance	θja = (125-25°C)/0.42W= 238°C/W	400°C/W



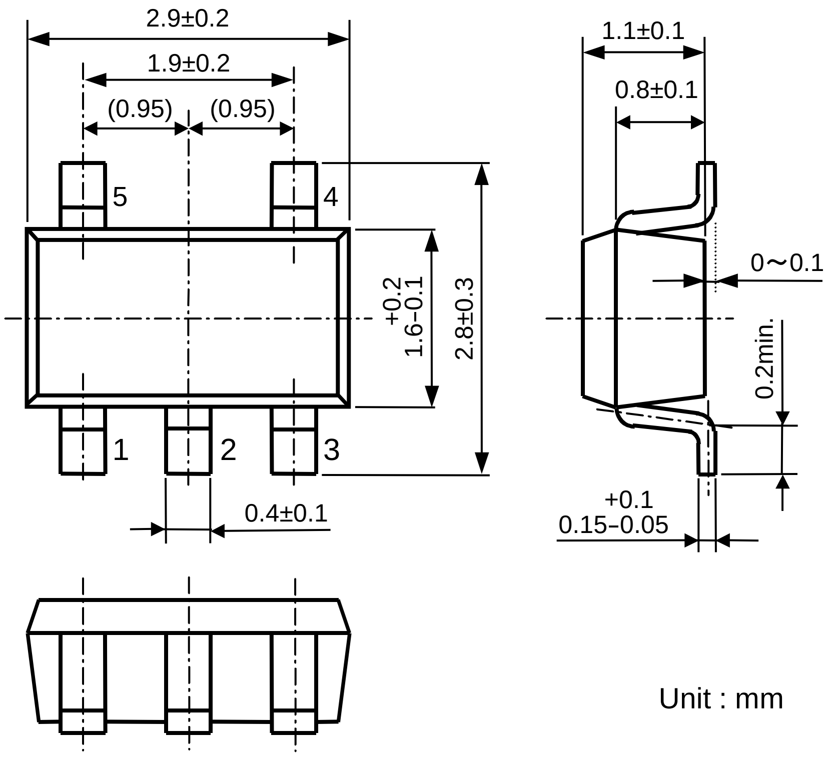
Power Dissipation



Measurement Board Pattern

IC Mount Area (Unit: mm)

Package Dimensions (SOT-23-5)

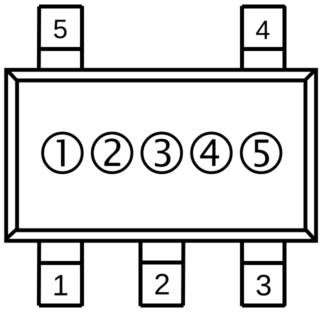


SOT-23-5 Package Dimensions

Mark Specifications (SOT-23-5)

①②③: Product Code … **Refer to "*RP130N Mark Specification Table*"**

④⑤: Lot Number …Alphanumeric Serial Number



SOT-23-5 Mark Specifications

RP130N Mark Specification Table (SOT-23-5)

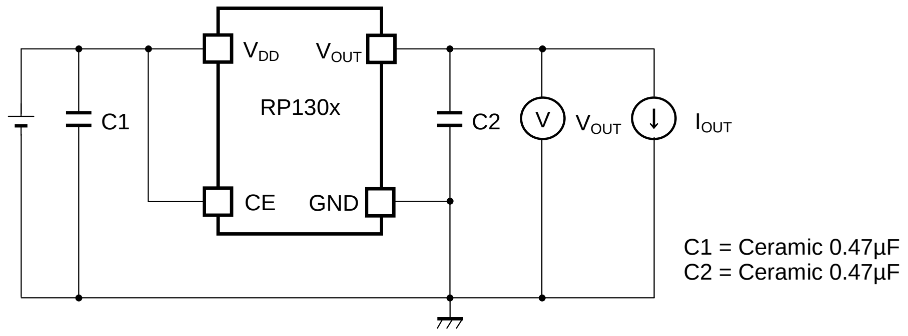
RP130Nxx1B

Product Name	①②③	V_{SET}
RP130N121B	H 1 A	1.2 V
RP130N151B	H 1 D	1.5 V
RP130N181B	H 1 G	1.8 V
RP130N251B	H 1 Q	2.5 V
RP130N281B	H 1 T	2.8 V
RP130N301B	H 1 W	3.0 V
RP130N331B	H 1 Z	3.3 V
RP130N341B	J 1 A	3.4 V
RP130N361B	J 1 C	3.6 V
RP130N501B	J 1 S	5.0 V

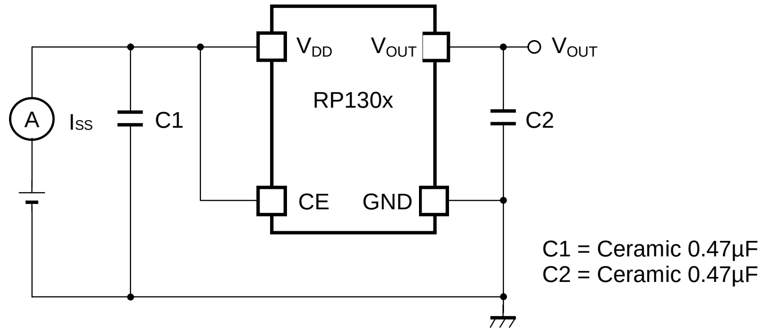
RP130Nxx1D

Product Name	①②③	V_{SET}
RP130N121D	H 2 A	1.2 V
RP130N151D	H 2 D	1.5 V
RP130N181D	H 2 G	1.8 V
RP130N251D	H 2 Q	2.5 V
RP130N281D	H 2 T	2.8 V
RP130N301D	H 2 W	3.0 V
RP130N331D	H 2 Z	3.3 V
RP130N341D	J 2 A	3.4 V
RP130N361D	J 2 C	3.6 V
RP130N501D	J 2 S	5.0 V

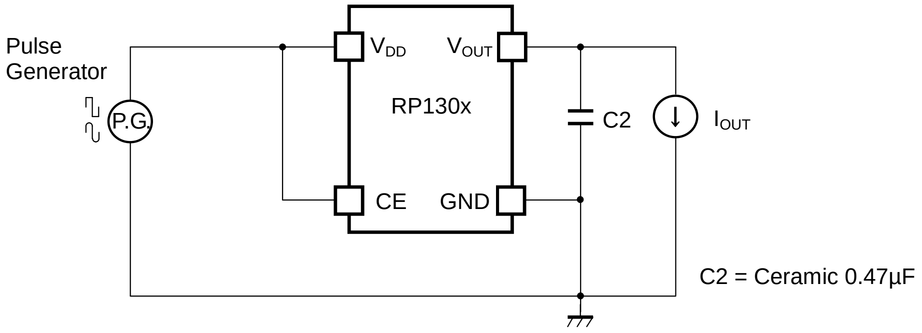
TEST CIRCUITS



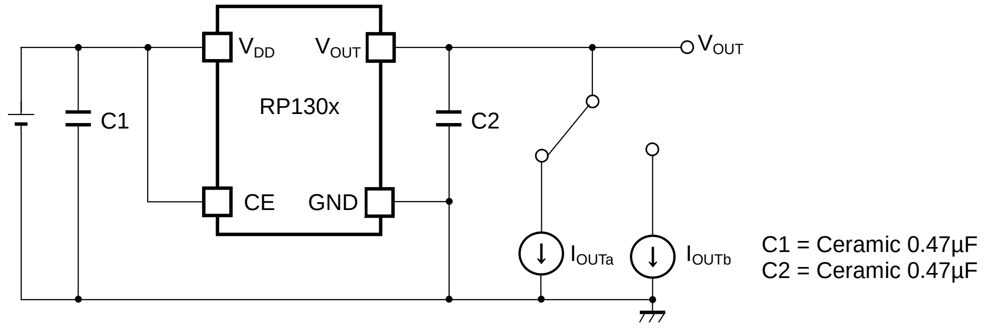
Standard Test Circuit



Supply Current Test Circuit



Ripple Rejection, Line Transient Response Test Circuit



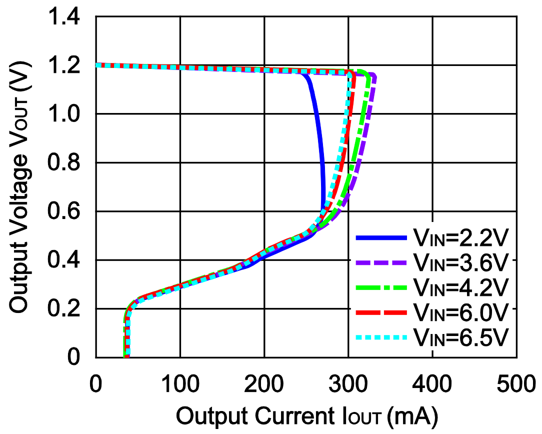
Load Transient Response Test Circuit

TYPICAL CHARACTERISTICS

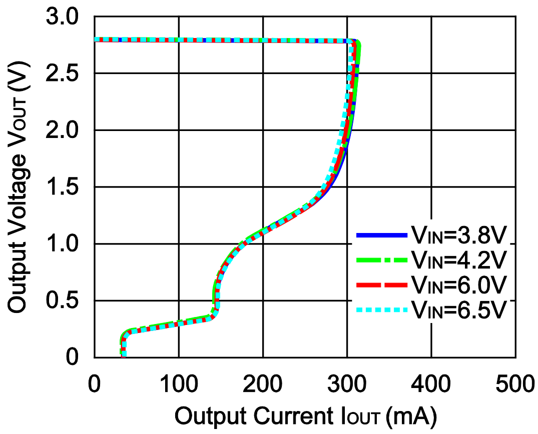
Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.

1) Output Voltage vs. Output Current (C1 = C2 = 0.47μF, Ta = 25°C)

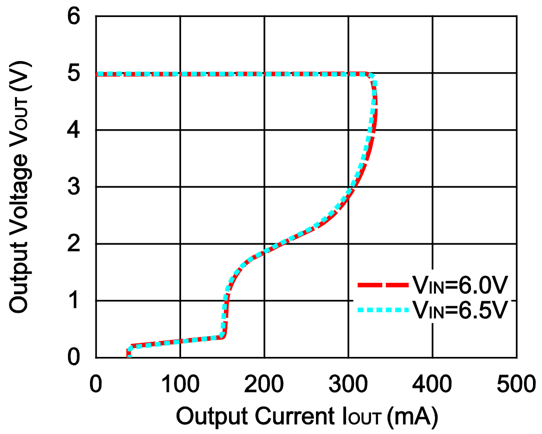
RP130x121x



RP130x281x

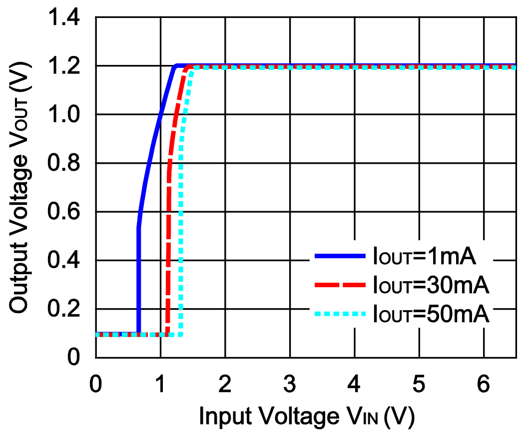


RP130x501x

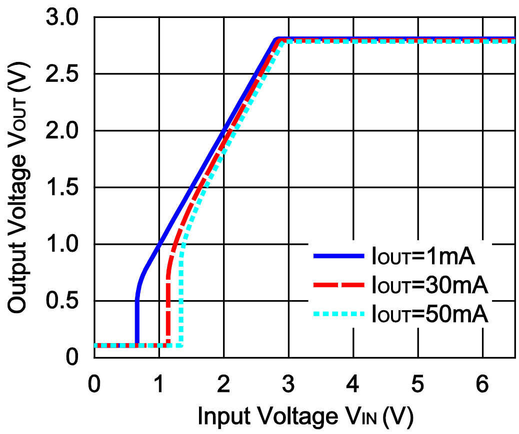


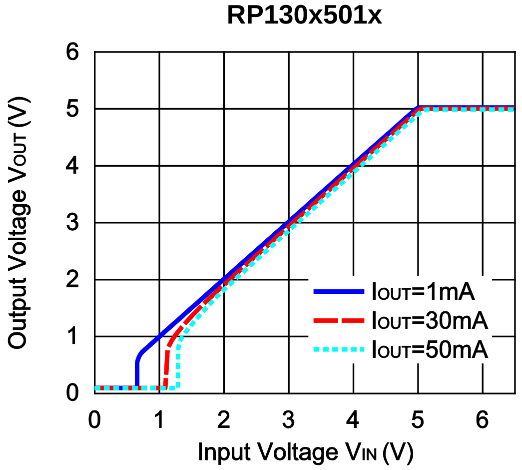
2) Output Voltage vs. Input Voltage (C1 = C2 = 0.47μF, Ta = 25°C)

RP130x121x

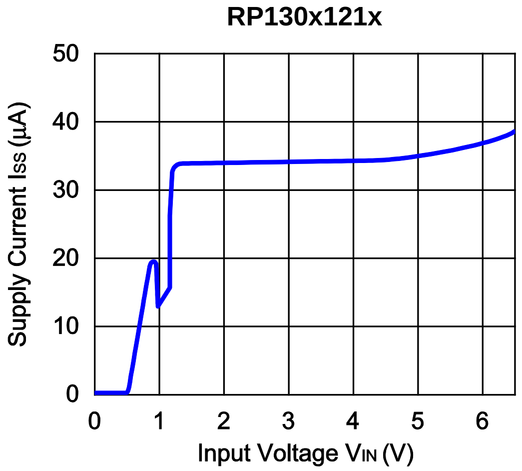


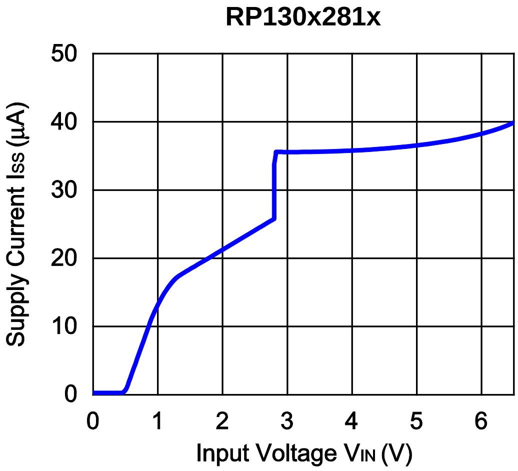
RP130x281x

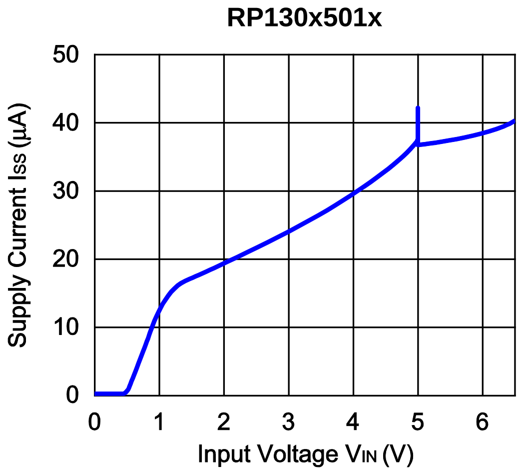




3) Supply Current vs. Input Voltage (C1 = C2 = 0.47μF, Ta = 25°C)

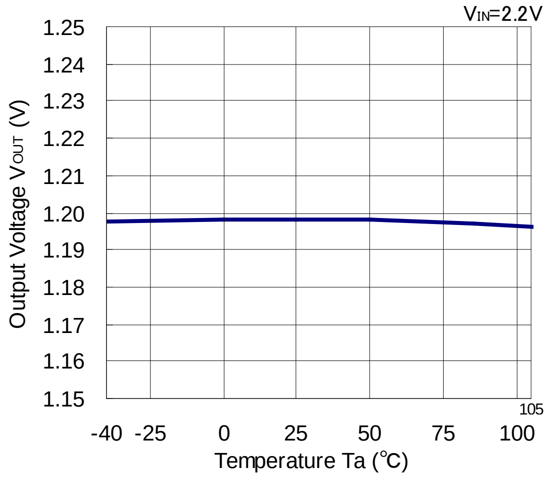




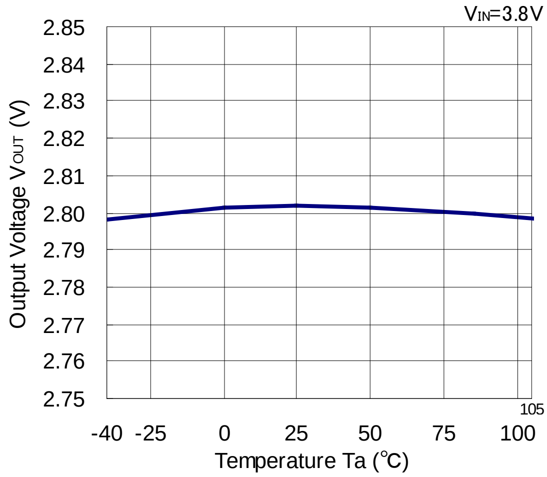


4) Output Voltage vs. Temperature (I_{OUT} = 1mA, C1 = C2 = 0.47μF)

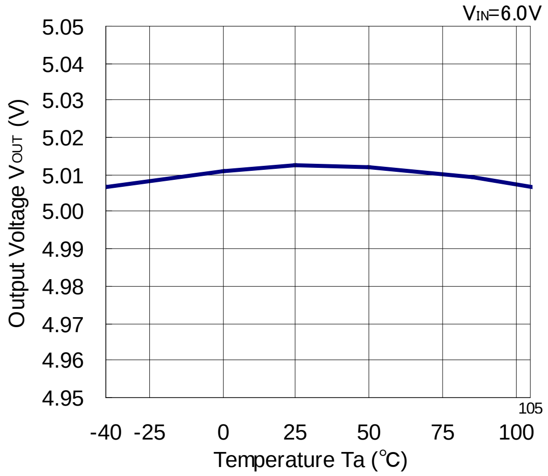
RP130x121x



RP130x281x

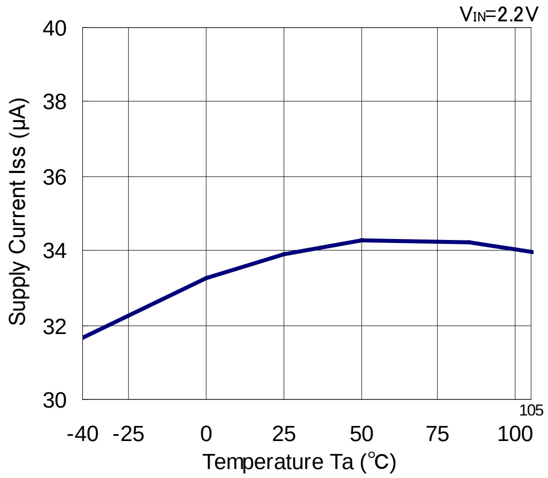


RP130x501x

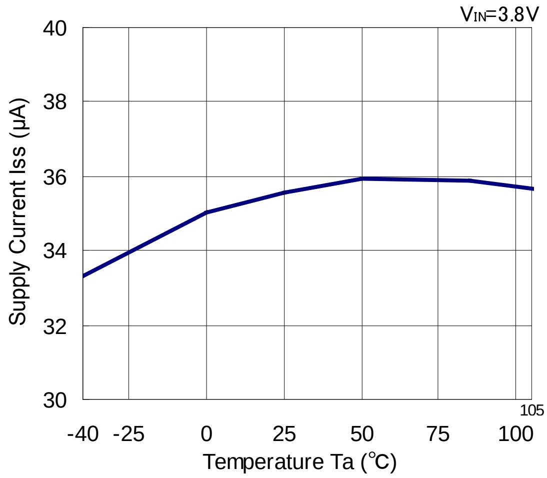


5) Supply Current vs. Temperature (I_{OUT} = 0mA, C1 = C2 = 0.47μF)

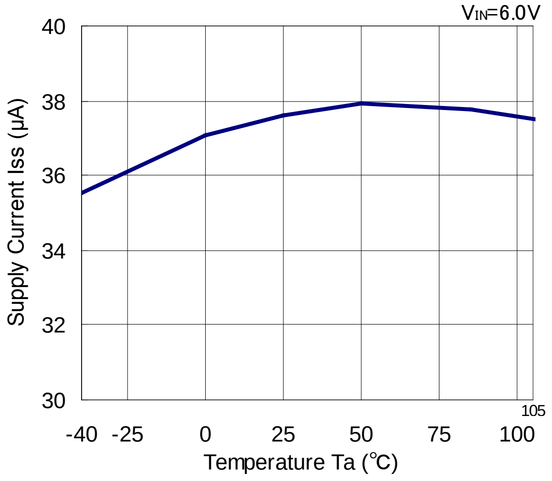
RP130x121x



RP130x281x

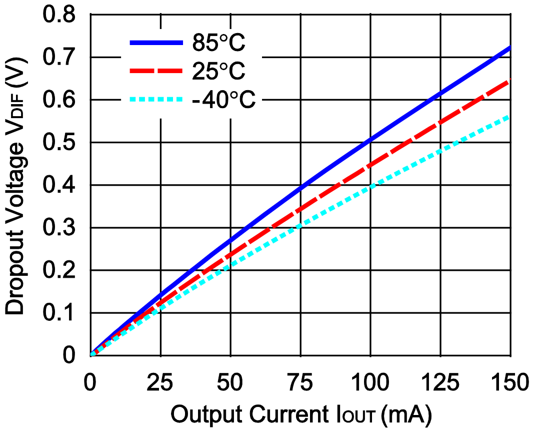


RP130x501x

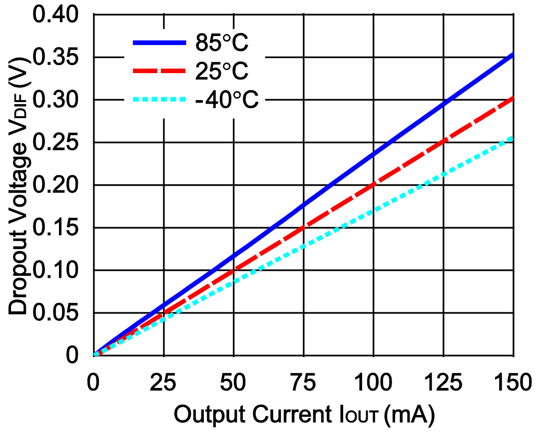


6) Dropout Voltage vs. Output Current (C1 = C2 = 0.47µF)

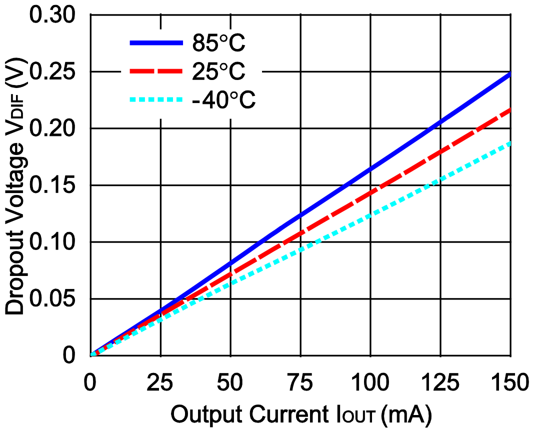
RP130x121x



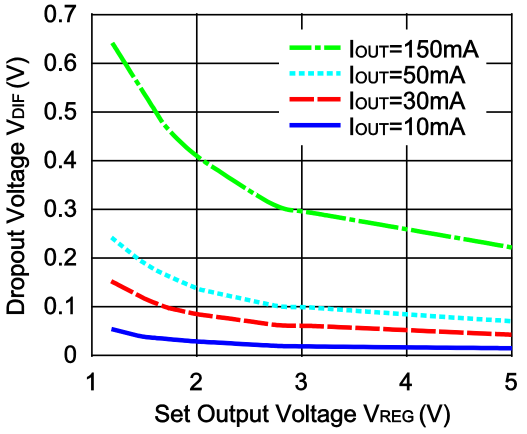
RP130x281x



RP130x501x

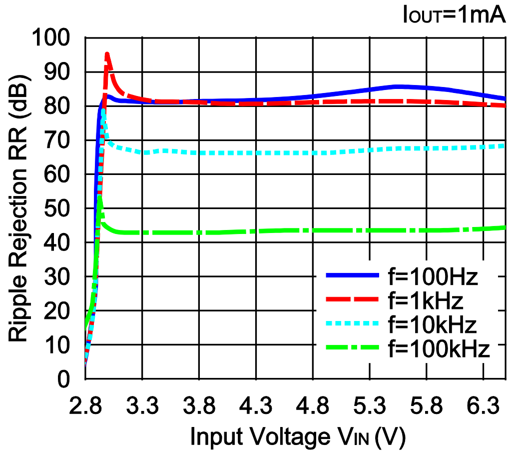


7) Dropout Voltage vs. Set Output Voltage (C1 = C2 = 0.47μF)

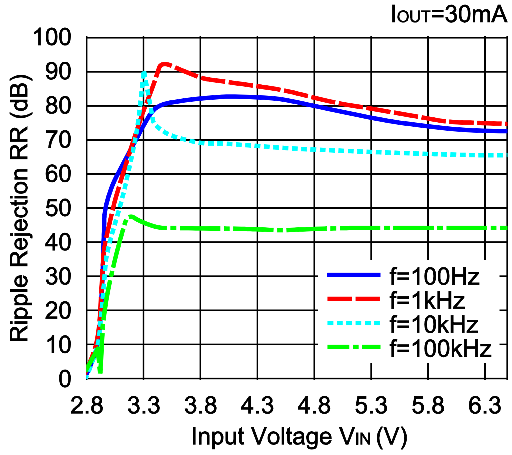


8) Ripple Rejection vs. Input Bias Voltage (C1 = none, C2 = 0.47μF, Ripple = 0.2Vp-p, Ta = 25°C)

RP130x281x

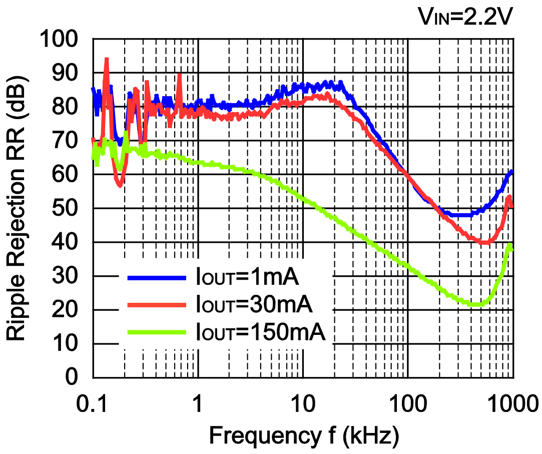


RP130x281x

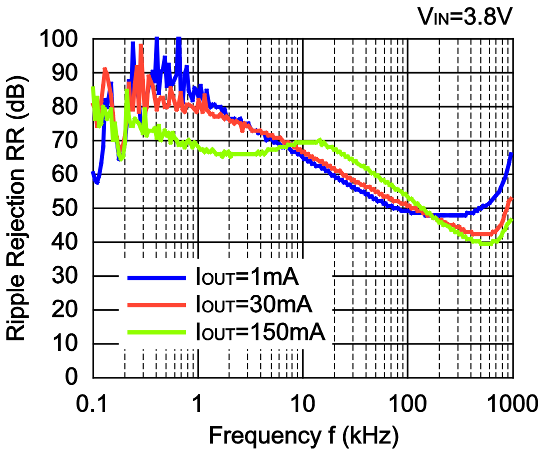


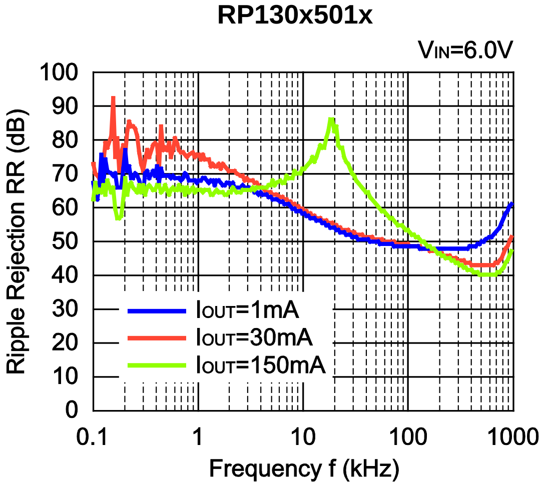
9) Ripple Rejection vs. Frequency (C1 = none, C2 = 0.47μF, Ripple = 0.2Vp-p, Ta = 25°C)

RP130x121x

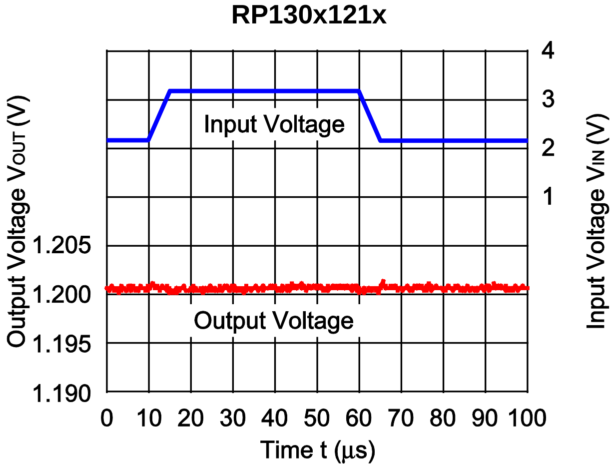


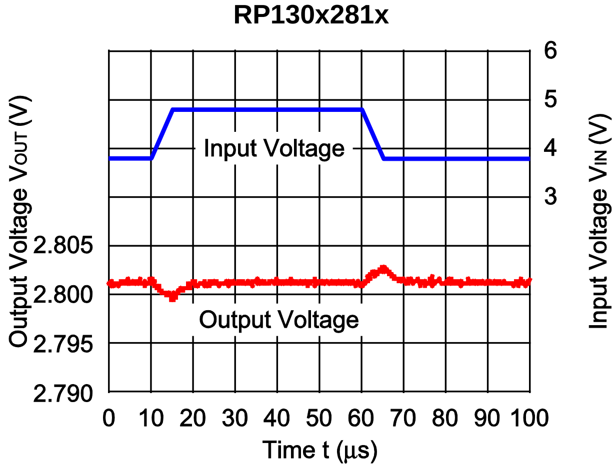
RP130x281x

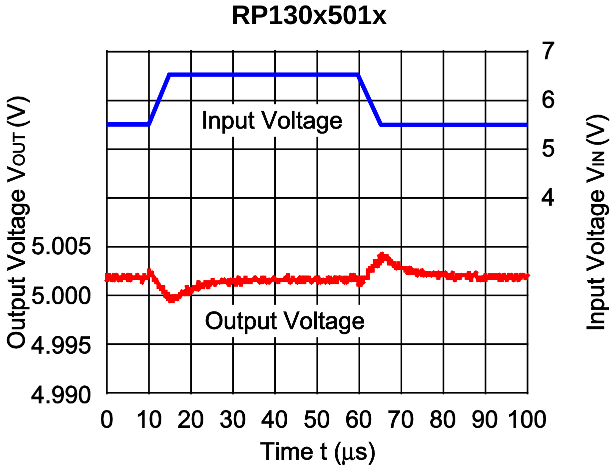




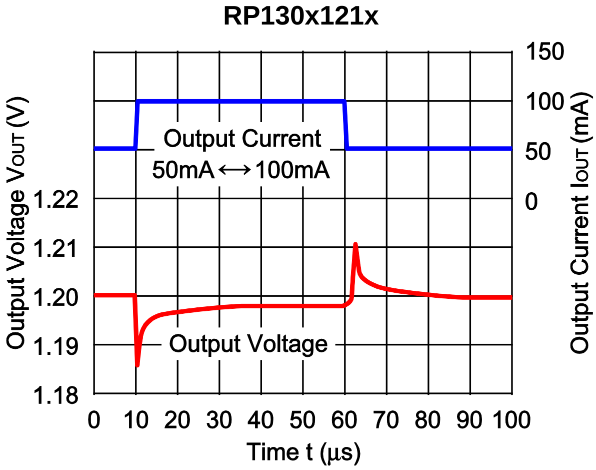
10) Input Transient Response (I_{OUT} = 30mA, tr = tf = 5μs, C1 = none, C2 = 0.47μF, Ta = 25°C)

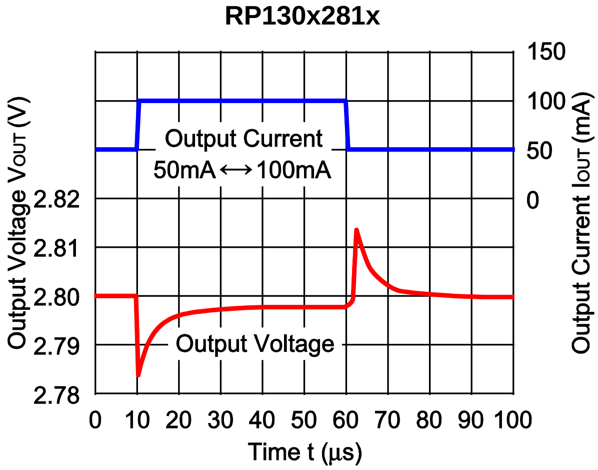


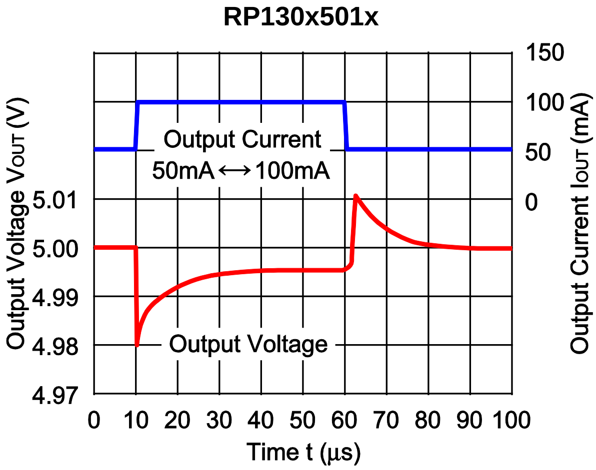




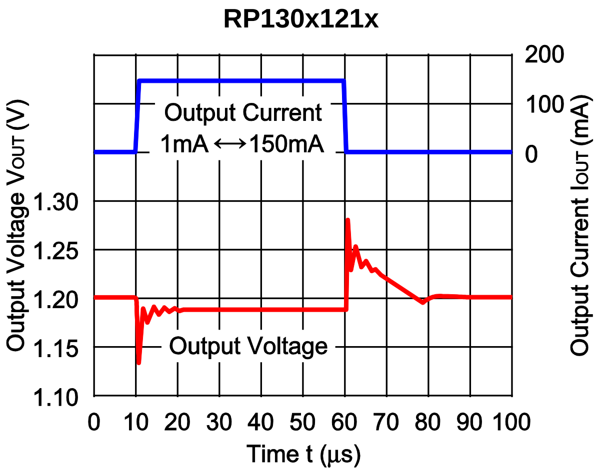
11) Load Transient Response (tr = tf = 0.5μs, C1 = C2 = 0.47μF, I_{OUT} = 50mA ⇔100mA, Ta = 25°C)

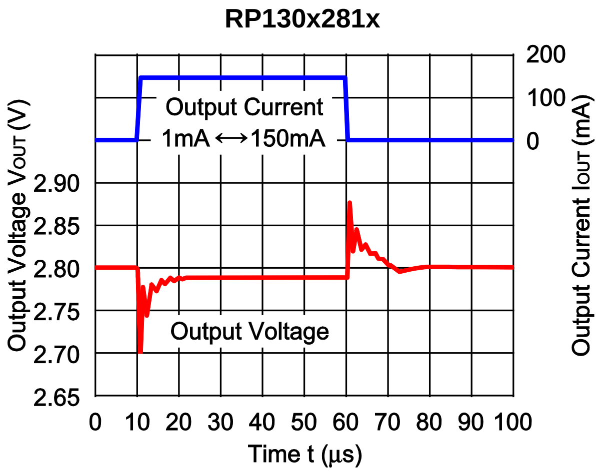




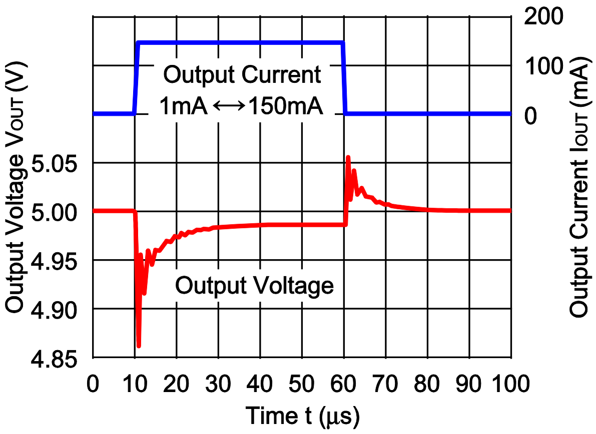


12) Load Transient Response (tr = tf = 0.5μs, C1 = C2 = 0.47μF, I_{OUT} = 1mA ⇔ 150mA, Ta = 25°C)



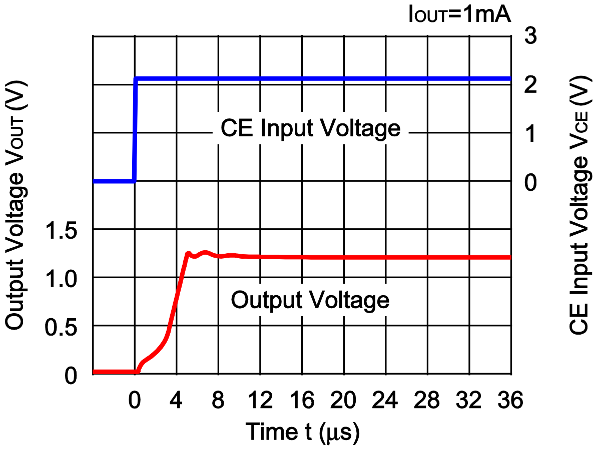


RP130x501x

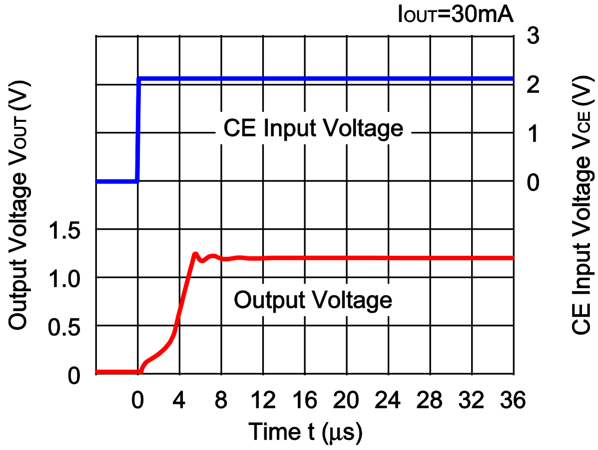


13) Rise Time with CE Pin (C1 = C2 = 0.47µF, Ta = 25°C)

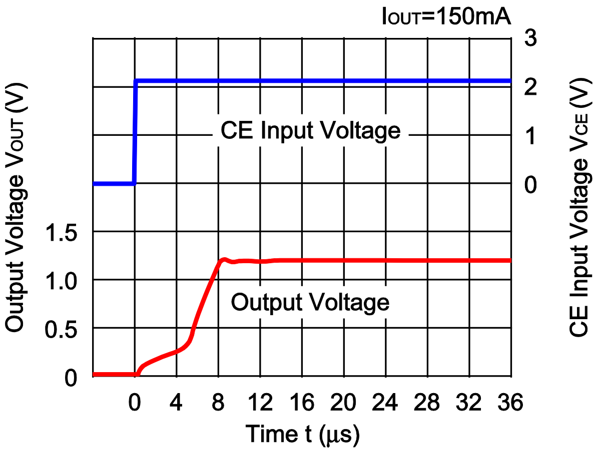
RP130x121x



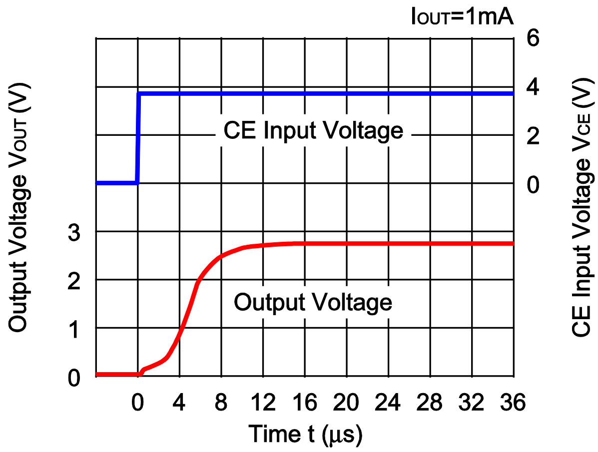
RP130x121x

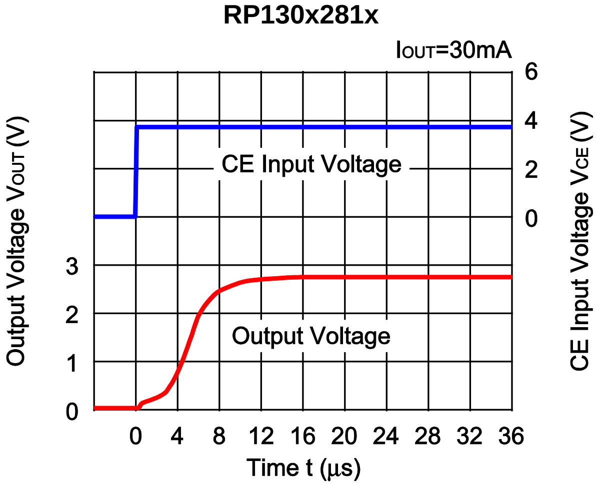


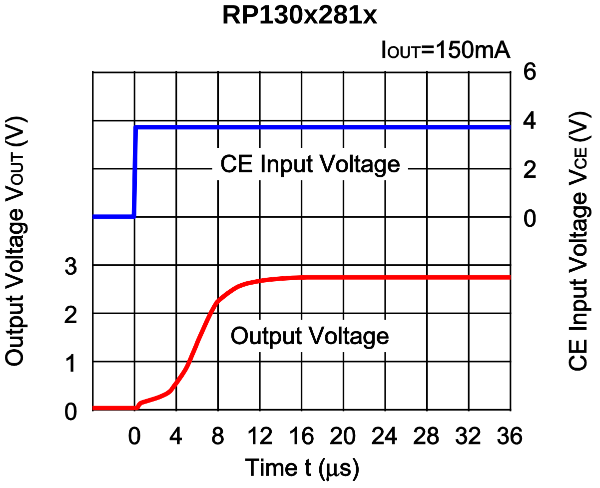
RP130x121x

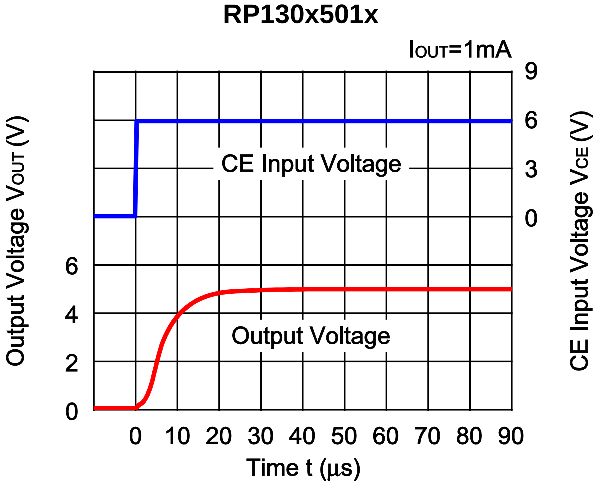


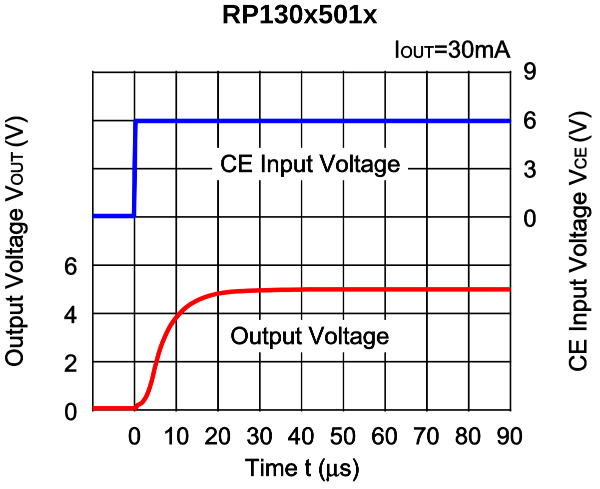
RP130x281x

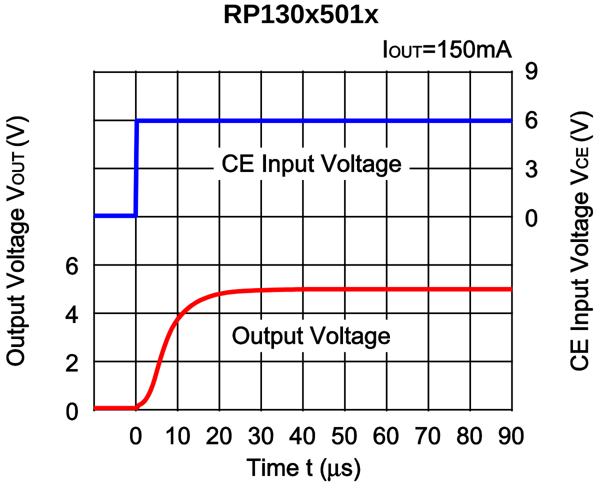



RP130x281x
IOUT=30mA
CE Input Voltage
Output Voltage
Output Voltage VOUT (V)
CE Input Voltage VCE (V)
Time t (μs)
0 4 8 12 16 20 24 28 32 36
0 1 2 3
0 2 4 6

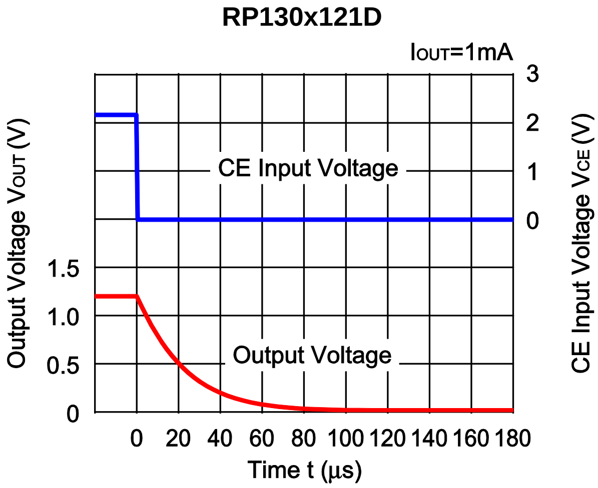

RP130x281x
IOUT=150mA
CE Input Voltage
Output Voltage
Output Voltage VOUT (V)
CE Input Voltage VCE (V)
Time t (μs)
0 4 8 12 16 20 24 28 32 36
0 1 2 3
0 2 4 6

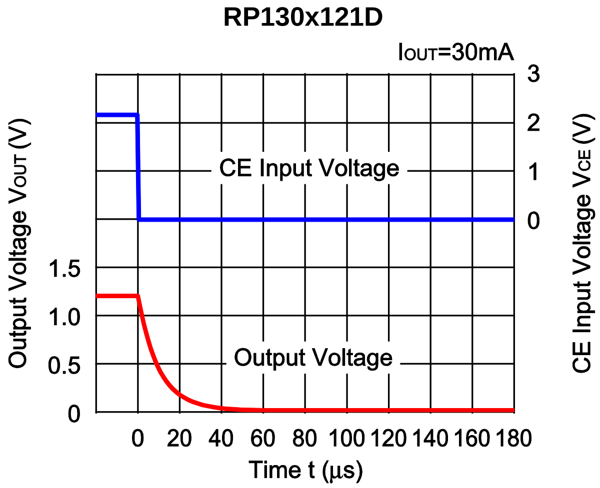

RP130x501x
IOUT=1mA
CE Input Voltage
Output Voltage
Output Voltage VOUT (V)
CE Input Voltage VCE (V)
Time t (μs)
0 10 20 30 40 50 60 70 80 90
0 2 4 6
0 3 6 9

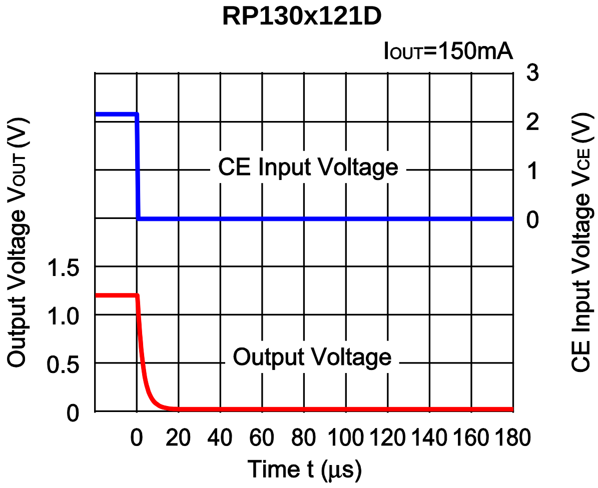

RP130x501x
IOUT=30mA
CE Input Voltage
Output Voltage
Output Voltage VOUT (V)
CE Input Voltage VCE (V)
Time t (μs)
0 10 20 30 40 50 60 70 80 90
0 2 4 6
0 3 6 9

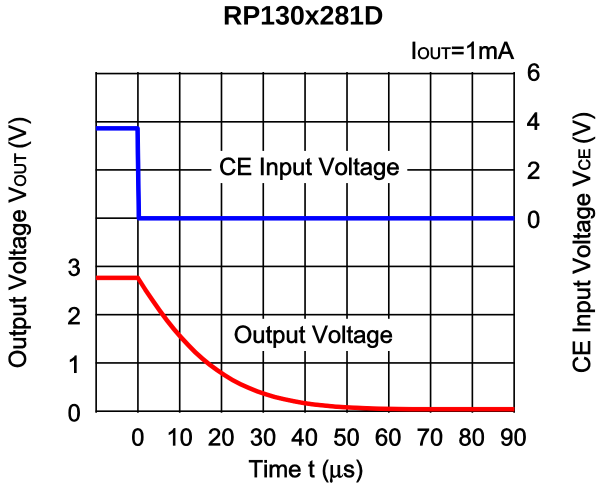

RP130x501x
IOUT=150mA
CE Input Voltage
Output Voltage
Output Voltage VOUT (V)
CE Input Voltage VCE (V)
Time t (μs)
0 10 20 30 40 50 60 70 80 90
0 2 4 6
0 3 6 9

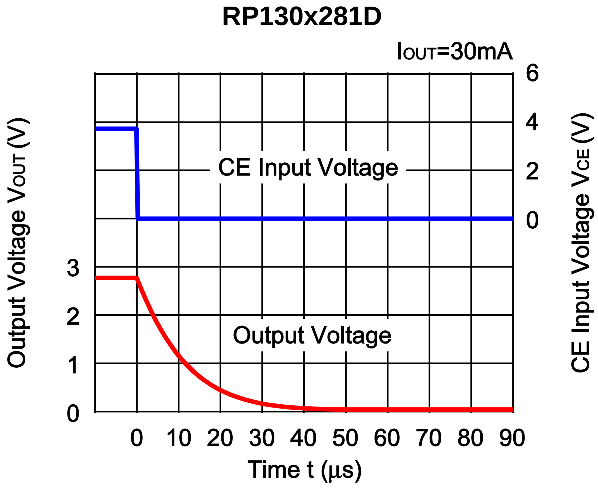
14) Fall Time with CE Pin in D-Version (C1 = C2 = 0.47µF, Ta = 25°C)

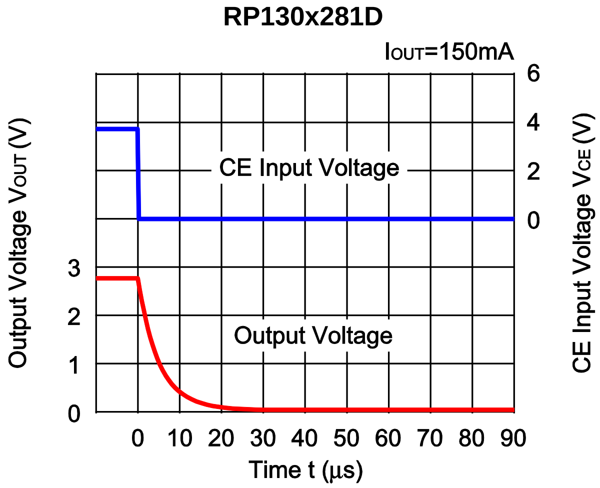






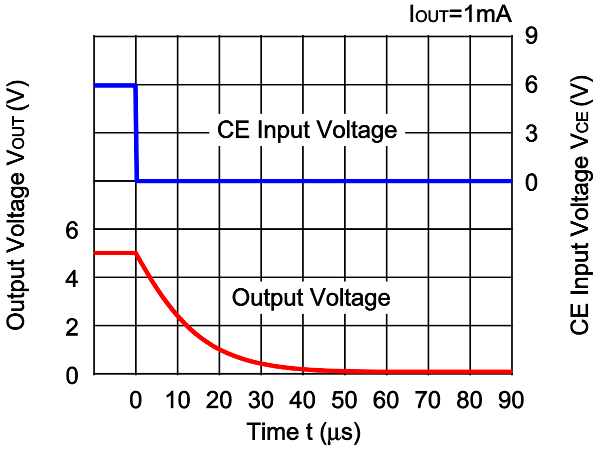




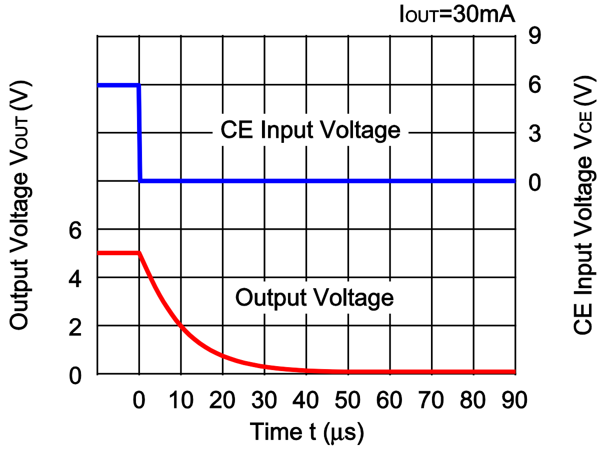


RICOH

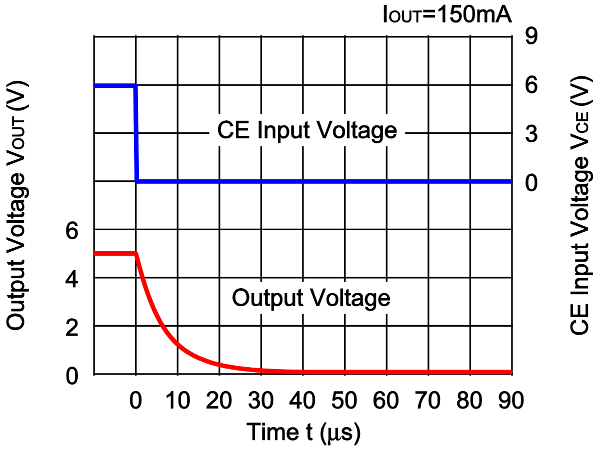
RP130x501D



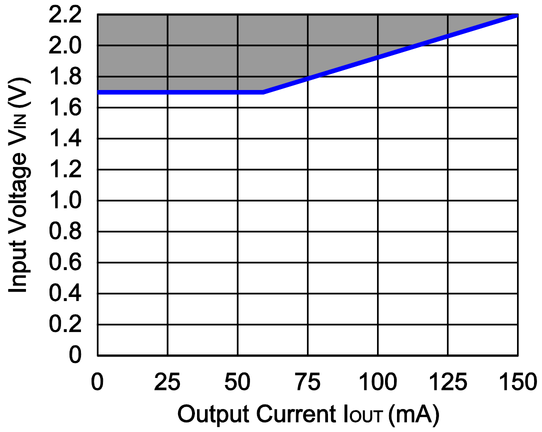
RP130x501D



RP130x501D



15) Minimum Operating Voltage (C1 = C2 = 0.47μF)



Hatched area is available for 1.2V output.

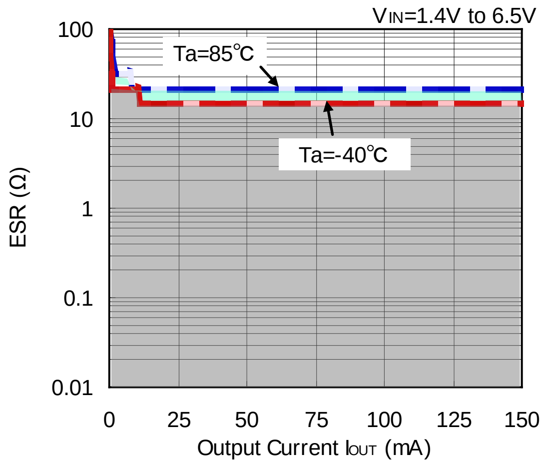
ESR vs. Output Current

The RP130x is recommended to use a ceramic type capacitor, but the RP130x can be used other capacitors of the lower ESR type. The relation between the output current (I_{OUT}) and the ESR of output capacitor is shown below.

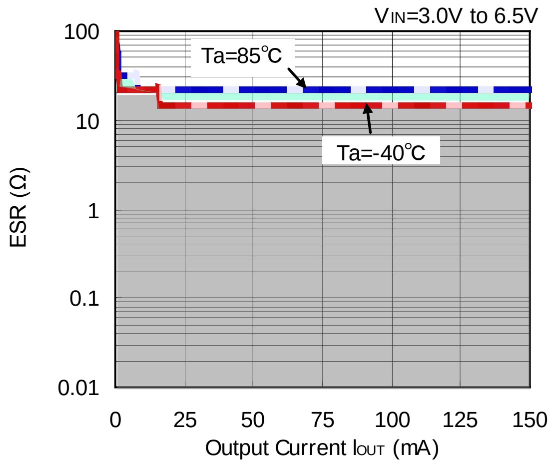
Measurement conditions

Frequency Band: 10 Hz to 3 MHz
Measurement Temperature: −40°C to 85°C
Hatched area: Noise level is 40 μV (average) or below
Ceramic Capacitor: C1 = Ceramic 0.47 μF, C2 = 0.47 μF

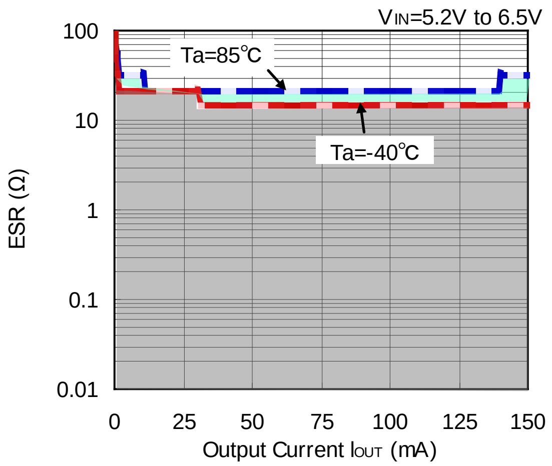
RP130x121x



RP130x281x



RP130x501x



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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.

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