

SAW filter
Short range devices

Series/type: B3760

Ordering code: B39431B3760Z810

Date: April 27, 2017

Version: 2.4

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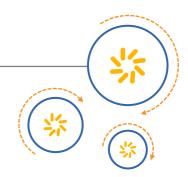
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RF360 Europe GmbH
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## 1 Application

- Low-loss RF filter for remote control receivers
- Balanced and unbalanced operation possible

#### 2 Features

- Package size 3.8±0.15 mm × 3.8±0.15 mm
- Package height 1.5+0.1/-0.15 mm
- Package code QCC8B
- Approximate weight 0.07 g
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Lead free soldering compatible with J-STD20C
- Filter surface passivated
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 1 (MSL1)
- AEC-Q200 qualified component family (Grade 1: -40 °C to +125 °C)

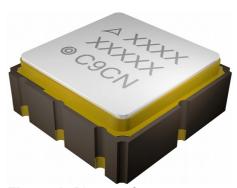


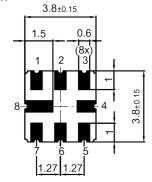
Figure 1: Picture of component with example of product marking.

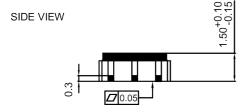
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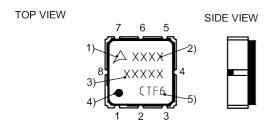
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## 3 Package

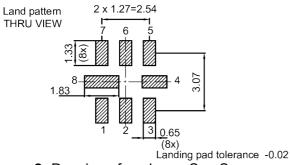
**BOTTOM VIEW** 







- 1) Company logo
- 2) Device designation
- 3) Last five digits of the lot number
- 4) Marking for pad number 1
- 5) Example of production location and date code



**Figure 2:** Drawing of package. See Sec. Package information (p. 16).

## 4 Pin configuration

- 2 Input (recommend or ground)
- 5 Output (recommend or ground)
- 1 Ground (recommend or input)
- 3, 4, 7, 8 Ground
- 6 Ground (recommend or output)



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#### 5 Matching circuit

■  $L_{s2}$  = 33 nH



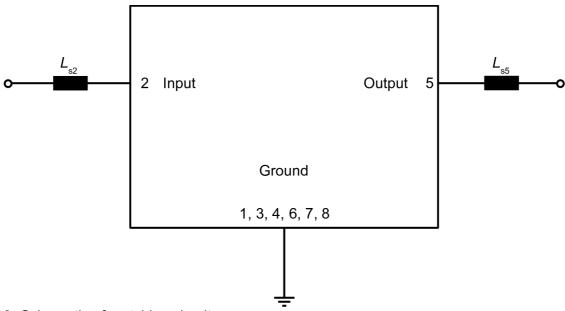


Figure 3: Schematic of matching circuit.

#### Minimising the crosstalk

the upper grounding plane.

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem. Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from

A common GND inductivity of 0.5 nH degrades the ultimate rejection (crosstalk) by 20 dB. The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection.

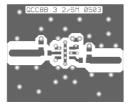


Figure 4: Optimised PCB layout for SAW filters in QCC8B package, pinning 2,5 (top side, scale 1:1).

The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.



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#### 6 Characteristics

Temperature range for specification  $T_{\text{SPEC}} = -40 \, ^{\circ}\text{C} \dots +95 \, ^{\circ}\text{C}$  Input terminating impedance  $Z_{\text{IN}} = 50 \, \Omega$  with ser. 33 nH<sup>1)</sup> Output terminating impedance  $Z_{\text{OUT}} = 50 \, \Omega$  with ser. 33 nH<sup>1)</sup>

Characteristics			$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	<b>typ.</b> @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
Center frequency (center frequency between 3 dB points)		f <sub>C</sub>	_	433.92	_	MHz
Minimum insertion attenuation		$\alpha_{min}$				
Incl. loss in matching components ( $Q_L$ =47)			_	1.9	2.6	dB
Excl. loss in matching components			_	1.6	2.3	dB
Pass band (relative to $\alpha_{\min}$ )		$\alpha_{_{rel,\;max}}$				
433.76 434.0	8 MHz		_	0.5	2.0	dB
433.74 434.1	MHz		_	0.7	3.0	dB
433.7 434.1	4 MHz		_	1.0	6.0	dB
Minimum attenuation (relative to $\alpha_{\mbox{\tiny min}}$ )		$\alpha_{\text{rel},\text{min}}$				
10 414	MHz		52	56	_	dB
414 423.5	MHz		48	52	<u> </u>	dB
423.5 431.7	2 MHz		29	34	_	dB
431.72 432.1	2 MHz		26	35	_	dB
432.12 433.1	MHz		17	21	_	dB
434.7 434.9	2 MHz		16	24	_	dB
434.92 442	MHz		18	21	_	dB
442 500	MHz		40	45	_	dB
500 700	MHz		50	55	_	dB
700 805	MHz		45	50	_	dB
805 1000	MHz		60	65	_	dB
Band width relative to attenuation level of 3.0 dB				0.68	0.74	MHz
Impedance for pass band matching						
@ input port: $Z_{in,iN} = R_{iN}    C_{iN}$		$Z_{in,IN}^{(2)}$	_	240    2.4	_	Ω    pF
@ output port: $Z_{in,OUT} = R_{OUT}    C_{OUT}$		$Z_{\text{in,OUT}}^{2)}$	_	240    2.4	_	Ω    pF

<sup>&</sup>lt;sup>1)</sup> See Sec. Matching circuit (p. 6).

Impedance for pass band matching bases on an ideal, perfect matching of the SAW filter to source and to load impedance (here 50 Ω). After removal of the SAW filter the input impedance of the input and output matching network is calculated. The conjugate complex value of these characteristic impedances are the input and output impedances for flat pass band. For more details we refer to RF360 application note #18.



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## 7 Maximum ratings

Operable temperature	T <sub>OP</sub> = −45 °C +125 °C	
Storage temperature	T <sub>STG</sub> <sup>1)</sup> = −45 °C +125 °C	
DC voltage	V <sub>DC</sub>   = 6.0 V	
Source power	$P_{\rm S}$ = 10 dBm	Source impedance 50 Ω.

Not valid for packaging material. Storage temperature for packaging material is −25 °C to +40 °C.



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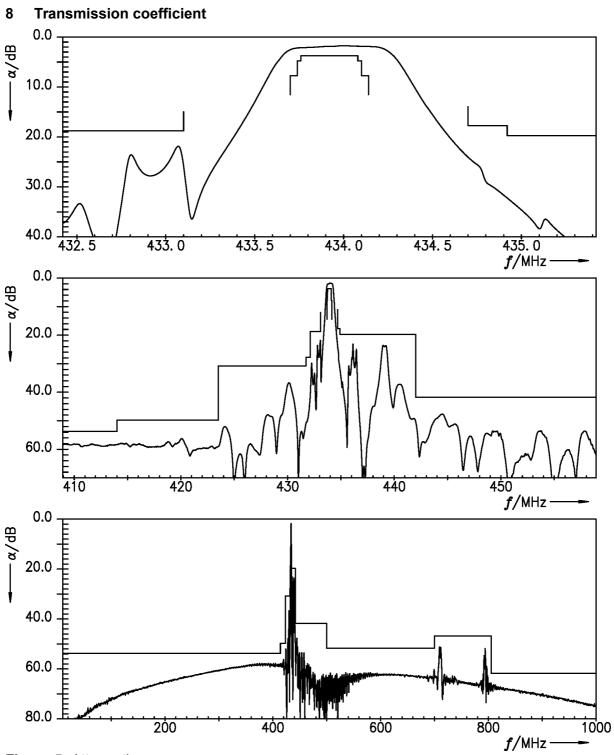


Figure 5: Attenuation.

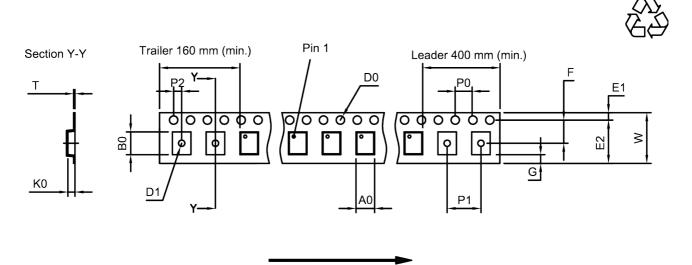


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## 9 Packing material

## 9.1 Tape



User direction of unreeling

Figure 6: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A <sub>0</sub>	4.1±0.1 mm	E	10.25 mm (min.)	P <sub>1</sub>	8.0±0.1 mm
B <sub>0</sub>	4.1±0.1 mm	F	5.5±0.05 mm	P <sub>2</sub>	2.0±0.1 mm
$D_0$	1.5+0.1/-0 mm	G	0.75 mm (min.)	Т	0.3±0.05 mm
D <sub>1</sub>	1.5 mm (min.)	K	1.8±0.1 mm	W	12.0+0.3/-0.1 mm
E <sub>1</sub>	1.75±0.1 mm	P	4.0±0.1 mm		

Table 1: Tape dimensions.



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### 9.2 Reel with diameter of 330 mm

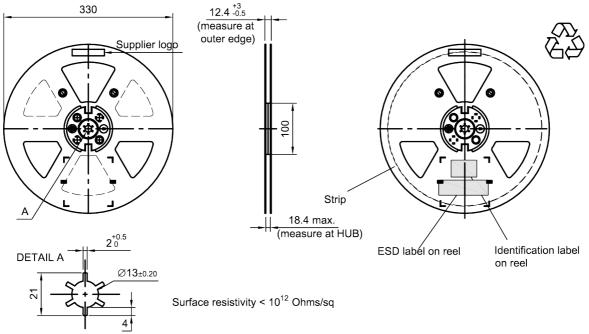


Figure 7: Drawing of reel (first-angle projection) with diameter of 330 mm.

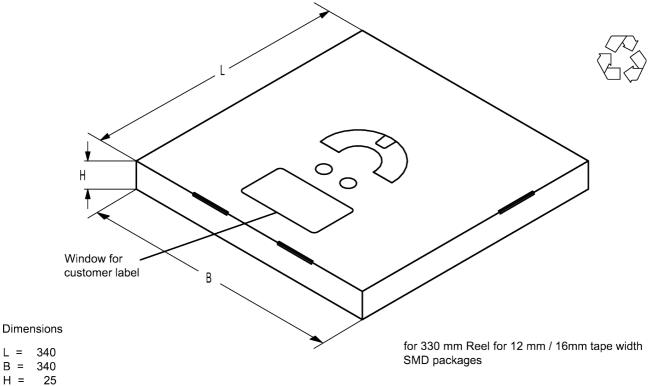


Figure 8: Drawing of folding box for reel with diameter of 330 mm.



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#### 10 Marking

Products are marked with device designation, lot number, as well as production location and date code.

■ Device designation: The 4-character device designation of the ordering code is used for the marking.

Example for 4-character device designation: B3xxxxB1234xxxx

■ Lot number: The last 5 digits of the lot number are used for the marking.

Example: <u>12345</u>

■ Production location and date code: The production location is Wuxi (encoded in the first character 'C'). The production date code is encoded in the last three characters according to Table 2.

1 <sup>st</sup> digit (day)					2 <sup>nd</sup> digit (year)			3 <sup>rd</sup> digit (month)					
Day	Code	Day	Code	Day	Code	Year	Code	Year	Code	Month	Code	Month	Code
1	1	11	Α	21	М	2010	Α	2022	Р	Jan	1	Jul	7
2	2	12	В	22	N	2011	В	2023	R	Feb	2	Aug	8
3	3	13	С	23	Р	2012	С	2024	S	Mar	3	Sep	9
4	4	14	D	24	R	2013	D	2025	Т	Apr	4	Oct	0
5	5	15	E	25	S	2014	E	2026	U	May	5	Nov	N
6	6	16	F	26	Т	2015	F	2027	V	Jun	6	Dec	D
7	7	17	Н	27	U	2016	Н	2028	W				
8	8	18	J	28	V	2017	J	2029	Х				
9	9	19	K	29	W	2018	K	2030	Z				
10	0	20	L	30	Х	2019	L	2031	Α				
				31	Z	2020	М	2032	В				
						2021	N	and	so on				

Table 2: Production date code.

Example of how to decode production location and date code:

Code: C T F 6

Location: C  $\rightarrow$  Wuxi Day: T  $\rightarrow$  26<sup>th</sup> Year: F  $\rightarrow$  2015 Month: 6  $\rightarrow$  June



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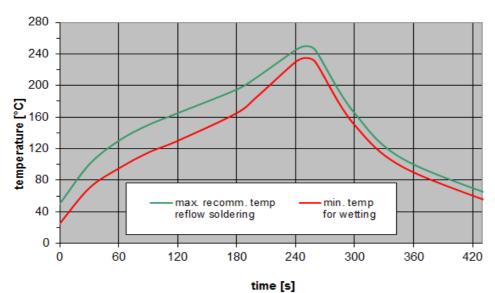
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## 11 Soldering profile

The recommended soldering process is in accordance with IEC  $60068-2-58-3^{rd}$  edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
T > 220 °C	30 s to 70 s
T > 230 °C	min. 10 s
T > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature T <sub>peak</sub>	250 °C +0/-5 °C
wetting temperature $T_{min}$	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).



**Figure 9:** Recommended reflow profile for convection and infrared soldering – lead-free solder.



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#### 12 ESD protection of SAW filters

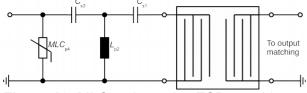
SAW filters are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3<sup>rd</sup> order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.



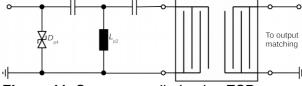
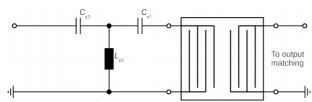


Figure 10: MLC varistor plus ESD matching.

**Figure 11:** Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.



**Figure 12:** 3<sup>rd</sup> order high-pass structure for basic ESD protection.

In all three figures the shunt inductor  $L_{\rm p2}$  could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to RF360 Application report: "**ESD protection for SAW filters**". This report can be found under <a href="https://www.rf360jv.com/rke">www.rf360jv.com/rke</a>. Click on "Applications Notes".



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#### 13 Annotations

## 13.1 Matching coils

See TDK inductor pdf-catalog <a href="http://www.tdk.co.jp/tefe02/coil.htm#aname1">http://www.tdk.co.jp/tefe02/coil.htm#aname1</a> and Data Library for circuit simulation <a href="http://www.tdk.co.jp/etvcl/index.htm">http://www.tdk.co.jp/etvcl/index.htm</a>.

## 13.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

## 13.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.



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## 14 Cautions and warnings

## 14.1 Display of ordering codes for RF360 products

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#### 14.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

### 14.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

#### 14.4 Package information

## Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

#### **Dimensions**

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

#### **Projection method**

Unless otherwise specified first-angle projection is applied.



#### Important notes

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Адрес: 198099, г. Санкт-Петербург, ул. Калинина,

дом 2, корпус 4, литера А.