

# ZXMN3G32DN8 30V SO8 dual N-channel enhancement mode MOSFET

## **Summary**

V <sub>(BR)DSS</sub>	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)
30	0.028 @ V <sub>GS</sub> = 10V	7.1
	0.045 @ V <sub>GS</sub> = 4.5V	5.6



## **Description**

This new generation Trench MOSFET from Zetex features low onresistance and fast switching speed.

#### **Features**

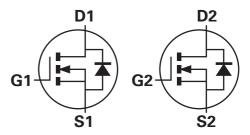
- · Low on-resistance
- · 4.5V gate drive capability
- · Fast switching bullet

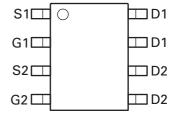
## **Applications**

- DC-DC Converters
- · Power management functions
- Motor Control
- · Backlighting

# Ordering information

DEVICE	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN3G32DN8TA	7	12	500





## **Device marking**

ZXMN

3G32D

## **Absolute maximum ratings**

Parameter	Symbol	Limit	Unit
Drain source voltage	V <sub>DSS</sub>	30	V
Gate source voltage	$V_{GS}$	±20	V
Continous Drain Current @ V <sub>GS</sub> =10; T <sub>A</sub> =25°C <sup>(b)</sup>	I <sub>D</sub>	7.1	Α
@ V <sub>GS</sub> =10; T <sub>A</sub> =70°C <sup>(b)</sup>		5.7	Α
@ V <sub>GS</sub> =10; T <sub>A</sub> =25°C <sup>(a)</sup>		5.5	Α
Pulsed drain current <sup>(c)</sup>	I <sub>DM</sub>	33.6	А
Continuous source current (body diode)(b)	I <sub>S</sub>	3.1	Α
Pulsed source current (body diode)(c)	I <sub>SM</sub>	33.6	Α
Power dissipation at T <sub>A</sub> =25°C <sup>(a)(d)</sup>	$P_{D}$	1.25	W
Linear derating factor		10	mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(a)(e)</sup>	$P_{D}$	1.8	W
Linear derating factor		14	mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(b)(d)</sup>	$P_{D}$	2.1	W
Linear derating factor		17	mW/°C
Operating and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 to 150	°C

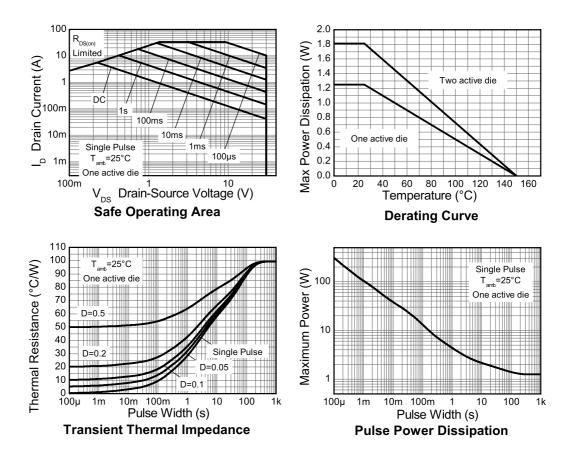
#### Thermal resistance

Parameter	Symbol	Limit	Unit
Junction to ambient <sup>(a)(d)</sup>	$R_{\Theta JA}$	100	°C/W
Junction to ambient <sup>(a)(e)</sup>	$R_{\Theta JA}$	70	°C/W
Junction to ambient <sup>(b)(d)</sup>	$R_{\Theta JA}$	60	°C/W
Junction to lead <sup>(f)</sup>	$R_{\Theta JL}$	51	°C/W

### NOTES:

- (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air
- (b) For a device surface mounted on FR4 PCB measured at  $t \leq 10 \mbox{ sec.}$
- (c) Repetitive rating 25mm x 25mm FR4 PCB, D=0.02, pulse width  $300\mu s$  pulse width limited by maximum junction temperature.
- (d) For a dual device with one active die.
- (e) For a device with two active die running at equal power.
- (f) Thermal resistance from junction to solder-point (at end of drain lead).

#### Thermal characteristics



# Electrical characteristics (at T<sub>amb</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Static				.1	I	1	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	30			V	$I_{D}$ = 250 $\mu$ A, $V_{GS}$ =0V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			0.5	μА	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V	
Gate-Body Leakage	I <sub>GSS</sub>			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	1.0		3.0	V	$I_D$ = 250 $\mu$ A, $V_{DS}$ = $V_{GS}$	
Static Drain-Source On-State Resistance (*)	R <sub>DS(on)</sub>			0.028 0.045	Ω Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6.0A V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.9A	
Forward Transconductance <sup>(*)(†)</sup>	9 <sub>fs</sub>		12		S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 6.0A	
Dynamic (†)						1	
Input Capacitance	C <sub>iss</sub>		472		pF		
Output Capacitance	C <sub>oss</sub>		178		pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> =0V f=1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		65		pF	_ 1= IIVID2	
Switching (‡)(†)				1		-	
Turn-On-Delay Time	t <sub>d(on)</sub>		2.5		ns		
Rise Time	t <sub>r</sub>		3.1		ns	$V_{DD}$ = 15V, $I_{D}$ = 1A - $R_{G} \approx 6.0\Omega$ , $V_{GS}$ =10V	
Turn-Off Delay Time	t <sub>d(off)</sub>		14		ns	- n <sub>G</sub> ≅ 0.052, v <sub>GS</sub> =10 v	
Fall Time	t <sub>f</sub>		9.7		ns		
Total Gate Charge	$O_g$		10.5		nC	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 10V	
Gate-Source Charge	Q <sub>gs</sub>		1.86		nC	I <sub>D</sub> = 6A	
Gate Drain Charge	Q <sub>gd</sub>		2.3		nC		
Source-drain diode			I	1	I		
Diode Forward Voltage <sup>(*)</sup>	V <sub>SD</sub>		0.68	1.2	V	$T_j$ =25°C, $I_S$ = 1.7A, $V_{GS}$ =0V	

## NOTES:

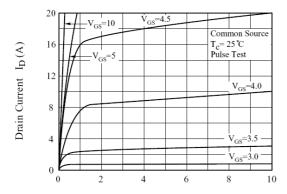
<sup>(\*)</sup> Measured under pulsed conditions. Pulse width  $\leq 300 \mu s;$  duty cycle  $\leq\!\!2\%.$ 

<sup>(†)</sup> For design aid only, not subject to production testing

<sup>(‡)</sup> Switching characteristics are independent of operating junction temperature.

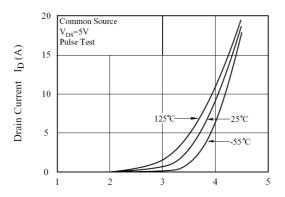
## **Typical characteristics**

Fig1.  $I_D$  -  $V_{DS}$ 



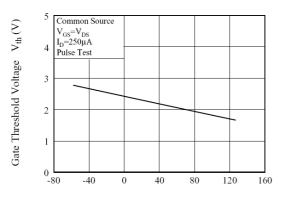
Drain - Source Voltage  $V_{DS}(V)$ 

Fig3. 
$$I_D$$
 -  $V_{GS}$ 



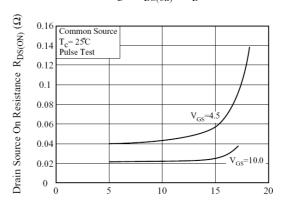
Gate - Source Voltage  $V_{GS}(V)$ 

Fig5. V<sub>th</sub> - T<sub>j</sub>



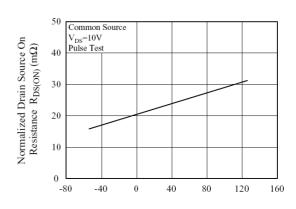
Junction Temperature Tj (°C)

Fig2. R<sub>DS(on)</sub> - I<sub>D</sub>



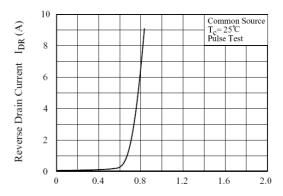
Drain Current I<sub>D</sub> (A)

Fig4. R<sub>DS(on)</sub> - T<sub>j</sub>



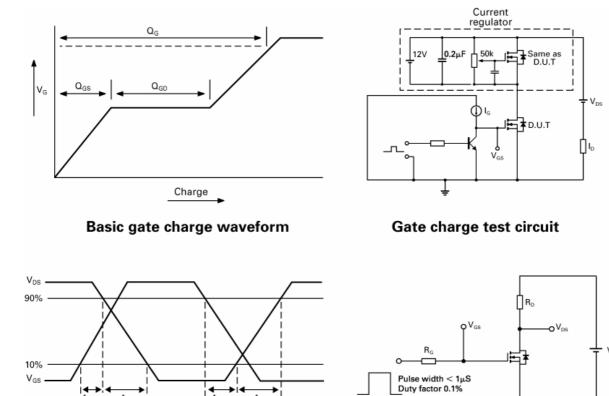
Junction Temperature Tj (°C)

Fig6.  $I_{DR}$  -  $V_{SDF}$ 



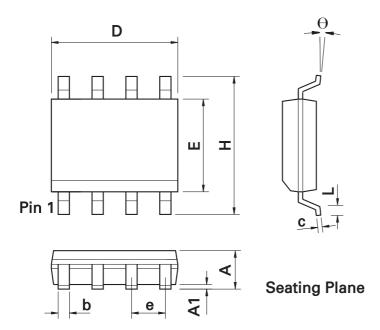
Source - Drain Forward Voltage  $V_{SDF}(V)$ 

## **Test circuits**



Switching time test circuit

# Package outline - SO8



DIM	Inc	hes	Millin	neters	DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
Α	0.053	0.069	1.35	1.75	е	0.050	BSC	1.27	BSC
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°
Е	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Note: Controlling dimensions are in inches. Approximate dimensions are provided in millimeters

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