# RENESAS

## DATA SHEET

## PHOTOCOUPLER

# PS8501,PS8501L1,PS8501L2,PS8501L3

## HIGH SPEED ANALOG OUTPUT TYPE 8 mm CREEPAGE 8-PIN PHOTOCOUPLER -NEPOC Series-

#### DESCRIPTION

The PS8501, PS8501L1, PS8501L2 and PS8501L3 are 8-pin high speed photocouplers containing a GaAlAs LED on input side and a PN photodiode and a high speed amplifier transistor on output side on one chip. The PS8501 is in a plastic DIP (Dual In-line Package) with 8 mm creepage distance product.

The PS8501L1 is lead bending type for long creepage distance.

The PS8501L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

The PS8501L3 is lead bending type (Gull-wing) for surface mounting.

#### **FEATURES**

- Long creepage distance (8 mm MIN.: PS8501L1, PS8501L2)
- High supply voltage (Vcc = 35 V MAX.)
- High speed response (tphL, tplH = 0.8  $\mu$ s MAX.)
- High isolation voltage (BV = 5 000 Vr.m.s.)
- TTL, CMOS compatible with a resistor
- Ordering number of tape product: PS8501L2-E3: 1 000 pcs/reel
  - : PS8501L3-E3: 1 000 pcs/reel
- Pb-Free product
- Safety standards
  - UL approved: No. E72422
  - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
  - BSI approved: No. 8937, 8938
  - SEMKO approved: No. 615433
  - NEMKO approved: No. P06207243
  - DEMKO approved: No. 314091
  - FIMKO approved: No. FI 22827

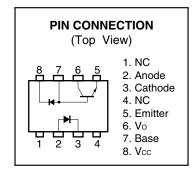
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DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40019182 (Option)

#### APPLICATIONS

- Interface for measurement or control equipment
- · Substitutions for relays and pulse transformers
- Modem, communications device
- General purpose inverter



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Document No. PN10656EJ04V0DS (4th edition) Date Published September 2009 NS Printed in Japan

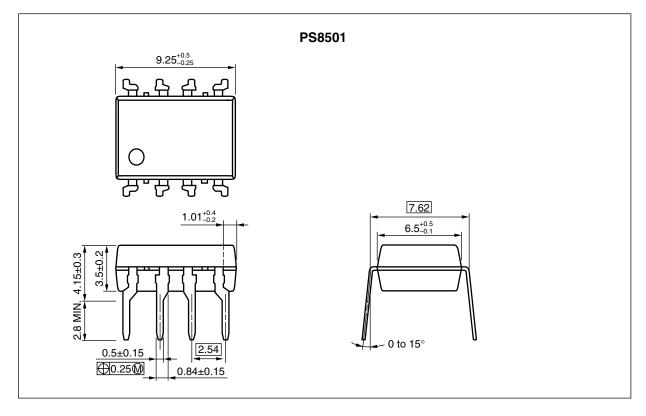
The mark <R> shows major revised points. © NEC Electronic Structure Structur

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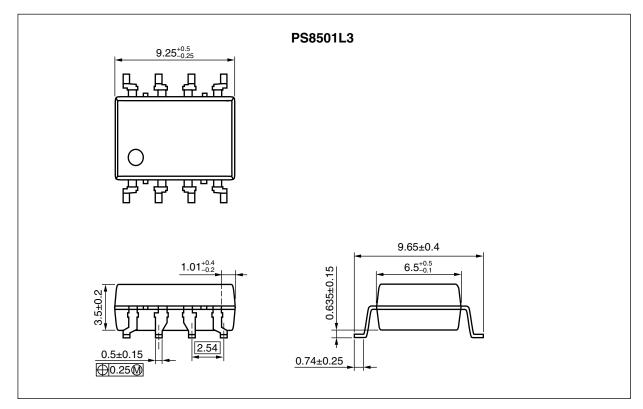
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

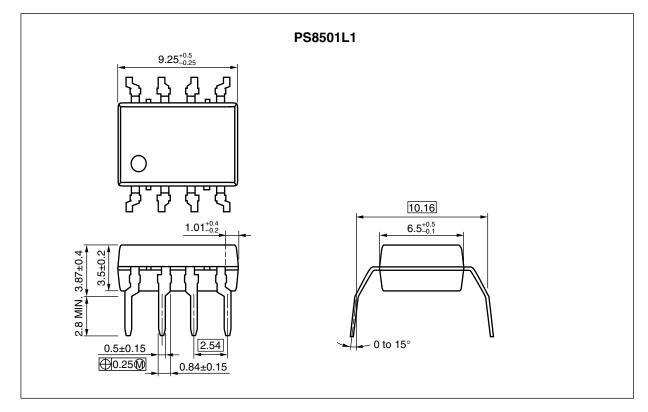
<R> PACKAGE DIMENSIONS (UNIT: mm)

## **DIP Type**



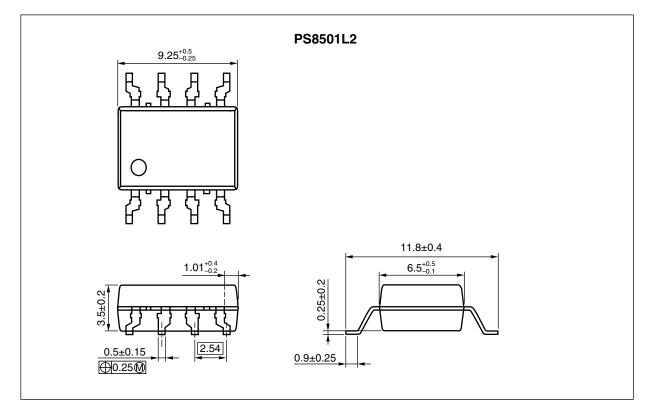
## Lead Bending Type (Gull-wing) For Surface Mount





Lead Bending Type For Long Creepage Distance

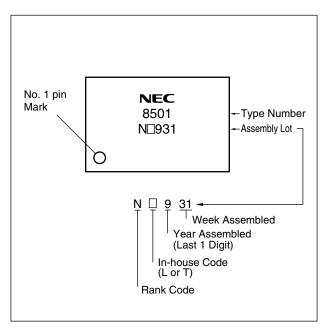
Lead Bending Type For Long Creepage Distance (Gull-wing) For Surface Mount



## PHOTOCOUPLER CONSTRUCTION

Parameter	PS8501, PS8501L3	PS8501L1, PS8501L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

## <R> MARKING EXAMPLE



Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number* <sup>1</sup>
PS8501	PS8501-AX	Pb-Free	Magazine case 50 pcs	Standard products	PS8501
PS8501L1	PS8501L1-AX	(Ni/Pd/Au)		(UL, CSA, BSI,	PS8501L1
PS8501L2	PS8501L2-AX			SEMKO, NEMKO,	PS8501L2
PS8501L3	PS8501L3-AX			DEMKO, FIMKO	PS8501L3
PS8501L2-E3	PS8501L2-E3-AX		Embossed Tape 1 000 pcs/reel	approved)	PS8501L2
PS8501L3-E3	PS8501L3-E3-AX				PS8501L3
PS8501-V	PS8501-V-AX		Magazine case 50 pcs	DIN EN60747-5-2	PS8501
PS8501L1-V	PS8501L1-V-AX			(VDE0884 Part2)	PS8501L1
PS8501L2-V	PS8501L2-V-AX			Approved (Option)	PS8501L2
PS8501L3-V	PS8501L3-V-AX				PS8501L3
PS8501L2-V-E3	PS8501L2-V-E3-AX		Embossed Tape 1 000 pcs/reel		PS8501L2
PS8501L3-V-E3	PS8501L3-V-E3-AX				PS8501L3

### ORDERING INFORMATION

\*1 For the application of the Safety Standard, following part number should be used.

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current <sup>*1</sup>	lf	25	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	35	v
	Output Voltage	Vo	35	V
	Output Current	lo	8	mA
	Power Dissipation <sup>2</sup>	Pc	100	mW
Isolation	Voltage <sup>**</sup>	BV	5 000	Vr.m.s.
Operating	g Ambient Temperature	TA	–55 to +100	°C
Storage	Temperature	Tstg	-55 to +125	°C

\*1 Reduced to 0.33 mA/°C at  $T_A = 70$ °C or more.

**\*2** Reduced to 2.0 mW/°C at  $T_A = 75^{\circ}C$  or more.

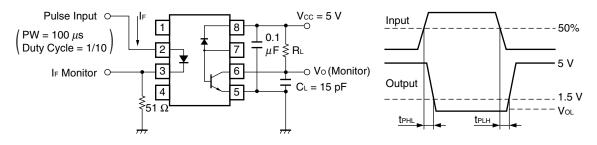
\*3 AC voltage for 1 minute at  $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-4 shorted together, 5-8 shorted together. <R>

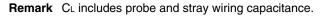
## ELECTRICAL CHARACTERISTICS (TA = 25°C)

	Parameter	Symbol	Conditions	MIN.	TYP. <sup>*1</sup>	MAX.	Unit
Diode	Forward Voltage	VF	I⊧ = 16 mA		1.7	2.2	V
	Reverse Current	IR	V <sub>R</sub> = 3 V			10	μA
	Forward Voltage Temperature Coefficent	<i>Δ</i> Vf/ <i>Δ</i> Ta	l⊧ = 16 mA		-2.1		mV/°C
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz		30		pF
Detector	High Level Output Current	Іон (1)	I⊧ = 0 mA, Vcc = Vo = 5.5 V		3	500	nA
	High Level Output Current	Іон (2)	I⊧ = 0 mA, Vcc = Vo = 35 V			100	μA
	Low Level Output Voltage	Vol	I⊧ = 16 mA, Vcc = 4.5 V, Io = 2.4 mA		0.15	0.4	V
	Low Level Supply Current	ICCL	I⊧ = 16 mA, V₀ = Open, Vcc = 35 V		150		μA
	High Level Supply Current	Іссн	I⊧ = 0 mA, Vo = Open, Vcc = 35 V		0.01	1	μA
	DC Current Gain	hfe	Vo = 5 V, Io = 3 mA		65		
Coupled	Current Transfer Ratio	CTR	IF = 16 mA, Vcc = 4.5 V, Vo = 0.4 V	15			%
	Isolation Resistance	<b>R</b> I-0	VI-O = 1 kVDC	10 <sup>11</sup>			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1 MHz		0.7		pF
	Propagation Delay Time $(H \rightarrow L)^{2}$	tрнL	$I_F = 16 \text{ mA}, \text{ Vcc} = 5 \text{ V}, \text{ R}_L = 1.9 \text{ k}\Omega$		0.22	0.8	μs
	Propagation Delay Time $(L \rightarrow H)^{2}$	tplh	I⊧ = 16 mA, Vcc = 5 V, R∟ = 1.9 kΩ		0.35	0.8	μs

\*1 Typical values at  $T_A = 25^{\circ}C$ 

\*2 Test circuit for propagation delay time

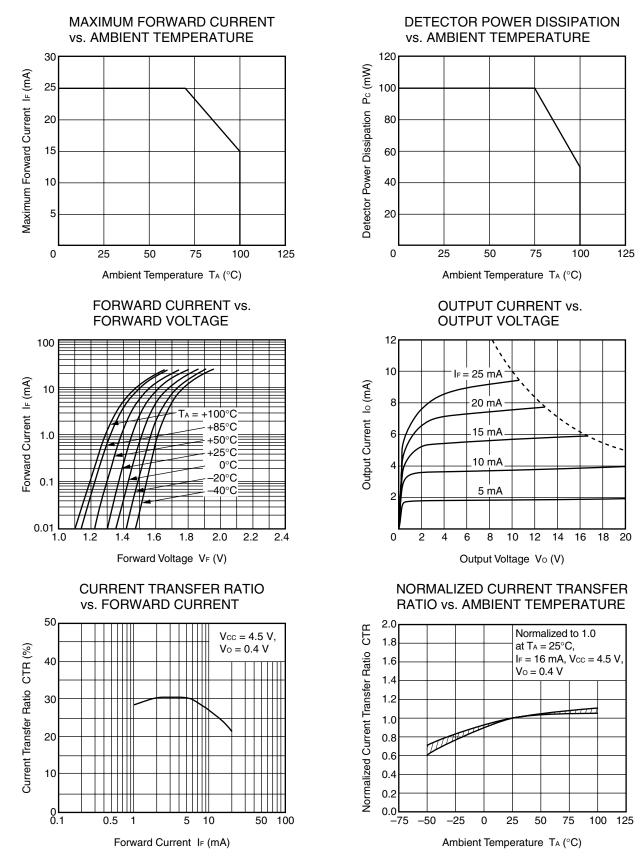




### **USAGE CAUTIONS**

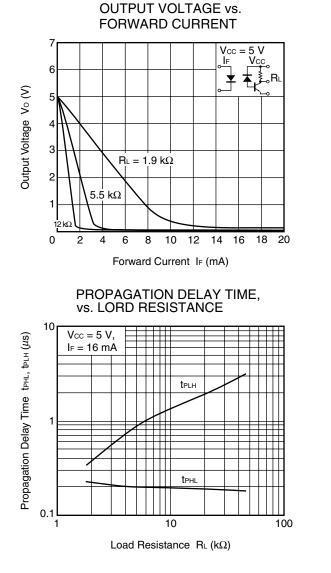
- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of more than 0.1  $\mu$ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- <R>
- 3. Pins 1, 4 (which is an NC<sup>1</sup> pin) can either be connected directly to the GND pin on the LED side or left open. Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.
  - \*1 NC: Non-Connection (No Connection)
  - 4. Avoid storage at a high temperature and high humidity.

### <R> TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)

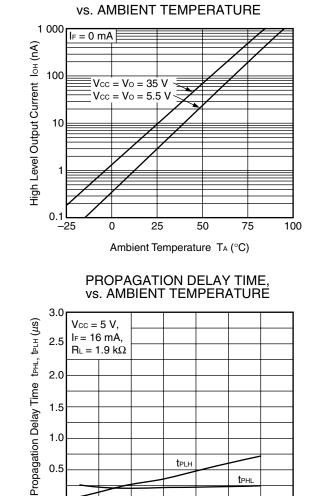


Remark The graphs indicate nominal characteristics.

HIGH LEVEL OUTPUT CURRENT



Remark The graphs indicate nominal characteristics.



Ambient Temperature TA (°C)

50

75

100

125

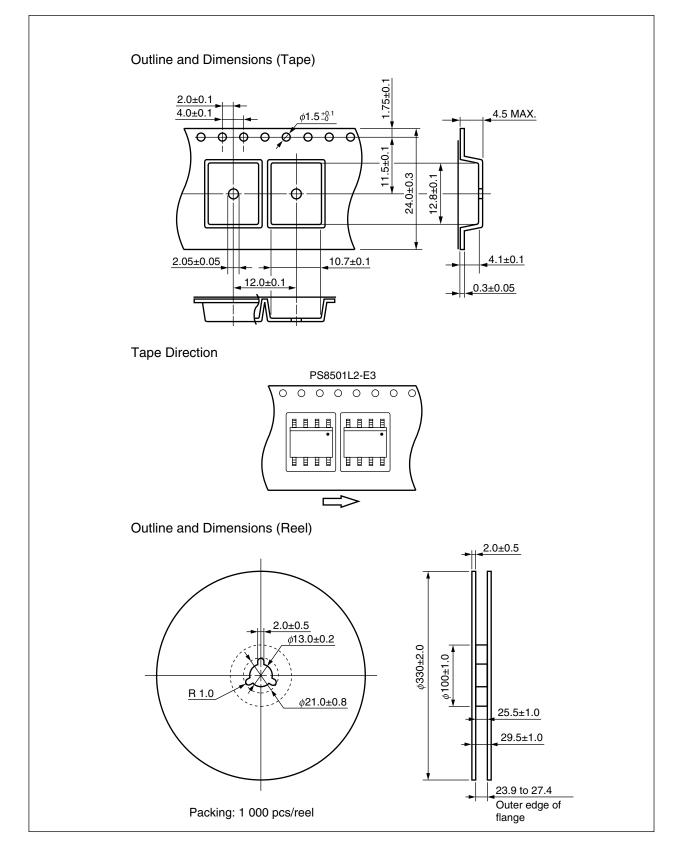
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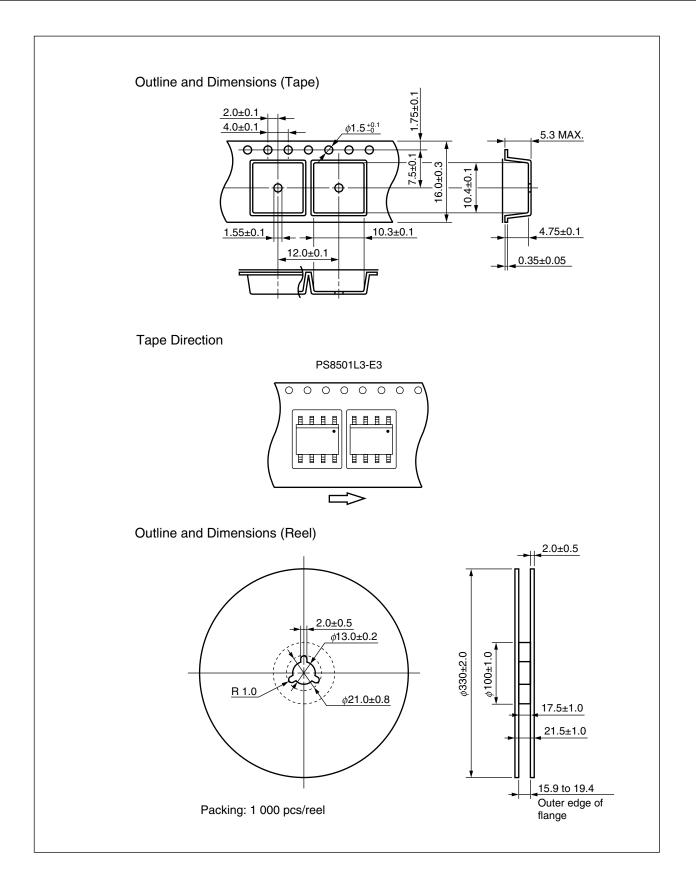
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-25

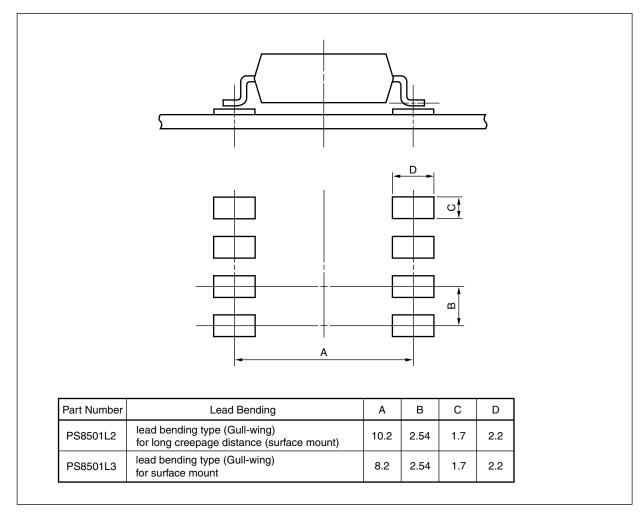
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## TAPING SPECIFICATIONS (UNIT: mm)





## RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



## NOTES ON HANDLING

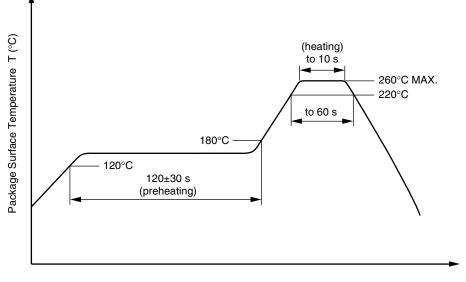
#### 1. Recommended soldering conditions

#### (1) Infrared reflow soldering

- Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

#### Recommended Temperature Profile of Infrared Reflow



Time (s)

#### (2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

#### (3) Soldering by soldering iron

<ul> <li>Peak temperature (lead part temperature)</li> </ul>	350°C or below
<ul> <li>Time (each pins)</li> </ul>	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.
- (b) Please be sure that the temperature of the package would not be heated over 100°C.

#### (4) Cautions

## • Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

### 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between Vccemitters at startup, the output side may enter the on state, even if the voltage is within the absolute maximum ratings.

## <R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}, P_d < 5 \text{ pC}$	Uiorm Upr	1 130 1 695	V <sub>peak</sub> V <sub>peak</sub>
Test voltage (partial discharge test, procedure b for all devices) $U_{\text{pr}}$ = 1.875 $\times$ U_{IORM}, $P_{\text{d}}$ < 5 pC	Upr	2 119	$V_{peak}$
Highest permissible overvoltage	Utr	8 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at } T_A \text{ MAX. at least } 100^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 <sup>12</sup> 10 <sup>11</sup>	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
$V_{IO} = 500 \text{ V dc at } T_A = Tsi$	Ris MIN.	10 <sup>9</sup>	Ω

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M8E0904E

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	• Do not lick the product or in any way allow it to enter the mouth.

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 PS8501-AX
 PS8501L1-AX

 PS8501L3-V-E3-AX
 PS8501L2-V-AX
 PS8501L2-V-AX
 PS8501L2-V-AX
 PS8501L3-V-AX
 PS8501L1-AX



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- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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