# RICOH

### 1000 mA Buck-Boost DC/DC Converter with Synchronous Rectifier

NO. EA-333-180618

### OUTLINE

The RP601x is a CMOS-based 1000 mA buck-boost DC/DC converter with synchronous rectifier. Internally, the RP601x consists of an oscillator circuit, a reference voltage unit, an error amplifier circuit, a switch control circuit, a mode control circuit, a soft-start circuit, an undervoltage lockout (UVLO) circuit, an overcurrent protection circuit, a thermal shutdown circuit and switching transistors.

The RP601x provides the forced PWM control or the PWM/ VFM auto switching control. The forced PWM control switches at fixed frequency rate in low output current in order to reduce noise. Likewise, the PWM/ VFM auto switching control automatically switches from PWM mode to VFM mode in low output current in order to achieve high efficiency. RICOH's unique control method can suppress a ripple voltage in the VFM mode, thus the RP601x can achieve both low ripple voltage at light load and high efficiency.

The RP601x can be switched into forced bypass mode in PWM/ VFM auto switching control by pulling the BP pin high. The RP601x features forced bypass mode with a typical 5  $\mu$ A current consumption that can maximize the battery life.

The RP601x is offered in a 16-bump WLCSP package measuring 1.95 mm x 1.95 mm which can achieve the smallest possible footprint solution on board where area is limited.

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### FEATURES

- Input Voltage Range (Maximum Rating)...... 2.3 V to 5.5 V (6.5 V)
- Output Voltage Range...... 2.75 V to 4.2 V
- Output Step Voltage...... Typ. 50 mV
- Output Voltage Setting ...... Single-wire (S-Wire) interface<sup>(1)</sup>
- Output Voltage Accuracy...... PWM mode = ±2%, VFM mode = PWM mode + 2%
- Output Voltage Temperature Coefficient ...... Typ. ±50 ppm/°C
- Line Regulation ...... Typ. 0.5% (PWM mode)
- Load Regulation ...... Typ. 0.5% at Iout = 10 mA to 800 mA (PWM mode)
- Maximum Output Current ...... Typ. 1.0 A
- Output Pulsed Current ...... Max. 2.0 A
- (t = 10 ms, On Duty = 0.1 (ton = 1 ms), V<sub>IN</sub> = 3.6 V, V<sub>SET</sub> = 3.3 V)
- BULX Current Limit ...... Typ. 4.0 A
- Oscillator Frequency...... Typ. 2.4 MHz
- Built-in Driver ON Resistance ...... Typ. Pch. 80 m $\Omega$ , Nch. 80 m $\Omega$  (V<sub>IN</sub> = 3.6 V)
- Device Quiescent Current...... Typ. 45 µA (VFM mode)
- Standby Current...... Max. 5 μA
- Quiescent Current in Forced Bypass Mode.......... Typ. 5 µA
- UVLO Detector Threshold...... Typ. 2.1 V
- Soft-start Function
- Power Good Function
- Thermal Shutdown
- Package ...... WLCSP-16-P1, 1.95 mm x 1.95 mm

### APPLICATIONS

- Power source for portable equipment such as laptops, PDAs, DSCs, cellular phones and smartphones
- Power source for Li-ion battery-used equipment

<sup>&</sup>lt;sup>(1)</sup> The default set output voltage (Default V<sub>SET</sub>) is set by Fix Trimming (selectable from 2.75 V to 4.20 V in 0.05 V step)



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### **SELECTION GUIDE**

#### **Selection Guide**

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
RP601Zxxx*-E2-F	WLCSP-16-P1	5,000 pcs	Yes	Yes

xxx: Specify the default set output voltage (Default V<sub>SET</sub>) from 2.75 V (275) to 4.20 V (420) in 0.05 V step.

\*: Specify the auto-discharge function <sup>(1)</sup> option.

- A: Auto-discharge function included
- B: Auto-discharge function not included

**BLOCK DIAGRAM** 



<sup>&</sup>lt;sup>(1)</sup>Auto-discharge function quickly lowers the output voltage to 0 V by releasing the electrical charge accumulated in the <u>external capacitor when the chip enable signal is switched from the active mode to the standby mode</u>.



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### **PIN DESCRIPTION**



### RP601x (WLCSP-16-P1) Pin Configuration

### **RP601x Pin Description**

Pin No	Symbol	Pin Description
C3, C4	BOLX	Boost Switching Output LX
B3, B4	BULX	Buck Switching Output LX
D1	PG	Power Good Output Pin, Nch Open Drain
C1	CE	Chip Enable and S-Wire Control Input Pin
B1	BP	Forced Bypass Mode Setting Input Pin Forced Bypass Mode: "H"
A1	MODE	Mode Control Pin Forced PWM Control: "H" PWM/VFM Auto Switching Control: "L"
D3, D4	Vout	Buck-Boost Output Pin
A3	AV <sub>IN</sub>	Analog Power Input Pin
A4	PV <sub>IN</sub>	Power Input Pin
A2, B2, C2, D2	GND	Power GND Pin

Same symbol pins must be connected together (BOLX (C3 and C4), BULX (B3 and B4), and VOUT (D3 and D4). The AVIN (A3) and PVIN (A4) pins must be connected together in the application. GND must be tied to the ground in the application (A2, B2, C2, and D2).

#### Pin Truth Table

CE Pin	MODE Pin <sup>*1</sup>	BP Pin	Operation
"L"	-	- OFF	
	"["	"L"	PWM/ VFM Auto Switching Control Mode
"H"	L	"H"	Forced Bypass Mode
п			Forced PWM Control Mode
	"H"	"H" <sup>*2</sup>	Forced Bypass Mode

\*1 The logic to the MODE pin should not be changed while CE = "H".

\*2 The logic to the BP pin should not be changed while MODE = "H" and CE = "H".

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### ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
V <sub>IN</sub>	AV <sub>IN</sub> / PV <sub>IN</sub> Input Voltage	-0.3 to 6.5	V
V <sub>LX</sub>	Lx Pin (BULX, BOLX) Voltage	-0.3 to V <sub>IN</sub> + 0.3	V
VCE	CE Pin Input Voltage	-0.3 to 6.5	V
V <sub>MODE</sub>	MODE Pin Input Voltage	-0.3 to 6.5	V
V <sub>BP</sub>	BP Pin Input Voltage	-0.3 to 6.5	V
Vout	Vout Pin Voltage	-0.3 to 6.5	V
PD	Power Dissipation <sup>(1)</sup> (WLCSP-16-P1, JEDEC STD. 51-9 Test Land Pattern)	1400	mW
Tj	Junction Temperature Range	-40 to 125	°C
Tstg	Storage Temperature Range	-55 to 125	°C

### ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

### **RECOMMENDED OPERATING CONDITIONS**

#### **Recommended Operating Conditions**

Symbol	Item	Rating	Unit
VIN	Input Voltage	2.3 to 5.5	V
Та	Operating Temperature	-40 to 85	°C

#### **RECOMMENDED OPERATING CONDITIONS**

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

<sup>&</sup>lt;sup>(1)</sup> Refer to POWEWR DISSIPATION for detailed information.

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### **ELECTRICAL CHARACTERISTICS**

### **RP601x Electrical Characteristics**

(Ta = 25°C)

Symbol	ltem		Condi	tions	Min.	Тур.	Max.	Unit
				V <sub>MODE</sub> = 0 V		35	60	μA
I <sub>DD</sub>	Quiescent Current	Vin	$V_{IN} = 5.5 V,$ $V_{SET} = 3.3 V,$	V <sub>MODE</sub> = 5.5 V		800		μA
		Vout	I <sub>оυт</sub> = 0 mA			10		μA
I <sub>DD-BP</sub>	Quiescent Current in Forced Bypass Mode		V <sub>IN</sub> = 3.6 V, I <sub>OL</sub>	<sub>JT</sub> = 0 mA		5.0		μA
Istandby	Standby Current		V <sub>IN</sub> = 5.5 V, V <sub>C</sub>	<sub>E</sub> = 0 V		0.1	5.0	μA
	Range of Output Voltag	je	Adjustable in 0	.05 V step	2.75		4.2	V
			PWM mode V	<sub>N</sub> = 4.2 V	-2		2	%
Vout	Output Voltage Accura	су	VFM mode (Refer to OUT) VOLTAGE AC (PWM MODE/	CURACY		PWM mode + 2.0		%
ΔV <sub>ουτ</sub> /ΔTa	Feedback Output Voltage Temperature Coefficient		-40°C ≤ Ta ≤ 85°C			±50		ppm/ °C
$\Delta V_{OUT}$ / $\Delta V_{IN}$	Line Regulation		$V_{SET} = 3.3 V,$ 3.2 V $\leq V_{IN} \leq 4$ Iout = 300 mA,	,		0.5		%
ΔVουτ /ΔΙουτ	Load Regulation		V <sub>IN</sub> = 3.6 V, V <sub>S</sub> 10 mA ≤ I <sub>OUT</sub> ≤ MODE = "H"			0.5		%
fosc	Switching Frequency		V <sub>IN</sub> = 4.2 V		2.16	2.4	2.64	MHz
ILIMHS	BULX Current Limit (1)		V <sub>IN</sub> = 3.6 V			4.0		А
ILIMSH	Short Protection Currer	nt <sup>(1)</sup>	V <sub>IN</sub> = 3.6 V, V <sub>C</sub>	<sub>UT</sub> = 0 V		1.2		А
VSHORT	Short Protection Detector Voltage		V <sub>OUT</sub> = falling			1.6		V
Ronp	On Resistance of Pch Tr.		V <sub>IN</sub> = 3.6 V, I <sub>Lx</sub> = -100 mA			80		mΩ
Ronn	On Resistance of Nch Tr.		V <sub>IN</sub> = 3.6 V, I <sub>Lx</sub> = -100 mA			80		mΩ
Rdis	On Resistance of Disch (RP601xxxxA.)	narge Tr.	V <sub>IN</sub> = 3.6 V, V <sub>C</sub>	<sub>E</sub> = 0 V		8.0		Ω

<sup>&</sup>lt;sup>(1)</sup> BULX Current Limit and Short Protection Current vary according to the switching duty ratio. 6

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### **ELECTRICAL CHARACTERISTICS (continued)**

RP601x El	ectrical Characteristics				(Ta	= 25°C)
Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
I <sub>PGLK</sub>	PG Pin Leakage Current	$V_{IN} = V_{CE} = 5.5 V,$ $V_{PG} = 5.5 V$	-1	0	1	μA
VCEH	CE Input "H" Voltage	V <sub>IN</sub> = 5.5 V	1.0			V
V <sub>CEL</sub>	CE Input "L" Voltage	V <sub>IN</sub> = 2.3 V			0.4	V
V <sub>MODEH</sub>	MODE "H" Input Voltage	V <sub>IN</sub> = 5.5 V	1.0			V
VMODEL	MODE "L" Input Voltage	V <sub>IN</sub> = 2.3 V			0.4	V
VBPH	BP Input "H" Voltage	V <sub>IN</sub> = 5.5 V	1.0			V
V <sub>BPL</sub>	BP Input "L" Voltage	V <sub>IN</sub> = 2.3 V			0.4	V
Ін	CE, MODE & BP Input "H" Current	$V_{\text{IN}} = V_{\text{CE}} = V_{\text{BP}} = V_{\text{MODE}}$ $= 5.5 \text{ V}$	-1	0	1	μA
١L	CE, MODE & BP Input "L" Current	$V_{CE} = V_{BP} = V_{MODE} = 0 V$ $V_{IN} = 5.5 V$	-1	0	1	μA
Maxduty	Maximum Duty Cycle at Boost- Mode			85		%
VUVLOD	UVLO Detector Threshold	V <sub>IN</sub> = falling	2.0	2.1		V
VUVLOR	UVLO Released Threshold	V <sub>IN</sub> = rising		V <sub>UVLOD</sub> + 0.1	2.3	V
T <sub>TSD</sub>	Thermal Shutdown Temperature	Junction Temperature		140		°C
T <sub>TSR</sub>	Thermal Shutdown Released Temperature	Junction Temperature		100		°C
tstart	Soft-start Time <sup>(1)</sup>	V <sub>SET</sub> = 3.3 V		350		μs
Vpg	PG Output "L" Voltage	I <sub>PG</sub> = + 1 mA			0.4	V

All test items listed under ELECTRICAL CHARACTERISTICS are done under the pulse load condition (Tj ≈ Ta = 25°C) except Feedback Output Voltage Temperature Coefficient.

Test circuit is "OPEN LOOP" and GND = 0 V unless otherwise specified.

<sup>&</sup>lt;sup>(1)</sup>The inclination of the output voltage (VOUT) during soft-start time is constant and not depending on the default set output voltage (Default VSET) but the soft-start time can be changed depending on Default VSET. Default VSET is set by Fix Trimming (2.75 V to 4.20 V. in 0.05 V step).

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	Vau	r (PWM mode	(Ta = 25°C)
Product name	MIN.	TYP.	MAX.
RP601Z275x	2.695	2.750	2.805
RP601Z280x	2.744	2.800	2.856
RP601Z285x	2.793	2.850	
			2.907
RP601Z290x	2.842	2.900	2.958
RP601Z295x	2.891	2.950	3.009
RP601Z300x	2.940	3.000	3.060
RP601Z305x	2.989	3.050	3.111
RP601Z310x	3.038	3.100	3.162
RP601Z315x	3.087	3.150	3.213
RP601Z320x	3.136	3.200	3.264
RP601Z325x	3.185	3.250	3.315
RP601Z330x	3.234	3.300	3.366
RP601Z335x	3.283	3.350	3.417
RP601Z340x	3.332	3.400	3.468
RP601Z345x	3.381	3.450	3.519
RP601Z350x	3.430	3.500	3.570
RP601Z355x	3.479	3.550	3.621
RP601Z360x	3.528	3.600	3.672
RP601Z365x	3.577	3.650	3.723
RP601Z370x	3.626	3.700	3.774
RP601Z375x	3.675	3.750	3.825
RP601Z380x	3.724	3.800	3.876
RP601Z385x	3.773	3.850	3.927
RP601Z390x	3.822	3.900	3.978
RP601Z395x	3.871	3.950	4.029
RP601Z400x	3.920	4.000	4.080
RP601Z405x	3.969	4.050	4.131
RP601Z410x	4.018	4.100	4.182
RP601Z415x	4.067	4.150	4.233
RP601Z420x	4.116	4.200	4.284

### **RP601Z Product-specific Electrical Characteristics**

### THEORY OF OPERATION

### SOFT-START AND SET OUTPUT VOLTAGE (VSET) ADJUSTMENT

The default set output voltage (Default  $V_{SET}$ ) is set in the range of 2.75 V to 4.20 V in 0.05 V step by Fix Trimming. The inclination of soft-start time is constant and not depending on Default  $V_{SET}$  but the soft-start time can be changed depending on Default  $V_{SET}$ .



**RP601x Timing Chart** 

### UNDERVOLTAGE LOCKOUT (UVLO) CIRCUIT

If the input voltage ( $V_{IN}$ ) becomes lower than the UVLO detector threshold ( $V_{UVLOD}$ ), the UVLO circuit starts to operate, and P-channel and N-channel built-in switch transistors turn off. To restart the operation,  $V_{IN}$  needs to be higher than the UVLO released voltage ( $V_{UVLOR}$ ).

### **OVERCURRENT PROTECTION CIRCUIT**

Overcurrent protection circuit supervises the inductor peak current (the peak current flowing through P-channel built-transistor) in each switching cycle, and if the peak current exceeds the BULX current limit ( $I_{LIMHS}$ ), it turns off P-channel transistor.  $I_{LXLIM}$  of the RP601x is set to Typ.4 A.

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### SHORT PROTECTION CIRCUIT

If the output voltage ( $V_{OUT}$ ) becomes lower than a short-circuit protection detection voltage ( $V_{SHORT}$ ), the BULX current limit ( $I_{LIMHS}$ ) will be reduced to the short-circuit protection current ( $I_{LIMSH}$ ) of typically 1.2A.



**Overcurrent Protection & Short Protection** 

### POWER GOOD (PG) FUNCTION

The RP601x contains a power good function using Nch open drain. If any abnormal condition is detected, the power good function turns Nch transistor on and switches the PG pin to low. If the cause of the abnormal condition is removed, the power good function turns Nch transistor off and switches the PG pin back to high. The followings are the abnormal conditions that the power good function can detect.

- CE = "L" (Shutdown)
- UVLO (Shutdown)
- Thermal Shutdown
- Short Protection Detection

Notes: The PG pin outputs "L" during soft-start.

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### SINGLE WIRE (S-WIRE) PULSE SIGNAL FIGURE



### Single Wire (S-Wire) Pulse Signal Figure

Symbol	ltem	Conditions	Min.	Тур.	Max.	Unit
tp_hi	S-Wire High Time		4	-	50	μs
tp_low	S-Wire Low Time		4	-	50	μs
tp_stop	S-Wire Setting Stop Time		120			μs
tp_off	S-Wire Setting Off Time		120			μs
tstart_pg	PG Output Delay Time	Starts from the completion point of tstart		32		μs
tr_ana	V <sub>OUT</sub> Change Time	Starts from the completion of of tp_stop 1 step-up/ down (1 step = 50 mV)		3		μs

S-Wire pulse signal input is prohibited during the following two conditions:

1. During soft-start time (tstart)

2. During VOUT change time (tr\_ana) + S-Wire setting stop time (tp\_stop)

The required transition time for each condition are listed below:

1. tstart = max. 500 µs (@4.2 V)

2. tr\_ana + tp\_stop + Stable time for Circuit = 470  $\mu$ s (@2.75 V  $\leftrightarrow$  4.2 V)

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### OUTPUT VOLTAGE ACCURACY (PWM MODE/ VFM MODE)

The output voltage ( $V_{OUT}$ ) in the VFM mode is typically 2% higher than  $V_{OUT}$  in the PWM mode.



Output Voltage Accuracy (PWM Mode/ VFM Mode)

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FORCED BYPASS MODE (PWM/ VFM Auto Switching Control)

In the forced bypass mode, the switching operation is stopped which makes the output voltage ( $V_{OUT}$ ) same level as the input voltage ( $V_{IN}$ ) and suppresses the supply current to typically 5  $\mu$ A.

In the PWM/ VFM auto switching control, the device can be switched into the forced bypass mode by setting the BP pin "H".



Forced Bypass Mode (PWM/ VFM Auto Switching Control)

- Even if the device is started up with BP = "H", the switching operation will be performed during soft-start.

- After soft-start with PG = "L" to "H", the device is automatically switched into the forced bypass mode.

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### SET OUTPUT VOLTAGE (V\_{SET}) SETTING TABLE

The set output voltage ( $V_{SET}$ ) is adjustable by sending a S-Wire input pulse signal from outside as shown in the table below.

Pulse Count		Vset [V]						
0 (Default)		The default set output voltage (Default V <sub>SET</sub> ) set by Fix Trimming Setting, 2.75 V to 4.20 V in 0.05 V step						
1	Return to Default	VSET						
Pulse Count	Vset [V]	Pulse Count	Vset [V]	Pulse Count	Vset [V]			
2	2.75	12	3.25	22	3.75			
3	2.8	13	3.3	23	3.8			
4	2.85	14	3.35	24	3.85			
5	2.9	15	3.4	25	3.9			
6	2.95	16	3.45	26	3.95			
7	3.0	17	3.5	27	4.0			
8	3.05	18	3.55	28	4.05			
9	3.1	19	3.6	29	4.1			
10	3.15	20	3.65	30	4.15			
11	3.2	21	3.7	31	4.2			

### Table 1. V<sub>SET</sub> Setting Table (31 Steps)

If a S-Wire input pulse signal  $\geq$  32 pulse count, VSET will be set to 4.2 V.

After the time lapse of S-Wire setting stop time (tp\_stop),  $V_{\text{SET}}$  will be changed.

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### **APPLICATION INFORMATION**

### **TYPICAL APPLICATION CIRCUIT**



**RP601x Typical Application** 

### **Recommended Capacitors**

Symbol	Description
$C_{\text{INP}}^{*1}$	22 μF x 1, Ceramic Capacitor, JMK107BJ226MA (TAIYO YUDEN)
CINA	-
Cout <sup>*2</sup>	22 μF x 2, Ceramic Capacitor, JMK107BJ226MA (TAIYO YUDEN)

\*1 Place CINP as close as possible to PVIN. \*2 Place COUT as close as possible to  $V_{OUT}$ .

#### **Recommended Inductors**

Symbol	Recommended Inductors		
L	1.0 μH, DFE201610C-1R0M (TOKO) 1.0 μH, TFM201610GHM-1R0MTAA (TDK)		
	2.2 μH, DFE252012P-2R2M (TOKO)		

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### **TECHNICAL NOTES**

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed a rated voltage, a rated current or a rated power. When designing a peripheral circuit, please be fully aware of the following points.

- Place the bypass capacitor (C<sub>INP</sub>) between the V<sub>INP</sub> pin and the GND pin with shortest-distance wiring.
- Place the output capacitor (C<sub>OUT</sub>) between the V<sub>OUT</sub> pin and the GND pin with shortest-distance wiring.
  Use a ceramic capacitor for C<sub>OUT</sub> having a low equivalent series resistance (ESR). Connect GND of C<sub>OUT</sub> to the GND pin with shortest-distance wiring.
- Make the GND plane wide.
- Ensure the V<sub>INP</sub> and GND lines are firmly connected. A large switching current flows through the V<sub>INP</sub>, GND, inductor, BOLX, BULX and V<sub>OUT</sub> lines. If their impedance is too high, noise pickup or unstable operation may result.
- When the built-in switches are turned off, the inductor may generate a spike-shaped high voltage. Use the high-breakdown voltage capacitor (C<sub>OUT</sub>) which output voltage is 1.5 times or more than the set output voltage.
- Connect the BOLX pin and the inductor and the BULX pin with shortest-distance wiring.
- Use an inductor that has a low DC resistance, has an enough tolerable current and is less likely to cause magnetic saturation.

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### **TYPICAL CHARACTERISTICS**

Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.



1000

20

10

0

0.01

0.1

1

OutputCurrent [mA]

10

TFM201610GHM-1R0MTAA

100

DFE201612E-1R0M

10

VLS201610HBX-1R0M

20

10

0

0.01

0.1

1

OutputCurrent [mA]

1000

DFE252012P-2R2M

100

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# 4) Temperature vs. Output Voltage

**RP601Z330x** 



# 5) Temperature vs. Standby Current RP601Z330x, $V_{IN}$ = 5.5 V



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### 6) Input Voltage vs. Output Current RP601Z275x, MODE = H

### RP601Z330x, MODE = H



#### **RP601Z420x, MODE = H**



#### 8) CE Start-up Waveform RP601Z330x, V<sub>IN</sub> = 3.6 V, MODE = L I<sub>OUT</sub> = 0 mA



#### 7) Temperature vs. Soft-start Time RP601Z330x



 $\label{eq:RP601Z330x} \begin{array}{l} \mathsf{RP601Z330x}, \, \mathsf{V_{IN}} = 3.6 \, \, \mathsf{V}, \, \mathsf{MODE} = \mathsf{H} \\ \mathsf{I}_{\mathsf{OUT}} = 0 \, \, \mathsf{mA} \end{array}$ 



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#### 9) VOUT Waveform

RP601Z275x, V<sub>IN</sub> = 3.6 V, MODE = L



RP601Z330x,  $V_{IN}$  = 3.6 V, MODE = L I<sub>OUT</sub> = 10 mA, L = DFE201610C (2016 size 1.0 µH)



RP601Z420x,  $V_{IN}$  = 3.6 V, MODE = L I<sub>OUT</sub> = 10 mA, L = DFE252012C (2520 size 2.2 µH)







RP601Z330x, V<sub>IN</sub> = 3.6 V, MODE = H I<sub>OUT</sub> = 0 mA, L = DFE201610C (2016 size 1.0 μH)



RP601Z420x,  $V_{IN}$  = 3.6 V, MODE = H I<sub>OUT</sub> = 0 mA, L = DFE201610C (2016 size 1.0 µH)



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11) CE Start-up Waveform RP601Z330x, V<sub>IN</sub> = 3.6 V, MODE = L IOUT = 0 mA



# $\begin{array}{l} \text{RP601Z330x, V_{\text{IN}} = 3.6 V, MODE = H} \\ \text{I}_{\text{OUT}} = 0 \text{ mA} \longleftrightarrow 500 \text{ mA} \end{array}$



RP601Z330x,  $V_{IN}$  = 3.6 V, MODE = H IOUT = 500 mA  $\leftarrow \rightarrow$  1000 mA



RP601Z330x,  $V_{IN}$  = 3.6 V, MODE = H lout = 0 mA



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13) Transient Response Waveform When Changing Vout Using a Single-wireRP601Zxxxx,  $V_{IN} = 3.6 V$ , MODE = H,  $I_{OUT} = 0 \text{ mA}$ RP601Zxxxx,  $V_{IN} = 3.6 V$  $V_{OUT} = 2.75 V \rightarrow 4.2 V$  $V_{OUT} = 4.2 V \rightarrow 2.75 V$ 



14) BP Switching Transient Response Waveform RP601Z330x,  $V_{IN}$  = 2.3 V, MODE = L  $I_{OUT}$  = 50 mA



/ουτ Using a Single-wire RP601Zxxxx, V<sub>IN</sub> = 3.6 V, MODE = H, Iουτ = 0 mA V<sub>OUT</sub> = 4.2 V → 2.75 V



# $\label{eq:RP601Z330x} \begin{array}{l} \mathsf{RP601Z330x}, \mathsf{V}_{\mathsf{IN}} = 5.5 \; \mathsf{V}, \; \mathsf{MODE} = \mathsf{L} \\ \mathsf{I}_{\mathsf{OUT}} = 50 \; \mathsf{mA} \end{array}$



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### POWER DISSIPATION

### WLCSP-16-P1

Ver. A

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-9.

#### **Measurement Conditions**

Item	Measurement Conditions	
Environment	Mounting on Board (Wind Velocity = 0 m/s)	
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)	
Board Dimensions	101.5 mm × 114.5 mm × 1.6 mm	
Copper Ratio	Outer Layers (First and Fourth Layers): 60%	
	Inner Layers (Second and Third Layers): 100%	

#### **Measurement Result**

(Ta = 25°C, Tjmax = 125°C)

Item	Measurement Result	
Power Dissipation	1400 mW	
Thermal Resistance (θja)	θja = 71°C/W	

θja: Junction-to-Ambient Thermal Resistance



Power Dissipation vs. Ambient Temperature



**Measurement Board Pattern** 

### PACKAGE DIMENSIONS

### WLCSP-16-P1

Ver. A



WLCSP-16-P1 Package Dimensions (Unit: mm)

# RICOH

## VISUAL INSPECTION CRITERIA

### WLCSP

VI-160823

No.	Inspection Items	Inspection Criteria	Figure
1	Package chipping	A≥0.2mm is rejected B≥0.2mm is rejected C≥0.2mm is rejected And, Package chipping to Si surface and to bump is rejected.	B ↓ A ↓ c
2	Si surface chipping	A≥0.2mm is rejected B≥0.2mm is rejected C≥0.2mm is rejected But, even if A≥0.2mm, B≤0.1mm is acceptable.	B t C
3	No bump	No bump is rejected.	
4	Marking miss	To reject incorrect marking, such as another product name marking or another lot No. marking.	
5	No marking	To reject no marking on the package.	
6	Reverse direction of marking	To reject reverse direction of marking character.	
7	Defective marking	To reject unreadable marking. (Microscope: X15/ White LED/ Viewed from vertical direction)	
8	Scratch	To reject unreadable marking character by scratch. (Microscope: X15/ White LED/ Viewed from vertical direction)	
9	Stain and Foreign material	To reject unreadable marking character by stain and foreign material. (Microscope: X15/ White LED/ Viewed from vertical direction)	

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#### Sales & Support Offices

#### Ricoh Electronic Devices Co., Ltd.

Shin-Yokohama Office (International Sales) 2-3, Shin-Yokohama 3-chome, Kohoku-ku, Yokohama-shi, Kanagawa, 222-8530, Japan Phone: +81-50-3814-7687 Fax: +81-45-474-0074

Ricoh Americas Holdings, Inc. 675 Campbell Technology Parkway, Suite 200 Campbell, CA 95008, U.S.A. Phone: +1-408-610-3105

#### Ricoh Europe (Netherlands) B.V. Semiconductor Support Centre

Prof. W.H. Keesomlaan 1, 1183 DJ Amstelveen, The Netherlands Phone: +31-20-5474-309

Ricoh International B.V. - German Branch Semiconductor Sales and Support Centre Oberrather Strasse 6, 40472 Düsseldorf, Germany Phone: +49::211-6546-0

Ricoh Electronic Devices Korea Co., Ltd. 3F, Haesung Bldg, 504, Teheran-ro, Gangnam-gu, Seoul, 135-725, Korea Phone: +82-2-2135-5700 Fax: +82-2-2051-5713

#### Ricoh Electronic Devices Shanghai Co., Ltd. Room 403, No.2 Building, No.690 Bibo Road, Pu Dong New District, Shanghai 201203, People's Republic of China Phone: +86-21-5027-3200 Fax: +86-21-5027-3299

Ricoh Electronic Devices Shanghai Co., Ltd.

Shenzhen Branch 1205, Block D(Jinlong Building), Kingkey 100, Hongbao Road, Luohu District, Shenzhen, China Phone: +86:755-8348-7600 Ext 225

#### Ricoh Electronic Devices Co., Ltd.

Taipei office Room 109, 10F-1, No.51, Hengyang Rd., Taipei City, Taiwan Phone: +886-2-2313-1621/1622 Fax: +886-2-2313-1623



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**Телефон:** 8 (812) 309 58 32 (многоканальный) **Факс:** 8 (812) 320-02-42 **Электронная почта:** <u>org@eplast1.ru</u> **Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.