### 1. General description

The 74AHCT244A is an 8-bit buffer/line driver with 3-state outputs and TTL inputs. The device features two output enables ( $1\overline{OE}$  and  $2\overline{OE}$ ). A HIGH on  $n\overline{OE}$  causes the associated outputs to assume a high-impedance OFF-state.

Designed to operate over a V<sub>CC</sub> range from 4.5 V to 5.5 V, the inputs are TTL compatible, which allows the device to be used to translate from 3.3 V to 5 V.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device is fully specified for partial Power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

### 2. Features and benefits

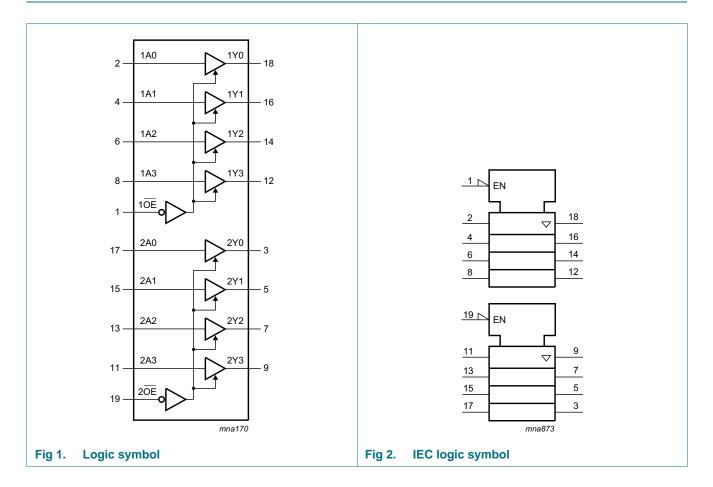
- Direct interface with TTL levels
- Supply voltage range from 4.5 V to 5.5 V
- Typical t<sub>pd</sub> of 2.8 ns at 5 V
- Typical V<sub>OL(p)</sub> < 0.8 V at V<sub>CC</sub> = 5 V, T<sub>amb</sub> = 25 °C
- Typical  $V_{OH(v)} > 2.3 \text{ V}$  at  $V_{CC} = 5 \text{ V}$ ,  $T_{amb} = 25 \text{ °C}$
- Supports mixed-mode voltage operation on all ports
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
  - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
  - MM JESD22-A115-A exceeds 150 V
  - CDM JESD22-C101E exceeds 2 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

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# 3. Ordering information

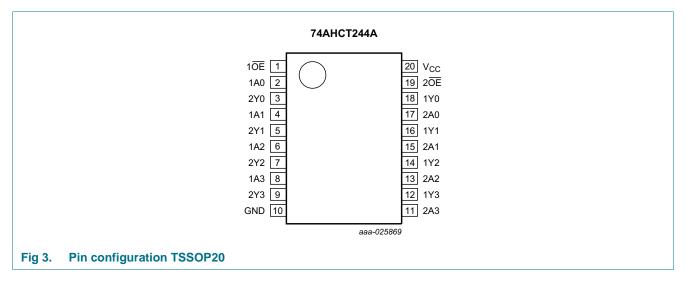
Table 1. Ordering information									
Type number Package									
	Temperature range	Name	Description	Version					
74AHCT244APW	–40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1					

# 4. Functional diagram



# 5. Pinning information

### 5.1 Pinning



### 5.2 Pin description

#### Table 2.Pin description

Symbol	Pin	Description
10E, 20E	1, 19	output enable input (active LOW)
1A0, 1A1, 1A2, 1A3	2, 4, 6, 8	data input
2Y0, 2Y1, 2Y2, 2Y3	3, 5, 7, 9	data output
GND	10	ground (0 V)
2A0, 2A1, 2A2, 2A3	17, 15, 13, 11	data input
1Y0, 1Y1, 1Y2, 1Y3,	18, 16, 14, 12	data output

# 6. Functional description

Table 3.   Function table [1]		
Control	Input	Output
nOE	nAn	nYn
L	L	L
L	Н	Н
Н	Х	Z

[1] H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

# 7. Limiting values

#### Table 4.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
VI	input voltage		<u>[1]</u>	-0.5	+7.0	V
Vo	output voltage	active mode	[2][3]	-0.5	V <sub>CC</sub> + 0.5	V
		power-down or 3-state mode	[2]	-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V		-20	-	mA
I <sub>ОК</sub>	output clamping current	V <sub>O</sub> < 0 V		-20	-	mA
I <sub>O</sub>	output current	$V_{O} = 0 V$ to $V_{CC}$		-	±25	mA
I <sub>CC</sub>	supply current			-	75	mA
I <sub>GND</sub>	ground current			-75	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$	<u>[4]</u>	-	500	mW

[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

[2] The output voltage ratings may be exceeded if the output current ratings are observed.

[3] This value is limited to 7.0 V maximum.

[4] For TSSOP20 package: above 100  $^{\circ}$ C the value of P<sub>tot</sub> derates linearly with 10 mW/K.

# 8. Recommended operating conditions

#### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		4.5	5.5	V
VI	input voltage		0	5.5	V
Vo	output voltage	active mode	0	V <sub>CC</sub>	V
		power-down or 3-state mode	0	5.5	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
$\Delta t / \Delta V$	input transition rise and fall rate	$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	20	ns/V

# 9. Static characteristics

#### Table 6. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		_40 °C	to +85 °C	–40 °C t	o +125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Мах	
V <sub>IH</sub>	HIGH-level input voltage	$V_{CC} = 4.5 V \text{ to } 5.5 V$	2	-	-	2	-	2	-	V
V <sub>IL</sub>	LOW-level input voltage	$V_{CC}$ = 4.5 V to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I <sub>O</sub> = -8 mA	3.94	-	-	3.8	-	3.7	-	V
V <sub>OL</sub> LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$									
	output voltage	I <sub>O</sub> = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 8 mA	-	-	0.36	-	0.44	-	0.55	V
I <sub>OZ</sub>	OFF-state output current	$\label{eq:V_CC} \begin{array}{l} V_{CC} = 5.5 \; V; \; V_{I} = V_{IH} \; \text{or} \; V_{IL}; \\ V_{O} = GND \; \text{to} \; 5.5 \; V \end{array}$	-	-	±0.25	-	±2.5	-	±2.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_1 \text{ or } V_0 = \text{GND to 5.5 V};$ $V_{CC} = 0 \text{ V}$	-	-	0.5	-	5	-	5	μA
I <sub>I</sub>	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0 V$ to 5.5 V	-	-	±0.1	-	±1	-	±1	μA
I <sub>CC</sub>	supply current		-	-	2	-	20	-	20	μA
$\Delta I_{CC}$	additional supply current	per input pin; V <sub>I</sub> = 3.4 V; I <sub>O</sub> = 0 A; other pins at V <sub>CC</sub> or GND; V <sub>CC</sub> = 5.5 V	-	-	1.35	-	1.5	-	1.5	mA

# **10.** Dynamic characteristics

#### Table 7.Dynamic characteristics

GND = 0 V. For test circuit see <u>Figure 6</u>.

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	–40 °C to +125 °C		Unit
				Min	Typ <mark>[1]</mark>	Max	Min	Max	Min	Max	
t <sub>pd</sub>	propagation	nAn to nYn; see Figure 4	[2]								
	delay	$V_{CC}$ = 4.5 V to 5.5 V									
		C <sub>L</sub> = 15 pF		-	2.8	7.4	1	8.5	1	9.5	ns
		C <sub>L</sub> = 50 pF		-	4.4	8.4	1	9.5	1	10.5	ns
t <sub>en</sub>	enable time	nOE to nYn; see Figure 5									
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$									
		C <sub>L</sub> = 15 pF		-	3.8	10.4	1	12	1	13	ns
		C <sub>L</sub> = 50 pF		-	5.4	11.4	1	13	1	14.5	ns
t <sub>dis</sub>	disable time	nOE to nYn; see Figure 5	[2]								
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$									
		C <sub>L</sub> = 15 pF		-	2.9	8	1	11	1	11	ns
		C <sub>L</sub> = 50 pF		-	5.1	11.4	1	13	1	14.5	ns
t <sub>sk(o)</sub>	skew	$V_{CC} = 4.5 V \text{ to } 5.5 V;$ $C_{L} = 50 \text{ pF}$		-	-	1	-	1	-	1	ns
CI	input capacitance	$V_I = V_{CC} \text{ or GND};$ $V_{CC} = 5 \text{ V}$		-	2	6	-	6	-	6	pF
Co	output capacitance	$V_{O} = V_{CC} \text{ or GND};$ $V_{CC} = 5 \text{ V}$		-	5	-	-	-	-	-	pF
C <sub>PD</sub>	power dissipation capacitance	per buffer; C <sub>L</sub> = 0 pF; f = 10 MHz; V <sub>I</sub> = GND to V <sub>CC</sub>	<u>[3]</u>	-	8	-	-	-	-	-	pF

[1] Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 5 V.

[3]  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D (\mu W)$ .  $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$  where:  $f_i =$  input frequency in MHz;  $f_o =$  output frequency in MHz;

 $C_L$  = output load capacitance in pF;

 $V_{CC}$  = supply voltage in Volts.

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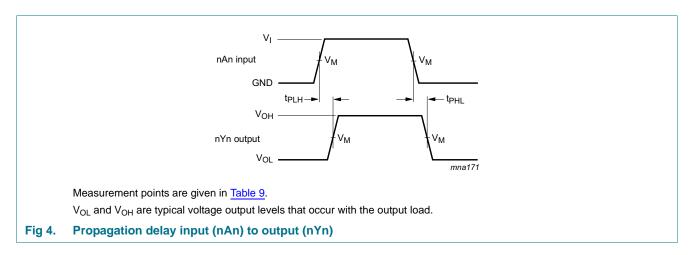
Octal buffer/line driver; 3-state

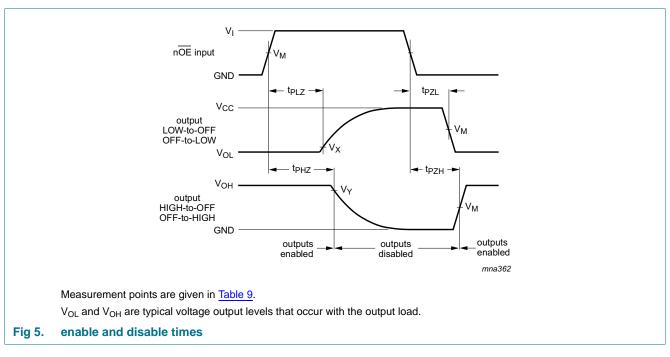
#### Table 8.Noise characteristics

GND = 0 V. For test circuit see Figure 6.

Symbol	Parameter	Conditions	T	T <sub>amb</sub> = 25 °C			
			Min	Тур	Max		
$V_{\rm CC} = 5$ V	/; C <sub>L</sub> = 50 pF		l.				
V <sub>OL(p)</sub>	LOW-level output voltage (peak)		-	0.5	1.5	V	
V <sub>OL(v)</sub>	LOW-level output voltage (valley)		-1.5	-0.3	-	V	
V <sub>OH(v)</sub>	HIGH-level output voltage (valley)		-	4.5	-	V	
V <sub>IH(AC)</sub>	AC HIGH-level input voltage (dynamic)		2	-	-	V	
V <sub>IL(AC)</sub>	AC LOW-level input voltage (dynamic)		-	-	0.8	V	

# 11. Waveforms





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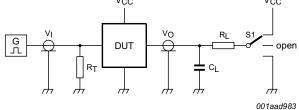
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Table 9.

# 74AHCT244A

#### Octal buffer/line driver; 3-state

Input	Output	Output							
V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	VY						
1.5 V	0.5V <sub>CC</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> – 0.3 V						
	$\begin{array}{c c} 0 \lor & & \\ & & \\ \hline & & \\ & &$	$\begin{array}{c c} 10 \% \\ \hline \\ \hline \\ \hline \\ 90 \% \\ \end{array} \qquad \qquad$							
		Vcc	V <sub>CC</sub>						



Test data is given in Table 10.

**Measurement points** 

Definitions test circuit:

 $R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator

 $C_L$  = Load capacitance including jig and probe capacitance

R<sub>L</sub> = Load resistor

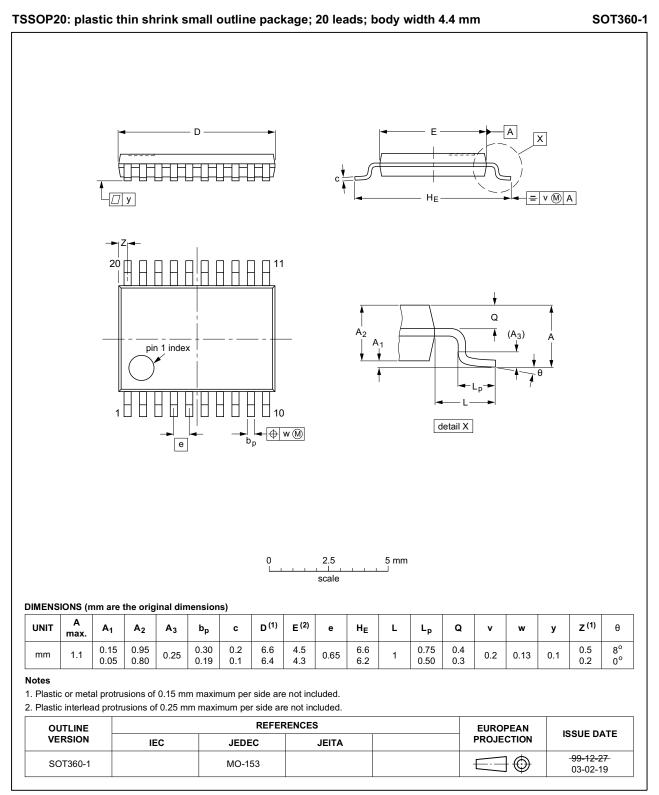
S1 = Test selection switch

Fig 6. Test circuit for measuring switching times

#### Table 10. Test data

Input	Load		S1 position			
VI	t <sub>r</sub> , t <sub>f</sub>	CL	RL	t <sub>PHL</sub> , t <sub>PLH</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
GND to 3.0 V	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V <sub>CC</sub>

# 12. Package outline



#### Fig 7. Package outline SOT360-1 (TSSOP20)

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# **13. Abbreviations**

Table 11. Abbreviations								
Acronym	Description							
CDM	Charge Device Model							
DUT	Device Under Test							
ESD	ElectroStatic Discharge							
НВМ	Human Body Model							
MM	Machine Model							
TTL	Transistor-Transistor Logic							

# 14. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHCT244A v.1	20161123	Product data sheet	-	-

# **15. Legal information**

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Document status[1][2]	Product status <sup>[3]</sup>	Definition
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Product data sheet

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# 74AHCT244A

#### Octal buffer/line driver; 3-state

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#### Octal buffer/line driver; 3-state

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