



## MIC22705YML EV

### Evaluation Board

#### 1MHz, 7A Integrated Switch High-Efficiency Synchronous Buck Regulator

## General Description

The Micrel MIC22705 is a high-efficiency, 7A, integrated switch, synchronous buck (step-down) regulator. The MIC22705 achieves more than 95% efficiency and switches at 1MHz. The ultra-high speed control loop keeps the output voltage within regulation even under the extreme transient load swings commonly found in FPGAs and low-voltage ASICs. The output voltage is pre-bias safe and is adjustable down to 0.7V.

The MIC22705 offers a full range of sequencing and tracking options. The Enable/Delay (EN/DLY) and Power Good (PG) inputs allow versatile turn-on and turn-off sequencing across multiple devices. The Ramp Control™ (RC) input allows start-up voltage tracking, either directly or ratio-metrically.

The MIC22705 is available in a 24-pin 4mm x 4mm MLF® with a junction operating range from -40°C to +125°C.

Data sheets and support documentation are found on the Micrel web site: [www.micrel.com](http://www.micrel.com).

### Requirements

The MIC22705YML EV requires a power supply of 2.9V to 5.5V, and a test load. Ensure that the power supply can provide the wattage required for the chosen test load. The load can be active (electronic load) or passive (resistor). Additionally, monitor the Power Good output (PG) with a multimeter or an oscilloscope if desired.

### Precautions

There is no reverse input protection on this board. When connecting supplies and signals ensure that correct polarities are observed.

## Getting Started

### 1. $V_{IN}$ Supplies

Connect the  $V_{IN}$  supply (2.9V to 5.5V) across the PVIN and PGND terminals. Monitor  $V_{IN}$  at the PVIN and PGND terminals with a voltmeter.

### 2. Enable/SHDN Inputs

The enable input EN is internally pulled up with a 1 $\mu$ A current source. When external on/off control is desired, install Q1 and R5, and connect a logic level control signal to the SHDN input. When SHDN is high, the output is off, and when SHDN is low, the output is on.

### 3. Monitor Outputs

Monitor the output  $V_{OUT}$  with a scope or DVM connected across the VOUT and PGND terminals.

### 4. Output Load

Connect a load across the VOUT and PGND terminals. Use an active or passive load.

### 5. Turn On the Power

Turn on the power supply and verify that  $V_{OUT} = 1.8V$ .

## Ordering Information

Part Number	Description
MIC22705YML EV	Evaluation Board for the MIC22705YML

## EV Board Features

See the MIC22705YML datasheet for detailed explanations of these functions.

### Enable/Delay (EN/DLY)

Enable/Delay allows delayed turn on of the MIC22705. Install a capacitor in location C6 to increase the start-up delay of the MIC22705.

### Shutdown Input (SHDN)

SHDN allows enable/disable of the MIC22705 with an external logic signal. To activate the shutdown feature, install components into the locations labeled Q1 and R5 (component recommendations are listed in the Bill of Materials later in this document). With the components installed, force SHDN high to disable the MIC22705, and low to allow the MIC22705 to operate normally.

### Delay (DELAY)

DELAY allows a delayed Power Good output (PG) indication. Install a capacitor in location C8 to increase the Power Good delay timing of the MIC22705.

### Ramp Control (RC)

Ramp control allows slowing the slew rate of the MIC22705 output. Increase the value of capacitor C7 to reduce the slew rate.

### Power Good Output (PG)

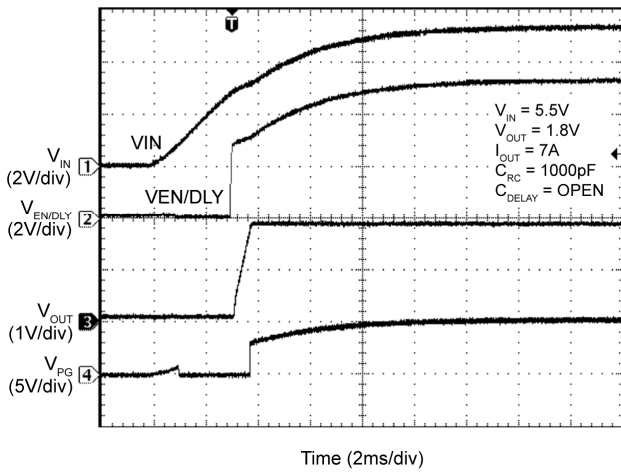
Open drain output PG pulls low when the output voltage of the MIC22705 is out of specification. PG is pulled up to  $V_{IN}$  by a 47.5k $\Omega$  resistor.

### Switch Voltage ( $V_{SW}$ )

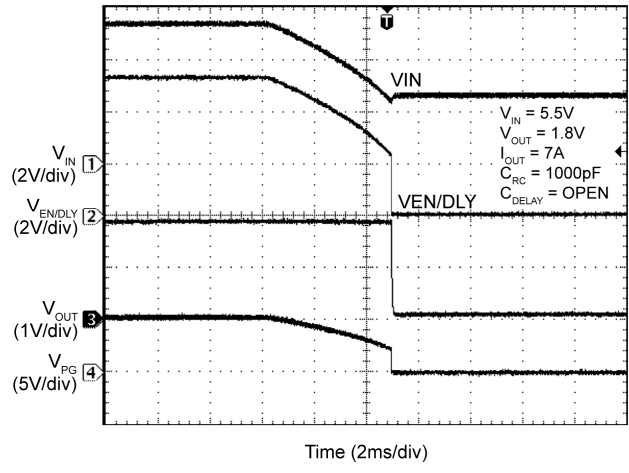
Test point  $V_{SW}$  is provided to monitor the internal switching node.  $V_{SW}$  is isolated from the switch node by 49.9 $\Omega$  resistor R6.

# Typical Characteristics

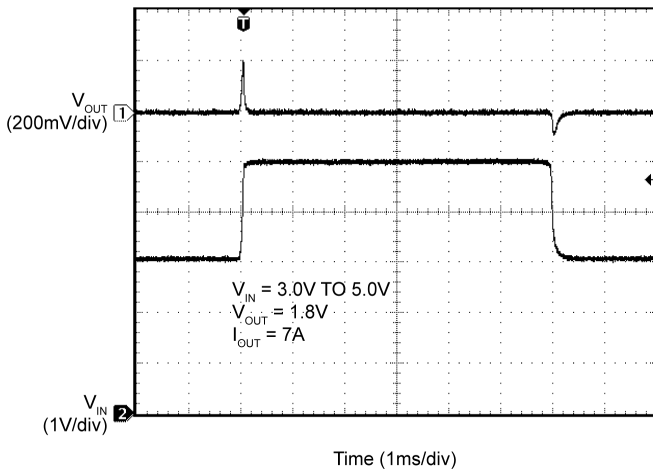
**V<sub>IN</sub> Turn-On**



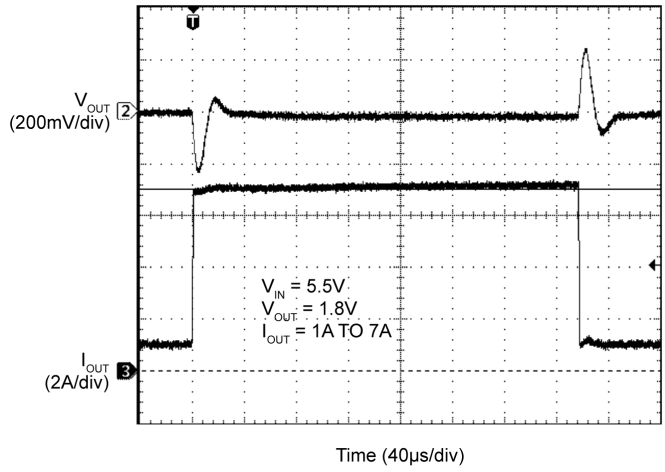
**V<sub>IN</sub> Turn-Off**



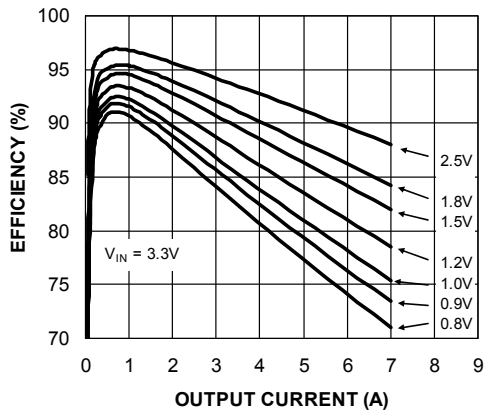
**Line Transient Response**



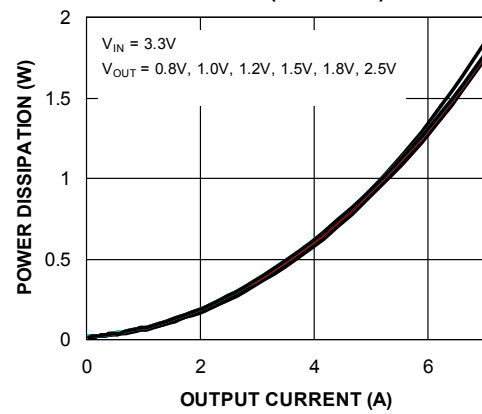
**Load Transient Response**



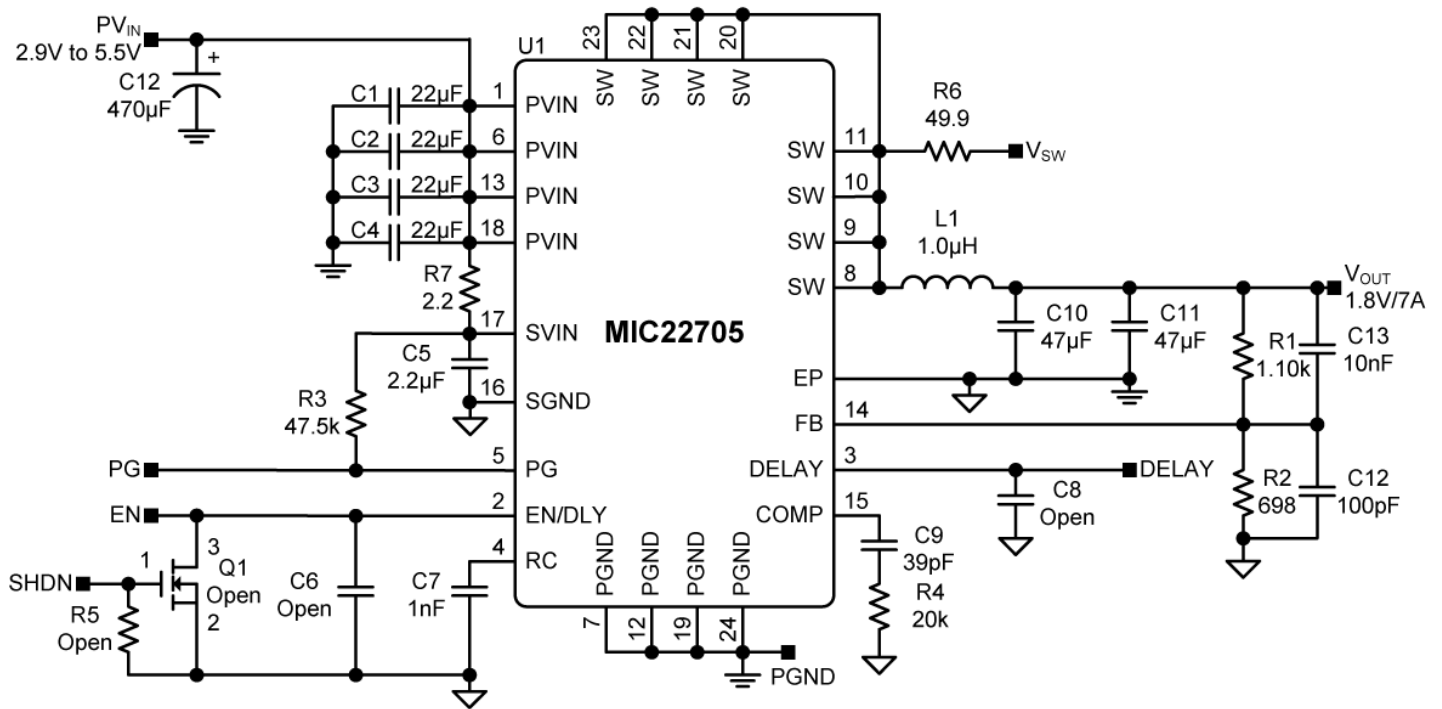
**Efficiency (V<sub>IN</sub> = 3.3V) vs. Output Current**



**IC Power Dissipation vs. Output Current (V<sub>IN</sub> = 3.3V)**



### Evaluation Board Schematic



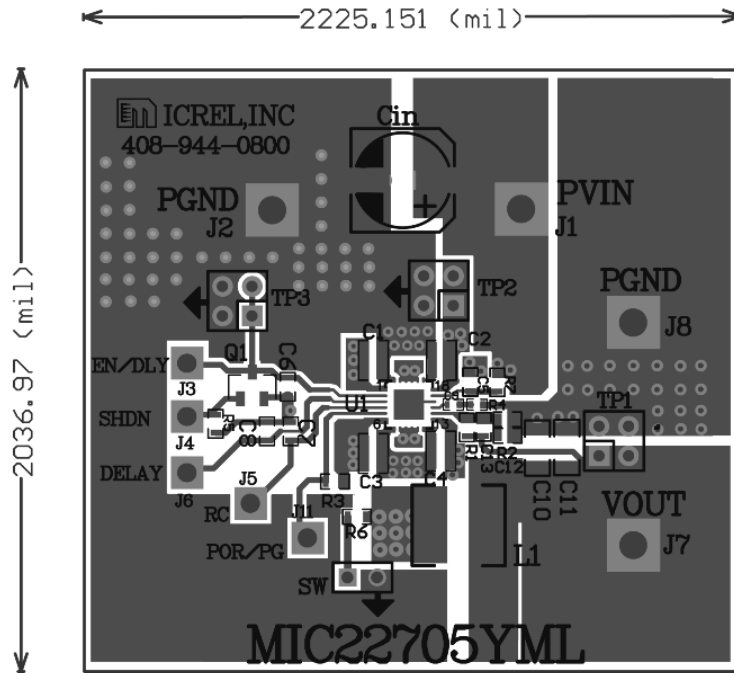
## Bill of Materials

Item	Part Number	Manufacturer	Description	Qty.
C1, C2, C3, C4	C2012X5R0J226M	TDK <sup>(1)</sup>	22 $\mu$ F/6.3V, 0805, Ceramic Capacitor	5
	08056D226MAT	AVX <sup>(2)</sup>		
	GRM21BR60J226ME39L	Murata <sup>(3)</sup>		
C5	06036D225TAAT2A	AVX <sup>(2)</sup>	2.2 $\mu$ F/6.3V, Ceramic Capacitor, X5R, Size 0805	1
	GRM188R7160J225M	Murata <sup>(3)</sup>	2.2 $\mu$ F/6.3V, Ceramic Capacitor, X7R, Size 0805	
	C1608X5R0J225M	TDK <sup>(1)</sup>		
C13	GRM188R71H103KA01D	Murata <sup>(3)</sup>	10nF, 0603, Ceramic Capacitor	1
C7	Open(VJ0603Y102KXQCW1BC)	Vishay <sup>(4)</sup>	1nF, 0603, Ceramic Capacitor	1
	Open(GRM188R71H102KA01D)	Murata <sup>(3)</sup>	1nF/50V, X7R, 0603, Ceramic Capacitor	
	Open(C1608COG1H102J)	TDK <sup>(1)</sup>	1nF/50V, COG, 0603, Ceramic Capacitor	
C6, C8	Open			
C9	GRM1555C1H390JZ01D	Murata <sup>(3)</sup>	39pF/50V, COG, 0402, Ceramic Capacitor	1
	VJ0402A390KXQCW1BC	BC Components <sup>(5)</sup>	39pF /10V, 0402, Ceramic Capacitor	
C10, C11	C3216X5R0J476M	TDK <sup>(1)</sup>	47 $\mu$ F/6.3V, X5R, 1206, Ceramic Capacitor	2
	GRM31CR60J476ME19	Murata <sup>(3)</sup>	47 $\mu$ F/6.3V, X5R, 1206, Ceramic Capacitor	
	GRM31CC80G476ME19L	Murata <sup>(3)</sup>	47 $\mu$ F/4V, X6S, 1206, Ceramic Capacitor	
C12	VJ0402A101KXQCW1BC	Vishay <sup>(4)</sup>	100pF, 0603, Ceramic Capacitor	1
	GRM1555C1H101JZ01D	Murata <sup>(3)</sup>	100pF/50V, COG, 0402, Ceramic Capacitor	
L1	SPM6530T-1R0M120	TDK <sup>(1)</sup>	1 $\mu$ H, 12A, size 7x6.5x3mm	1
	HCP0704-1R0-R	Coiltronics <sup>(6)</sup>	1 $\mu$ H, 12A, size 6.8x6.8x4.2mm	
C <sub>IN</sub>	BA1851A3477M	Epcos <sup>(7)</sup>	470 $\mu$ F/10V, Elect., 8x11.5	1
R1	CRCW06031101FKEYE3	Vishay <sup>(4)</sup>	Resistor, 1.10k, 0603, 1%	1
R2	CRCW04026980FKEYE3	Vishay <sup>(4)</sup>	Resistor, 698 $\Omega$ , 0603, 1%	1
R3	CRCW06034752FKEYE3	Vishay <sup>(4)</sup>	Resistor, 47.5k, 0603, 1%	1
R4	CRCW04022002FKEYE3	Vishay <sup>(4)</sup>	Resistor, 20k, 0402, 1%	1
R5	Open(CRCW06031003FRT1)	Vishay <sup>(4)</sup>	Resistor, 100k, 0603, 1%	1
R6	CRCW060349R9FKEA	Vishay <sup>(4)</sup>	49.9 $\Omega$ Resistor, 1%, Size 0603	1
R7	CRCW06032R20FKEA	Vishay <sup>(4)</sup>	2.2 $\Omega$ Resistor, 1%, Size 0603	1
Q1	Open(2N7002E)	Central Semiconductor <sup>(8)</sup>	Signal MOSFET – SOT23-6	1
	Open(CMDPM7002A)			
U1	<b>MIC22705YML</b>	<b>Micrel, Inc.</b> <sup>(6)</sup>	<b>1MHz, 7A Integrated Switch High-Efficiency Synchronous Buck Regulator</b>	<b>1</b>

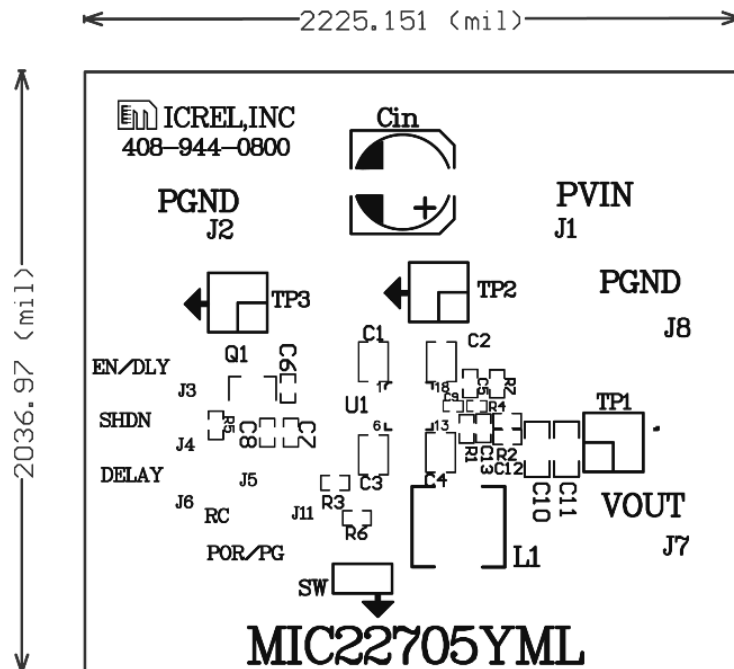
### Notes:

1. TDK: [www.tdk.com](http://www.tdk.com).
2. AVX.: [www.avx.com](http://www.avx.com).
3. Murata: [www.murata.com](http://www.murata.com).
4. Vishay Tel: [www.vishay.com](http://www.vishay.com).
5. BC Components: [www.bccomponents.com](http://www.bccomponents.com).
6. Coiltronics: [www.coiltronics.com](http://www.coiltronics.com).
7. Epcos: [www.epcos.com](http://www.epcos.com).
8. Central Semiconductor: [www.centalsemi.com](http://www.centalsemi.com).
9. **Micrel, Inc.:** [www.micrel.com](http://www.micrel.com).

### Evaluation Board PCB Layout

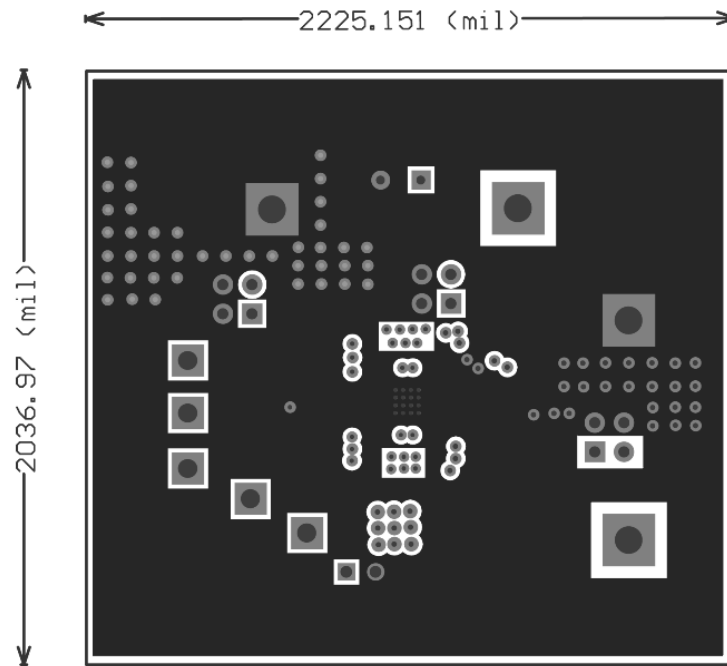


MIC22705 Evaluation Board Top Layer

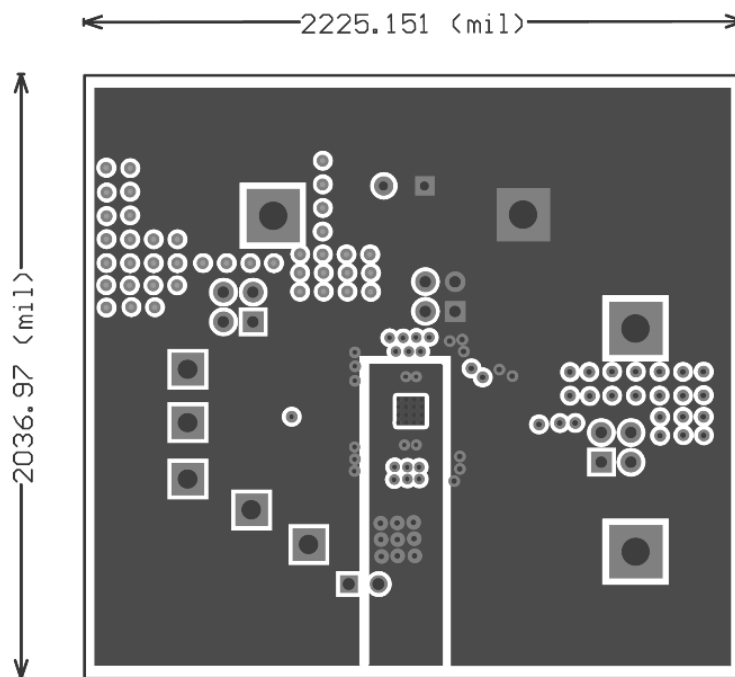


MIC22705 Evaluation Board Top Silk

### Evaluation Board PCB Layout (Continued)

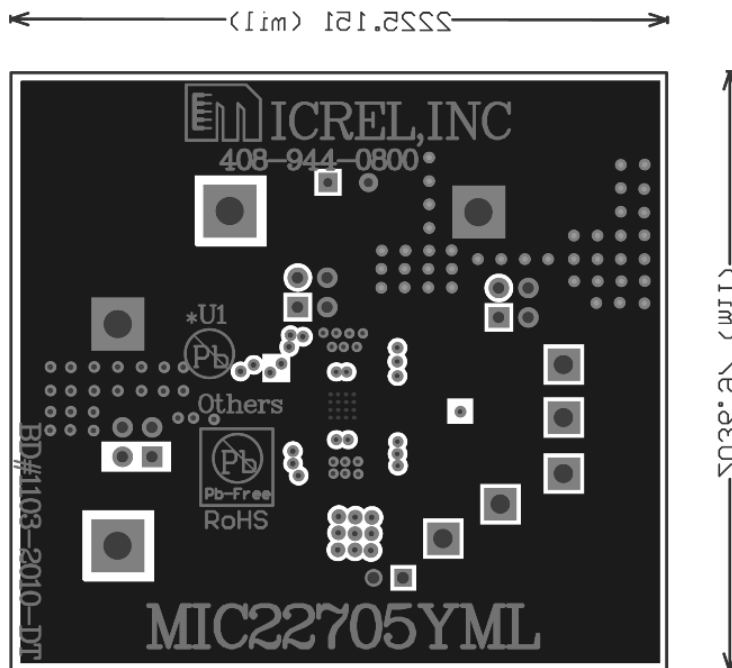


MIC22705 Evaluation Board Mid-Layer 1 (Ground Plane)

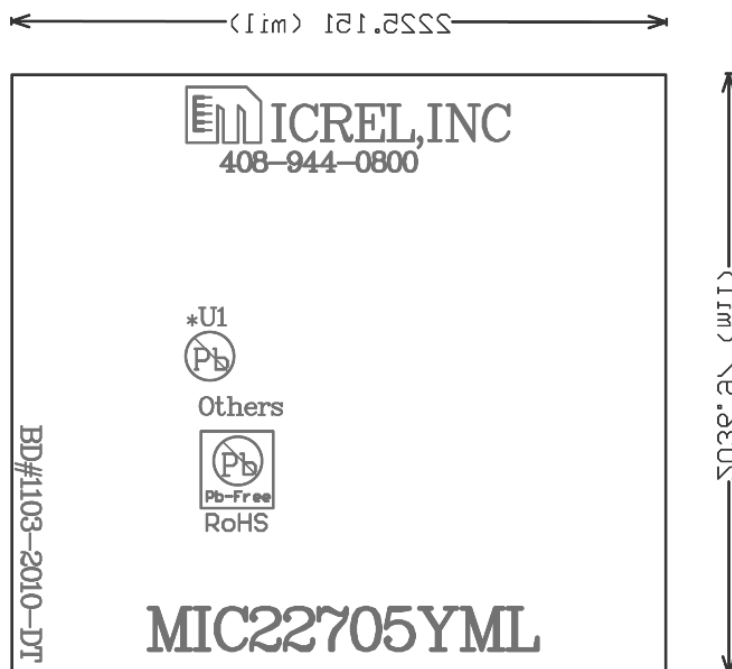


MIC22705 Evaluation Board Mid-Layer 2

### Evaluation Board PCB Layout (Continued)



MIC22705 Evaluation Board Bottom Layer



MIC22705 Evaluation Board Bottom Silk



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