

DATA SHEET

CLA Series: Silicon Limiter Diodes and Ceramic Hermetic Packaged Devices

Applications

- LNA receiver protection
- Commercial and defense radar

Features

- Established limiter diode process
- High power, mid-range, and cleanup designs
- Low insertion loss: 0.1 dB @ 10 GHz
- Peak power handling to +74 dBm
- Ultra low spike leakage power
- Tight control of I layer base width



Description

The Isolink CLA series of silicon limiter diodes provides passive receiver protection over a wide range of frequencies from 100 MHz to over 20 GHz. These devices use a well-established silicon technology resulting in high resistivity and tightly controlled base width PIN limiter diodes. Limiter circuits using these devices perform with strong limiting action and low loss.

The CLA series consists of ten individual chip designs of different intrinsic region base widths and capacitances designed to accommodate multi-stage limiter applications. The mesa-constructed, thin base width, low capacitance CLA4601 series, CLA4602 series, CLA4604 series, and CLA4605 series are designed for low-level and cleanup applications. The CLA4603 series, CLA4606 series, CLA4607 series, CLA4608 series and CLA4610 series are planar designs designated for high-power and mid-range applications.

The CLA4609 series thick base width mesa diode is designed for coarse limiter-stage applications.

The absolute maximum ratings of the CLA diode series are provided in Table 1. Electrical specifications are specified in Table 2. Typical performance characteristics are provided in Table 3 and Figures 1 through 4.

Table 1. CLA Series Absolute Maximum Ratings¹

Parameter	Symbol	Minimum	Typical	Maximum	Units
Power dissipation	P _{DIS}			$\frac{\text{Maximum } T_J - \text{Case Temp}}{\text{Thermal Resistance}_{\text{junction-to-case}}}$	W
Reverse voltage	V _R			Minimum rated breakdown voltage	V
Forward current	I _F			200	mA
Junction temperature	T _J	-65		+175	°C
Storage temperature	T _{STG}	-65		+200	°C
Electrostatic discharge:					
Charged Device Model (CDM), Class 4				1000	V
Human Body Model (HBM), Class 1C				1000	V
Machine Model (MM), Class A				150	V

¹ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

ESD HANDLING: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.

Table 2. CLA Series Electrical Specifications^{1,2} (1 of 2)

Part Number	Breakdown Voltage (V)	I Region (µm)	Total Capacitance (C _T) @ 0 V (pF)	Total Capacitance (C _T) @ 6 V (pF)	Series Resistance (R _S) @ 10 mA (Ω)	Minority Carrier Lifetime (TL) @ 10 mA (ns)	Thermal Resistance (θ) ³ Average (°C/W)
	Min to Max	Nominal	Typical	Maximum	Typical	Typical	Maximum
CLA4601-203	15 to 30	1.0	0.37	0.35	2.0	5	150
CLA4601-210	15 to 30	1.0	0.37	0.35	2.0	5	140
CLA4601-219	15 to 30	1.0	0.37	0.35	2.0	5	200
CLA4601-240	15 to 30	1.0	0.37	0.35	2.0	5	200
CLA4602-203	15 to 30	1.0	0.45	0.40	1.5	5	110
CLA4602-210	15 to 30	1.0	0.45	0.40	1.5	5	100
CLA4602-219	15 to 30	1.0	0.45	0.35	1.5	5	160
CLA4602-240	15 to 30	1.0	0.45	0.40	1.5	5	160
CLA4603-203	20 to 45	1.5	0.45	0.35	1.5	5	130
CLA4603-210	20 to 45	1.5	0.45	0.40	1.5	5	120
CLA4603-219	20 to 45	1.5	0.45	0.40	1.5	5	180
CLA4603-240	20 to 45	1.5	0.45	0.35	1.5	5	180
CLA4604-203	30 to 60	2.0	0.37	0.30	2.0	7	130
CLA4604-210	30 to 60	2.0	0.37	0.35	2.0	7	120
CLA4604-219	30 to 60	2.0	0.37	0.30	2.0	7	160
CLA4604-240	30 to 60	2.0	0.37	0.30	2.0	7	180

Table 2. CLA Series Electrical Specifications^{1,2} (2 of 2)

Part Number	Breakdown Voltage (V)	I Region (μm)	Total Capacitance (C _T) @ 0 V (pF)	Total Capacitance (C _T) @ 6 V (pF)	Series Resistance (RS) @ 10 mA (Ω)	Minority Carrier Lifetime (TL) @ 10 mA (ns)	Thermal Resistance (θ) ³ Average (°C/W)
	Min to Max	Nominal	Typical	Maximum	Typical	Typical	Maximum
CLA4605-203	30 to 60	2.0	0.45	0.30	1.5	7	100
CLA4605-210	30 to 60	2.0	0.45	0.40	1.5	7	90
CLA4605-219	30 to 60	2.0	0.45	0.40	1.5	7	150
CLA4605-240	30 to 60	2.0	0.45	0.35	1.5	7	150
CLA4606-203	45 to 75	2.5	0.45	0.35	1.5	10	110
CLA4606-210	45 to 75	2.5	0.45	0.40	1.5	10	100
CLA4606-219	45 to 75	2.5	0.45	0.40	1.5	10	160
CLA4606-240	45 to 75	2.5	0.45	0.35	1.5	10	106
CLA4607-203	120 to 180	7.0	0.45	0.35 @ 38 V	1.5	50	70
CLA4607-210	120 to 180	7.0	0.45	0.40 @ 38 V	1.5	50	60
CLA4607-219	120 to 180	7.0	0.45	0.40 @ 38 V	1.5	50	120
CLA4607-240	120 to 180	7.0	0.45	0.35 @ 38 V	1.5	50	120
CLA4608-203	120 to 180	7.0	0.85	0.75 @ 38 V	0.5	100	45
CLA4608-210	120 to 180	7.0	0.85	0.75 @ 38 V	0.5	100	35
CLA4608-219	120 to 180	7.0	0.85	0.70 @ 38 V	0.5	100	100
CLA4608-240	120 to 180	7.0	0.85	0.70 @ 38 V	0.5	100	100
CLA4609-203	250 (Min)	20.0	0.51	0.40	1.0	1175	45
CLA4609-210	250 (Min)	20.0	0.51	0.45	1.0	1175	35
CLA4609-219	250 (Min)	20.0	0.51	0.40	1.0	1175	100
CLA4609-240	250 (Min)	20.0	0.51	0.40	1.0	1175	100
CLA4610-203	80 to 120	4.5	0.38	0.40	1.7	20	182
CLA4610-210	80 to 120	4.5	0.38	0.45	1.7	20	81
CLA4610-219	80 to 120	4.5	0.38	0.40	1.7	20	152
CLA4610-240	80 to 120	4.5	0.38	0.40	1.7	20	163

¹ Performance is guaranteed only under the conditions listed in this table and is not guaranteed over the full operating or storage temperature ranges. Operation at elevated temperatures may reduce reliability of the device.

² TOP = +25 °C, C_J measured at 1 MHz, R_S measured at 500 MHz, C_W thermal resistance for infinite heat sink, unless otherwise noted.

³ Thermal resistance is calculated from the measured power dissipation @ f = 2.6 GHz, T_J max = 175 °C and T_{CASE} = 85 °C

Table 3. Typical Performance @ 25 °C @ 2.6 GHz, Z₀ = 50 Ω¹

Part Number	Insertion Loss @ -10 dBm (dB)	CW Input Power for 1 dB Insertion Loss (dBm)	Maximum CW Input Power (dBm)	Maximum Pulsed Input Power (dBm) ²	Output @ Maximum Pulsed Input (dBm) ²	Recovery Time (ns) ³	Spike Leakage (ergs) ⁴
CLA4601	0.1	12	36	65	21	5	Note 5
CLA4602	0.1	12	36	65	24	5	Note 5
CLA4603	0.1	10	38	67	22	5	Note 5
CLA4604	0.1	11	40	70	24	5	Note 5
CLA4605	0.1	12	40	70	27	5	0.08
CLA4606	0.1	14	41	71	27	5	0.03
CLA4607	0.1	26	43	73	39	5	0.21
CLA4608	0.2	26	43	73	44	5	0.15
CLA4609	0.3	37	44	74	50	5	25.77
CLA4610	0.1	24	40	57	32	5	Note 5

¹ Diode chip is mounted on a 0.5 oz Cu PC board using 1 to 2 mils of conductive epoxy. Bond wire connections are made with 0.8 mil Au wire. Limiter configured with shunt connected diode and 22 nH ground return and 100 pF DC blocking capacitors.

² Pulsed power measurements taken at 1 μs pulse width, @ f = 10 KHz, and 0.1% duty cycle.

³ Recovery time represents the transition time from the high-loss state to the low-loss state following the removal of a high-power input. It is defined as the time from the end of the high-power pulse to the time when insertion loss has returned to within 3 dB of the quiescent (low-power) state.

⁴ Spike Leakage (ergs) = t_s x P_s x 10⁷ where t_s is the spike width at the half-power point (in seconds) and P_s is the maximum spike amplitude in watts.

⁵ Not detectable under current test conditions described in Note 2.

Typical Performance Characteristics
(Tested at 25 °C, Board and Connector Loss Are Not De-Embedded)

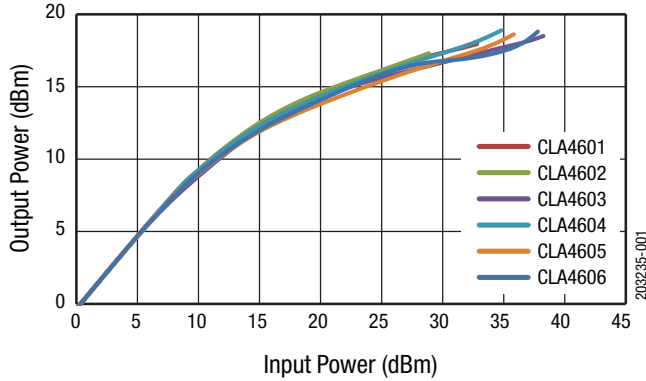


Figure 1. CLA4601 to CLA4606 CW Output vs Input Power

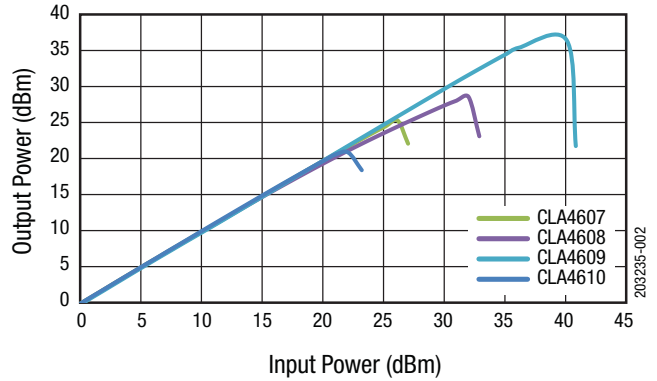


Figure 2. CLA4607 to CLA4610 CW Output vs Input Power

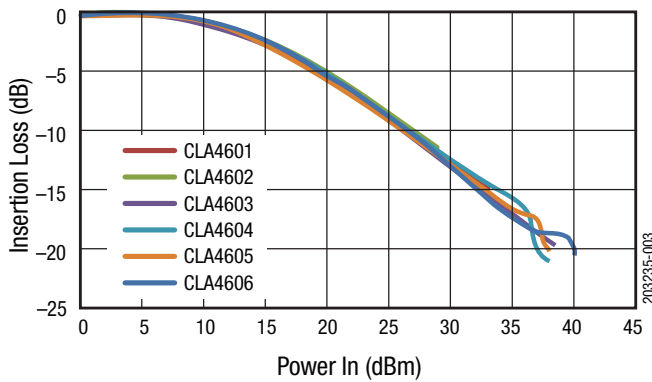


Figure 3. CLA4601 to CLA4606 Insertion Loss vs CW Input Power

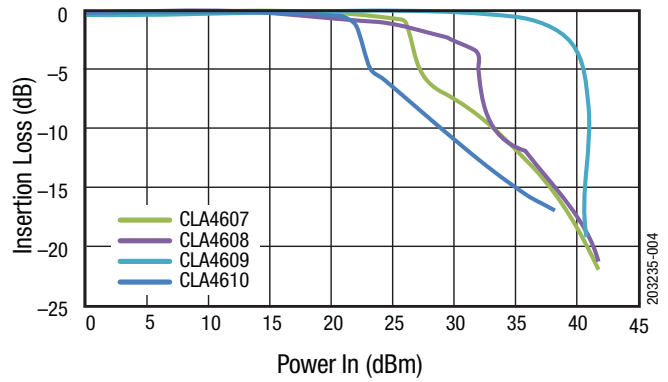


Figure 4. CLA4607 to CLA4610 Insertion Loss vs CW Input Power

Technical Description

The limiter can be used in various configurations to meet different application requirements. For example, in low-to-medium power applications, a single-stage limiter can be used. In this case, an inductor must be used in parallel with the limiter to create a DC return choke.

A dual-stage limiter design can handle much higher power. The first stage works as the coarse stage (CLA4607 through

CLA4610), and the second stage works as the cleanup stage (CLA4601 through CLA4606).

Package dimension drawings are provided in Figures 5 through 8.

For detailed application information, refer to the Skyworks Application Note: *PIN Limiter Diodes in Receiver Protectors* at www.skyworksinc.com.

Package Outline Drawings

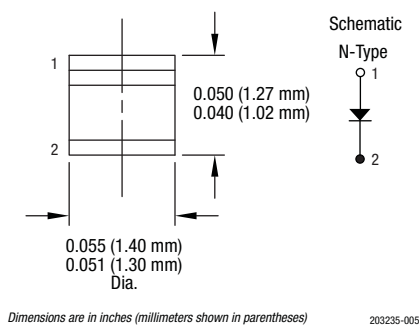


Figure 5. -203 Package

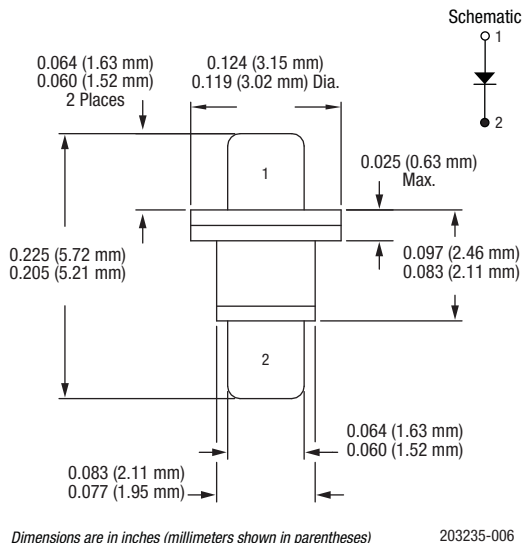
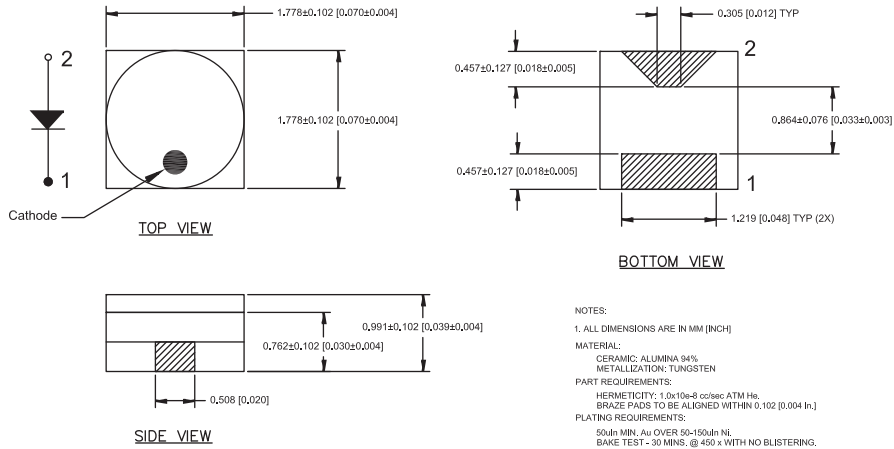
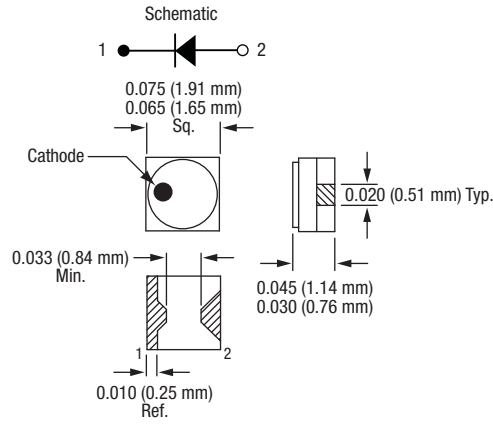


Figure 6. -210 Package



Version A

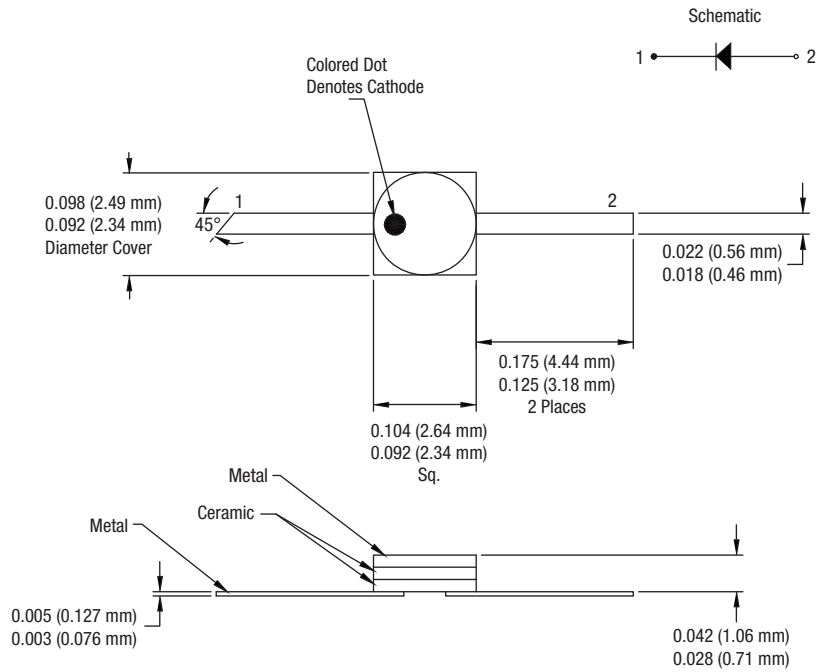


Dimensions are in inches (millimeters shown in parentheses)

Version B

203235-007

Figure 7. -219 Package



Dimensions are in inches (millimeters shown in parentheses)

203235-008

Figure 8. -240 Package

Copyright © 2014, 2017 Isolink, Inc. All Rights Reserved.

Information in this document is provided in connection with Isolink, Inc. ("Isolink"), a wholly-owned subsidiary of Skyworks Solutions, Inc. These materials, including the information contained herein, are provided by Isolink as a service to its customers and may be used for informational purposes only by the customer. Isolink assumes no responsibility for errors or omissions in these materials or the information contained herein. Isolink may change its documentation, products, services, specifications or product descriptions at any time, without notice. Isolink makes no commitment to update the materials or information and shall have no responsibility whatsoever for conflicts, incompatibilities, or other difficulties arising from any future changes.

No license, whether express, implied, by estoppel or otherwise, is granted to any intellectual property rights by this document. Isolink assumes no liability for any materials, products or information provided hereunder, including the sale, distribution, reproduction or use of Isolink products, information or materials, except as may be provided in Isolink Terms and Conditions of Sale.

THE MATERIALS, PRODUCTS AND INFORMATION ARE PROVIDED "AS IS" WITHOUT WARRANTY OF ANY KIND, WHETHER EXPRESS, IMPLIED, STATUTORY, OR OTHERWISE, INCLUDING FITNESS FOR A PARTICULAR PURPOSE OR USE, MERCHANTABILITY, PERFORMANCE, QUALITY OR NON-INFRINGEMENT OF ANY INTELLECTUAL PROPERTY RIGHT; ALL SUCH WARRANTIES ARE HEREBY EXPRESSLY DISCLAIMED. ISOLINK DOES NOT WARRANT THE ACCURACY OR COMPLETENESS OF THE INFORMATION, TEXT, GRAPHICS OR OTHER ITEMS CONTAINED WITHIN THESE MATERIALS. ISOLINK SHALL NOT BE LIABLE FOR ANY DAMAGES, INCLUDING BUT NOT LIMITED TO ANY SPECIAL, INDIRECT, INCIDENTAL, STATUTORY, OR CONSEQUENTIAL DAMAGES, INCLUDING WITHOUT LIMITATION, LOST REVENUES OR LOST PROFITS THAT MAY RESULT FROM THE USE OF THE MATERIALS OR INFORMATION, WHETHER OR NOT THE RECIPIENT OF MATERIALS HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

Customers are responsible for their products and applications using Isolink products, which may deviate from published specifications as a result of design defects, errors, or operation of products outside of published parameters or design specifications. Customers should include design and operating safeguards to minimize these and other risks. Isolink assumes no liability for applications assistance, customer product design, or damage to any equipment resulting from the use of Isolink products outside of stated published specifications or parameters.

Isolink is a trademark of Isolink Inc. in the United States and other countries. Skyworks and the Skyworks symbol are trademarks or registered trademarks of Skyworks Solutions, Inc., in the United States and other countries. Third-party brands and names are for identification purposes only, and are the property of their respective owners.



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.