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# 2N4403 / MMBT4403 PNP General-Purpose Amplifier

## Description

This device is designed for use as a general-purpose amplifier and switch for collector currents to 500 mA.



Figure 1. 2N4403 Device Package



Figure 2. MMBT4403 Device Package

Part Number	Marking	Package	Packing Method	
2N4403BU	2N4403	TO-92 3L	Bulk	
2N4403TF	2N4403	TO-92 3L	Tape and Reel	
2N4403TFR	2N4403	TO-92 3L	Tape and Reel	
2N4403TA	2N4403	TO-92 3L	Ammo	
2N4403TAR	2N4403	TO-92 3L	Ammo	
MMBT4403	2T	SOT-23 3L	Tape and Reel	

## **Ordering Information**

July 2014

## Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-40	V
V <sub>CBO</sub>	Collector-Base Voltage	-40	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5.0	V
۱ <sub>C</sub>	Collector Current - Continuous	-600	mA
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

## **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Ма	Unit	
	i alameter	2N4403 <sup>(3)</sup>	MMBT4403 <sup>(4)</sup>	Onit
Р	Total Device Dissipation	625	350	mW
PD	Derate Above 25°C	5.0	2.8	mW/°C
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case	83.3		°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	200	357	°C/W

Notes:

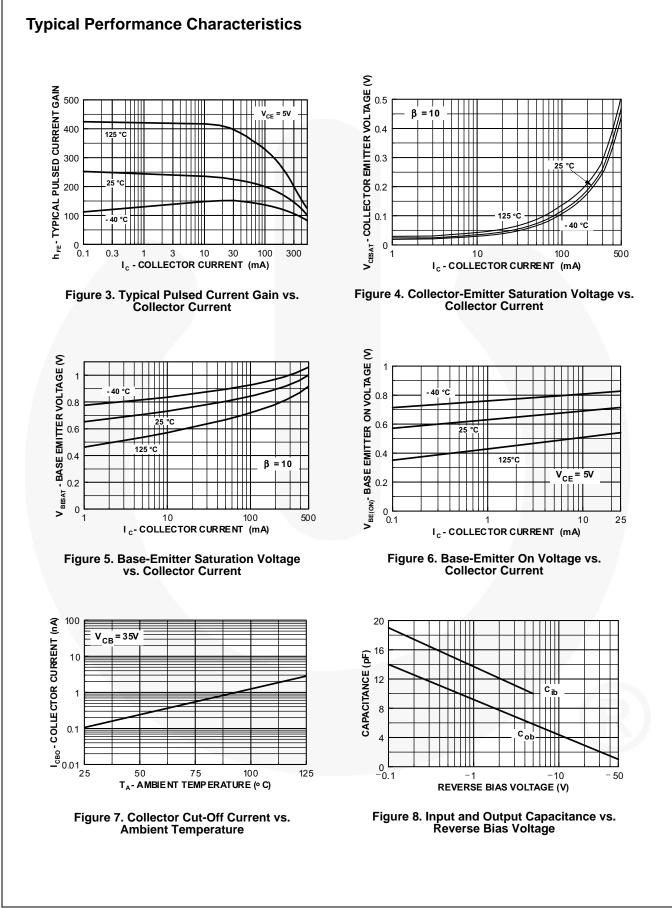
3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

4. Device mounted on FR-4 PCB 1.6 inch x 1.6 inch x 0.06 inch.

Symbol	Parameter	Conditions	Min.	Max.	Unit
Off Charact					
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(5)</sup>	I <sub>C</sub> = -1.0 mA, I <sub>B</sub> = 0	-40		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -0.1 mA, I <sub>E</sub> = 0	-40		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{\rm E} = -0.1 \text{ mA}, I_{\rm C} = 0$	-5.0		V
I <sub>BL</sub>	Base Cut-Off Current	$V_{CE} = -35 \text{ V}, \text{ V}_{EB} = -0.4 \text{ V}$		-0.1	μA
I <sub>CEX</sub>	Collector Cut-Off Current	$V_{CE} = -35 \text{ V}, \text{ V}_{EB} = -0.4 \text{ V}$		-0.1	μA
On Charact	eristics				
		$I_{\rm C}$ = -0.1 mA, $V_{\rm CE}$ = -1.0 V	30		
h <sub>FE</sub>		I <sub>C</sub> = -1.0 mA, V <sub>CE</sub> = -1.0 V	60		
	DC Current Gain	I <sub>C</sub> = -10 mA, V <sub>CE</sub> = -1.0 V	100		
		$I_{\rm C}$ = -150 mA, $V_{\rm CE}$ = -2.0 V <sup>(5)</sup>	100	300	
		$I_{\rm C}$ = -500 mA, $V_{\rm CE}$ = -2.0 V <sup>(5)</sup>	20		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(5)</sup>	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA		-0.40	V
(CE(Sar)		I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA		-0.75	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	$I_{\rm C}$ = -150 mA, $I_{\rm B}$ = -15 mA <sup>(5)</sup>	-0.75	-0.95	- v
v <sub>BE</sub> (sal)	Base-Emilier Saturation voltage	I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA		-1.30	
Small Signa	al Characteristics				
f <sub>T</sub>	Current Gain - Bandwidth Product	$I_{C} = -20 \text{ mA}, V_{CE} = -10 \text{ V},$ f = 100 MHz	200		MHz
C <sub>cb</sub>	Collector-Base Capacitance	$V_{CB} = -10 \text{ V}, I_E = 0, f = 140 \text{ kHz}$		8.5	pF
C <sub>eb</sub>	Emitter-Base Capacitance	$V_{BE} = -0.5 \text{ V}, I_{C} = 0,$ f = 140 kHz		30	pF
h <sub>ie</sub>	Input Impedance	$I_{C} = -1.0 \text{ mA}, V_{CE} = -10 \text{ V},$ f = 1.0 kHz	1.5	15.0	kΩ
h <sub>re</sub>	Voltage Feedback Ratio	$I_{C} = -1.0 \text{ mA}, V_{CE} = -10 \text{ V},$ f = 1.0 kHz	0.1	8.0	x10 <sup>-4</sup>
h <sub>fe</sub>	Small-Signal Current Gain	$I_{C} = -1.0 \text{ mA}, V_{CE} = -10 \text{ V}, f = 1.0 \text{ kHz}$	60	500	
h <sub>oe</sub>	Output Admittance	$I_{C} = -1.0 \text{ mA}, V_{CE} = -10 \text{ V},$ f = 1.0 kHz	1	100	μmhos
Switching C	Characteristics	· ·			
t <sub>d</sub>	Delay Time	V <sub>CC</sub> = -30 V, I <sub>C</sub> = -150 mA, I <sub>B1</sub> = -15 mA		15	ns
t <sub>r</sub>	Rise Time			20	ns
t <sub>s</sub>	Storage Time	V <sub>CC</sub> = -30 V, I <sub>C</sub> = -150 mA, I <sub>B1</sub> = I <sub>B2</sub> = -15 mA		225	ns
t <sub>f</sub>	Fall Time			30	ns

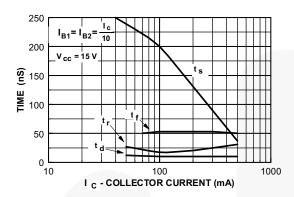
Note:

5. Pulse test: pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2.0%.



2N4403 / MMBT4403 — PNP General-Purpose Amplifier

### Typical Performance Characteristics (Continued)





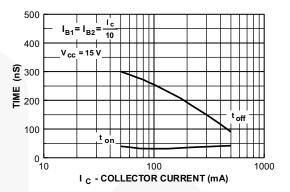
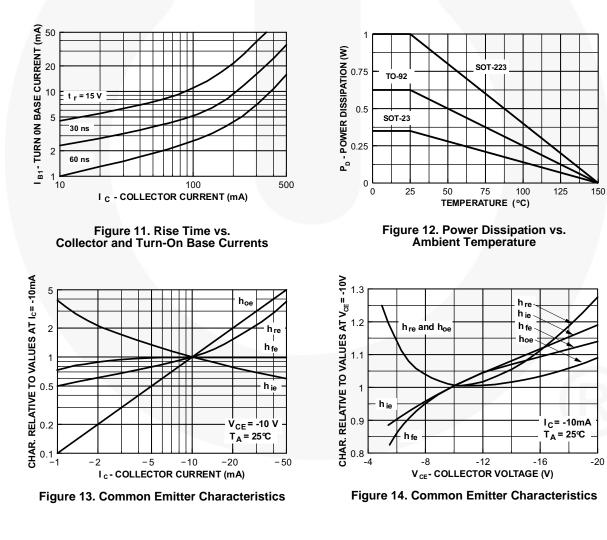
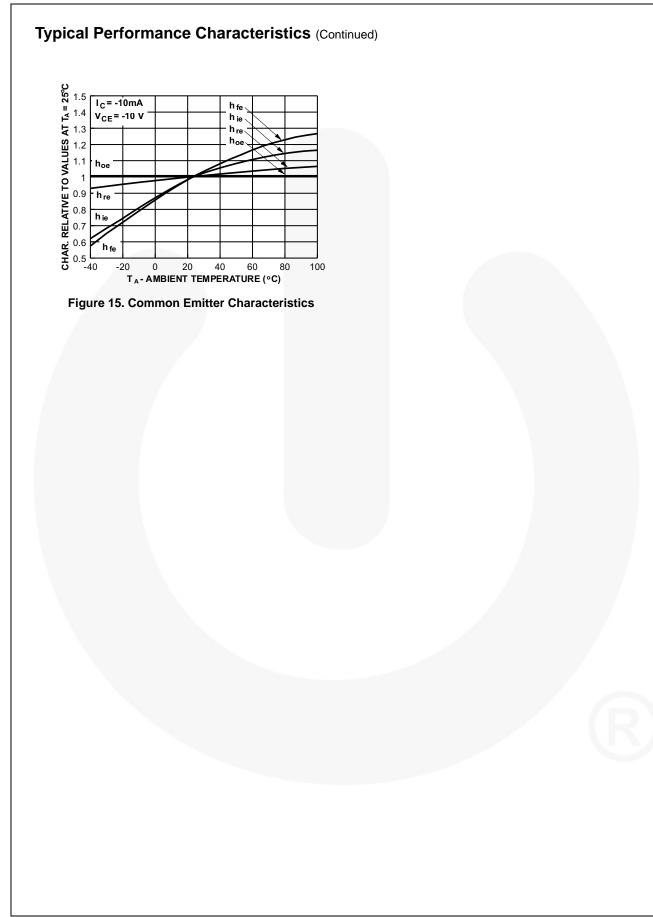
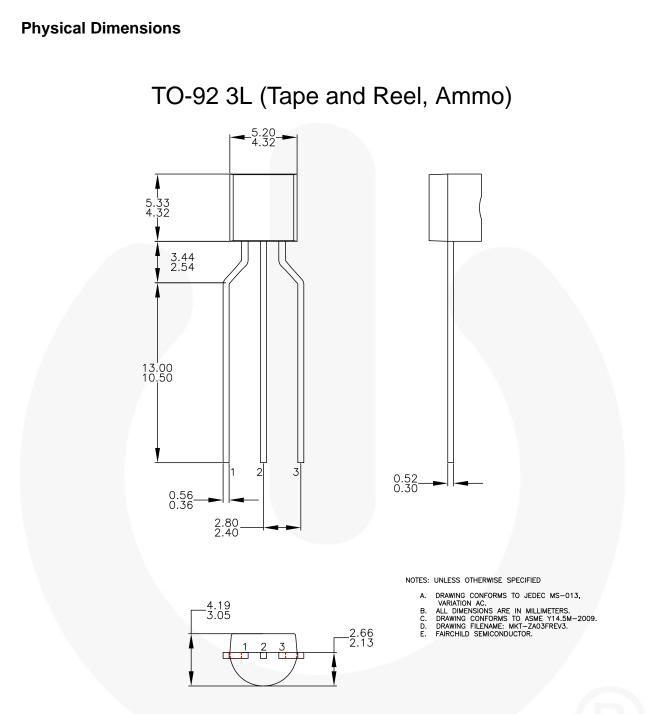


Figure 10. Turn-On and Turn-Off Times vs. Collector Current





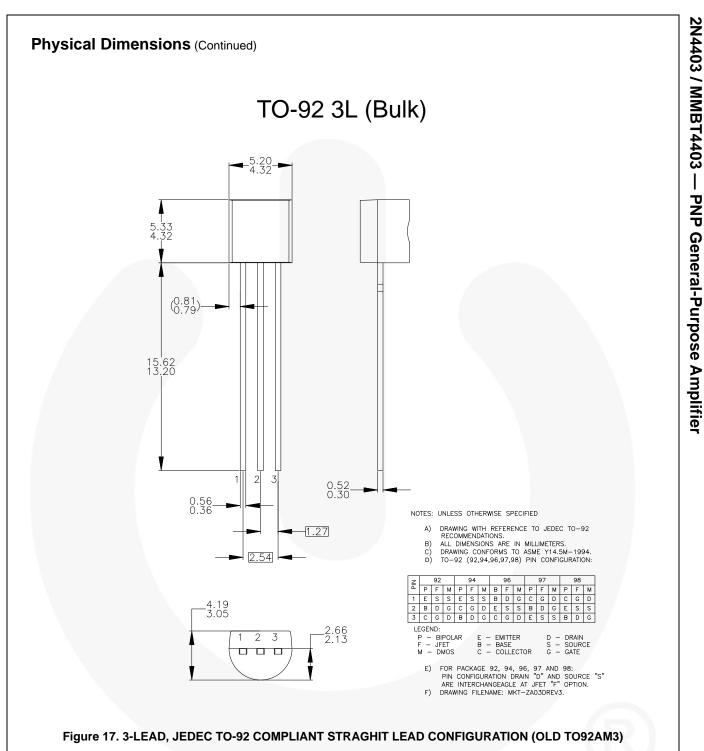


#### Figure 16. 3-LEAD, TO-92, MOLDED 0.200 IN LINE SPACING LD FORM (J61Z OPTION) (ACTIVE)

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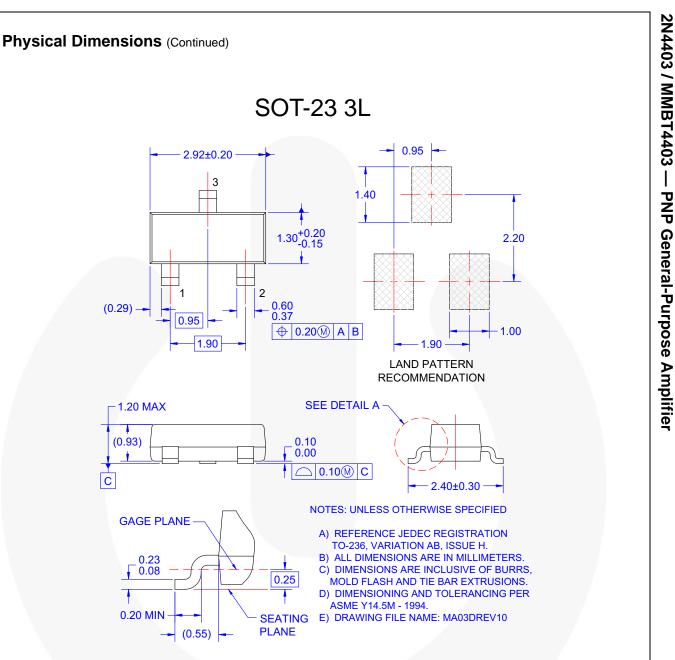
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#### **DETAIL A** SCALE: 2X

1

0.95

(0.29)

1.20 MAX

GAGE PLANE

0.23

0.20 MIN

(0.93)

C

#### Figure 18. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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