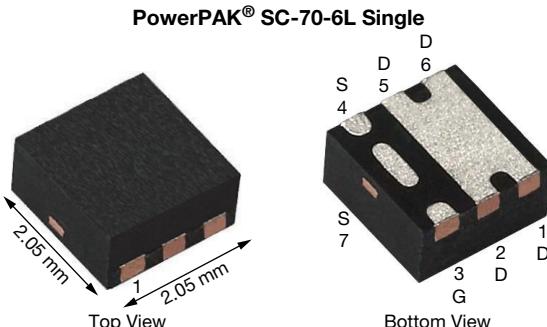


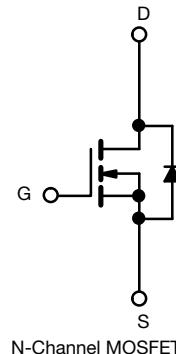
Automotive N-Channel 30 V (D-S) 175 °C MOSFET



Marking Code: QCXXXX

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified ^d
- 100 % R_g and UIS tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912



PRODUCT SUMMARY	
V_{DS} (V)	30
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.065
$R_{DS(on)}$ (Ω) at $V_{GS} = 2.5$ V	0.095
I_D (A)	2.25
Configuration	Single
Package	PowerPAK SC-70

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V_{DS}	30	V
Gate-source voltage	V_{GS}	± 12	
Continuous drain current ^a	I_D	2.25	
		2.25	
Continuous source current (diode conduction) ^a	I_S	2.25	A
Pulsed drain current ^a	I_{DM}	9	
Single pulse avalanche current	I_{AS}	9	
Single pulse avalanche energy		4	
Maximum power dissipation ^b	P_D	13.6	W
		4.5	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C
Soldering recommendations (peak temperature) ^{e, f}		260	

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient	R_{thJA}	90	°C/W
Junction-to-case (drain)		11	

Notes

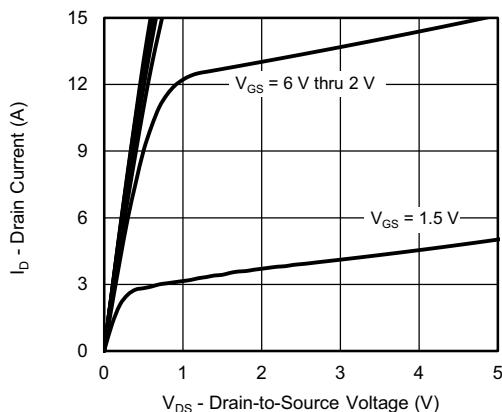
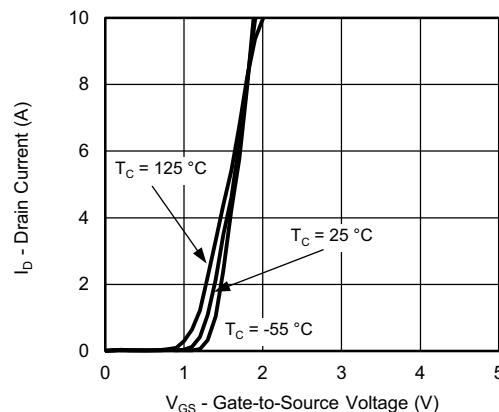
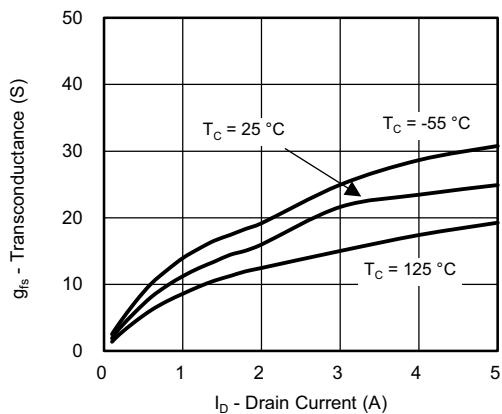
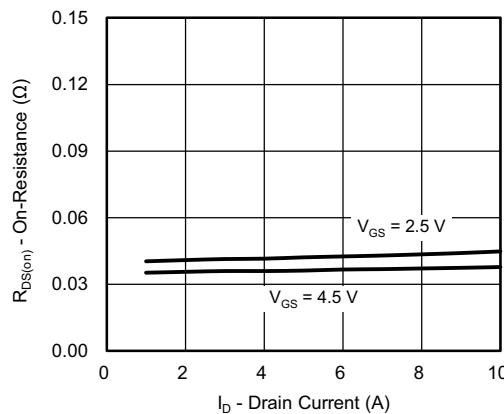
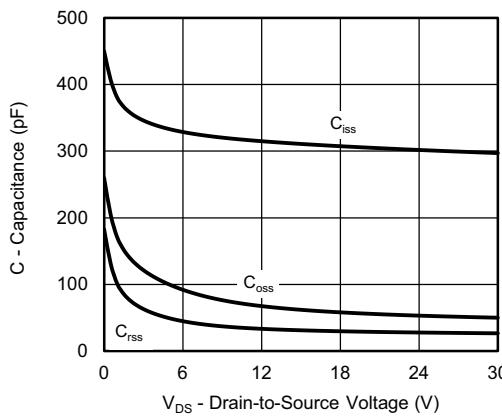
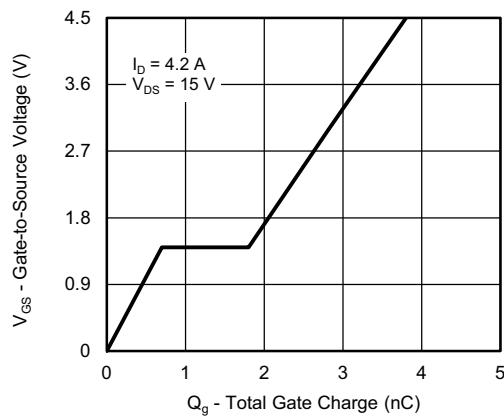
- Package limited
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
- When mounted on 1" square PCB (FR4 material)
- Parametric verification ongoing
- See solder profile (www.vishay.com/doc?73257). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components

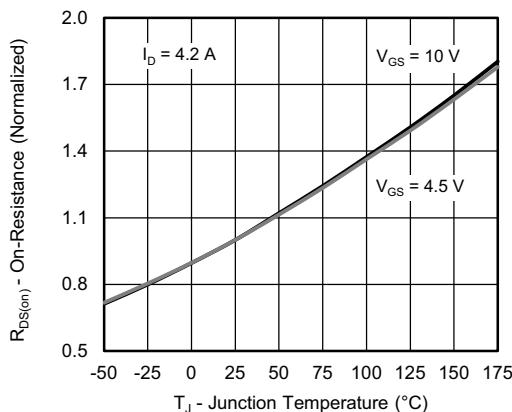
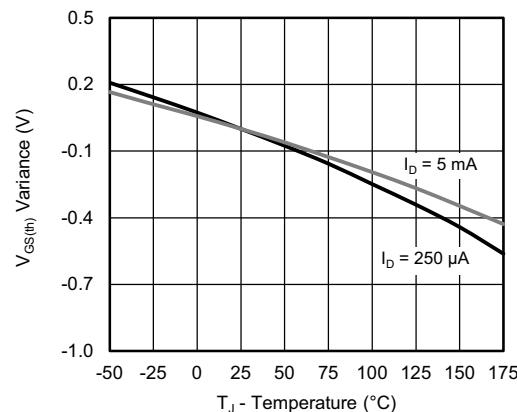
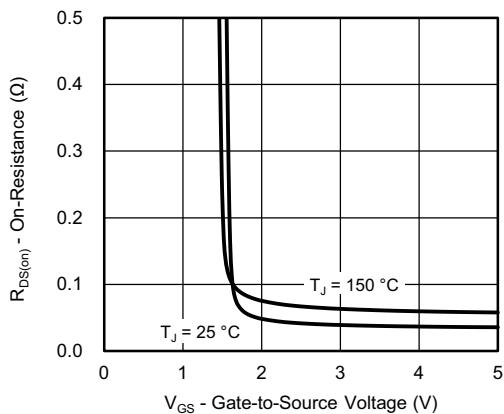
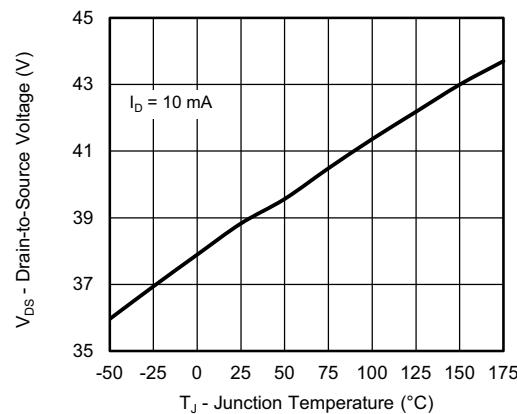
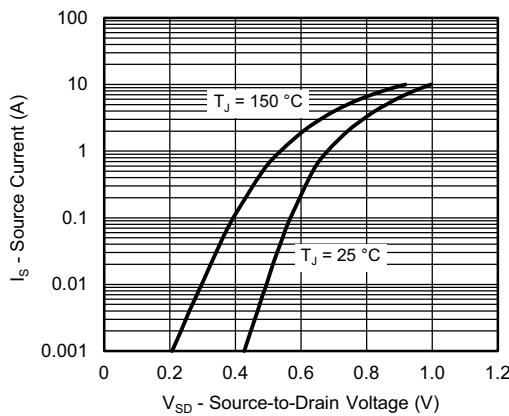
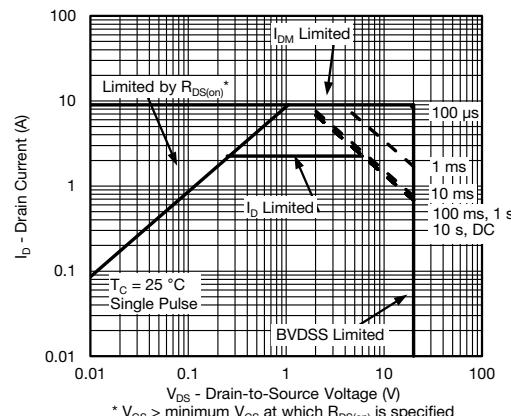
SPECIFICATIONS ($T_C = 25^\circ\text{C}$, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$		30	-	-	V	
Gate-source threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$		0.45	0.6	1.1		
Gate-source leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = \pm 8 \text{ V}$		-	-	± 100	nA	
Zero gate voltage drain current	I_{DSS}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 30 \text{ V}$	-	-	1	μA	
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 30 \text{ V}$, $T_J = 125^\circ\text{C}$	-	-	50		
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 30 \text{ V}$, $T_J = 175^\circ\text{C}$	-	-	250		
On-state drain current ^a	$I_{D(\text{on})}$	$V_{GS} = 4.5 \text{ V}$	$V_{DS} \geq 5 \text{ V}$	10	-	-	A	
Drain-source on-state resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 4.5 \text{ V}$	$I_D = 3 \text{ A}$	-	0.038	0.065	Ω	
		$V_{GS} = 4.5 \text{ V}$	$I_D = 3 \text{ A}$, $T_J = 125^\circ\text{C}$	-	-	0.077		
		$V_{GS} = 4.5 \text{ V}$	$I_D = 3 \text{ A}$, $T_J = 175^\circ\text{C}$	-	-	0.090		
		$V_{GS} = 2.5 \text{ V}$	$I_D = 3 \text{ A}$	-	0.048	0.095		
Forward transconductance ^b	g_{fs}	$V_{DS} = 15 \text{ V}$, $I_D = 2 \text{ A}$		-	16	-	S	
Dynamic ^b								
Input capacitance	C_{iss}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 20 \text{ V}$, $f = 1 \text{ MHz}$	-	312	440	pF	
Output capacitance	C_{oss}			-	56	80		
Reverse transfer capacitance	C_{rss}			-	29	41		
Total gate charge ^c	Q_g	$V_{GS} = 4.5 \text{ V}$	$V_{DS} = 15 \text{ V}$, $I_D = 4.2 \text{ A}$	-	4.6	6	nC	
Gate-source charge ^c	Q_{gs}			-	0.65	-		
Gate-drain charge ^c	Q_{gd}			-	0.9	-		
Gate resistance	R_g	$f = 1 \text{ MHz}$		2.1	3.6	5.7	Ω	
Turn-on delay time ^c	$t_{d(\text{on})}$	$V_{DD} = 10 \text{ V}$, $R_L = 10 \Omega$ $I_D \geq 1 \text{ A}$, $V_{GEN} = 4.5 \text{ V}$, $R_g = 1 \Omega$		-	7.3	11	ns	
Rise time ^c	t_r			-	18	26		
Turn-off delay time ^c	$t_{d(\text{off})}$			-	21	32		
Fall time ^c	t_f			-	9.5	14		
Source-Drain Diode Ratings and Characteristics ^b								
Pulsed current ^a	I_{SM}			-	-	24	A	
Forward voltage	V_{SD}	$I_F = 4.5 \text{ A}$, $V_{GS} = 0 \text{ V}$		-	0.75	1.2	V	

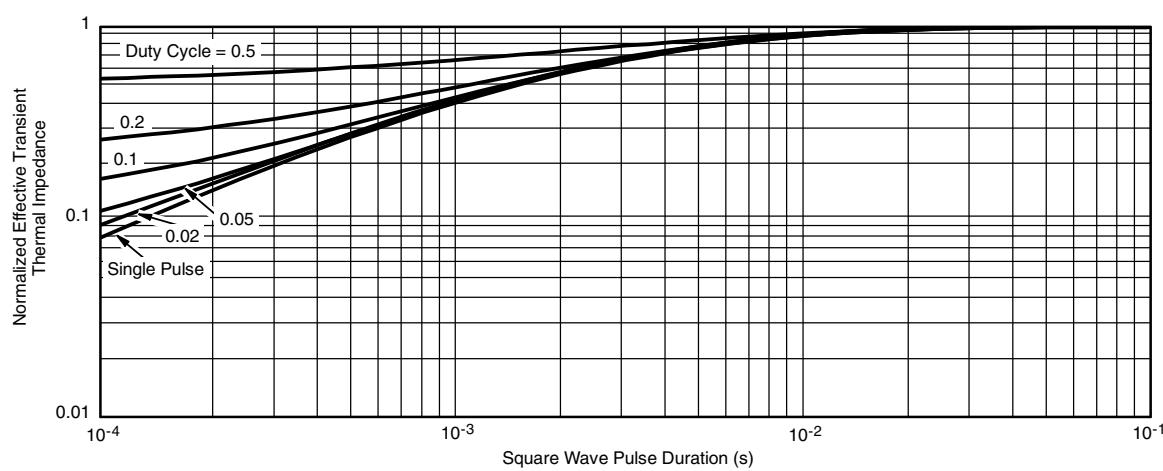
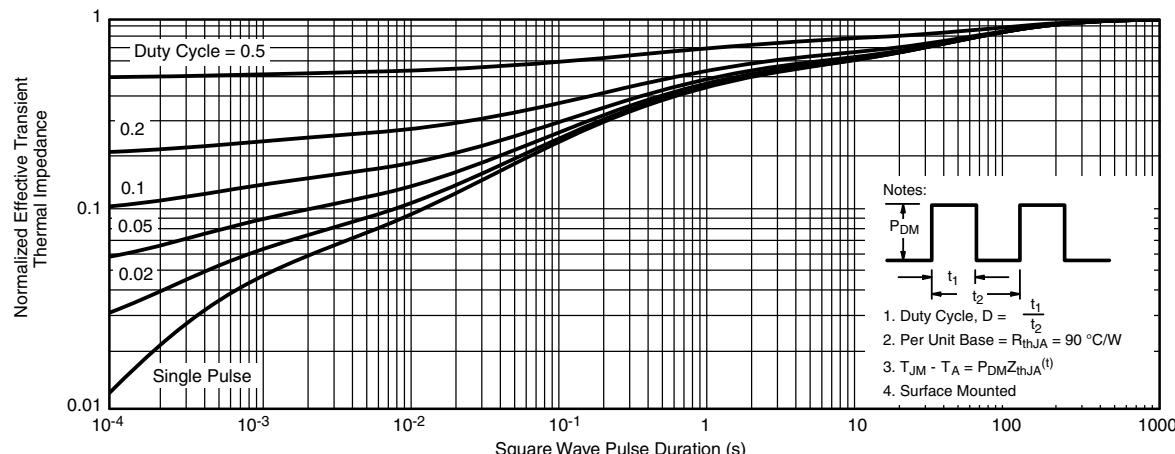
Notes

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2 \%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Output Characteristics

Transfer Characteristics

Transconductance

On-Resistance vs. Drain Current

Capacitance

Gate Charge

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

On-Resistance vs. Junction Temperature

Threshold Voltage

On-Resistance vs. Gate-to-Source Voltage

Drain Source Breakdown vs. Junction Temperature

Source Drain Diode Forward Voltage

Safe Operating Area

THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25°C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25°C)
- are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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- Поставка сложных, дефицитных, либо снятых с производства позиций;
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- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

Телефон: 8 (812) 309 58 32 (многоканальный)

Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.