

# Load Switch ICs 0.5A, 1.0A Load Switch ICs

## BD2200GUL BD2201GUL

## **General Description**

BD2200GUL and BD2201GUL are load switch ICs (N-Channel MOSFET) for portable devices. These ICs have an ON-Resistance of  $100m\Omega$  (Typ).

Furthermore, soft-start and output discharge circuits are integrated.

#### Features

- Single Channel of Low ON-Resistance N-Channel MOSFET Built in
- Soft-Start Circuit
- Output Discharge Circuit

#### Applications

Load Switches for Mobile Phone , Digital Still Cameras, PDA, MP3 Players, PCs, Portable Devices

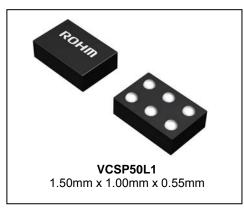
#### Key Specifications

Input Voltage Range:	2.7V to 5.5V
ON-Resistance:	100mΩ(Typ)
Continuous Current:	
BD2200GUL	0.5A(Max)
BD2201GUL	1.0A(Max)
Standby Current:	0.01µA(Typ)

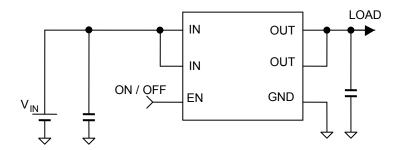
■ Operating Temperature Range: -25°C to +85°C

#### Package

W(Typ) D(Typ) H(Max)



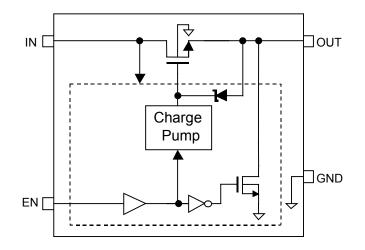
## **Typical Application Circuit**



#### Lineup

On-Resistance	Output Current	Output Turn On Time	Control Logic	Package		Orderable Part Number
100mΩ	0.5A	1.0ms	High	VCSP50L1	Reel of 3000	BD2200GUL-E2
100mΩ	1.0A	1.0ms	High	VCSP50L1	Reel of 3000	BD2201GUL-E2

## Block Diagram



## **Pin Configuration**

(BOTTOM VIEW)

в	IN	OUT	OUT
A	IN	EN	GND
	1	2	3

## **Pin Description**

Pin Number	Pin Name	Pin Function
A3	GND	Ground
B2, B3	OUT	Switch output (Connect each pin externally)
A1, B1	IN	Switch input (Connect each pin externally)
A2	EN	Enable input (Active-high switch on input)

## Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
IN Supply Voltage	V <sub>IN</sub>	-0.3 to +6.0	V
EN Input Voltage	$V_{\text{EN}}$	-0.3 to V <sub>IN</sub> + 0.3	V
OUT Voltage	V <sub>OUT</sub>	-0.3 to +6.0	V
Storage Temperature	Tstg	-55 to +150	°C
Power Dissipation	Pd	0.57 <sup>(Note 1)</sup>	W

(Note 1) Derate by 4.6mW /°C when operating above Ta=25°C (mounted on 50mm x 58mm x 1.75mm board). **Caution:** Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

## **Recommended Operating Conditions**

Parameter	Symbol		Unit		
Farameter	Symbol	Min	Тур	Max	Unit
Switch Input Voltage	V <sub>IN</sub>	2.7	3.3	5.5	V
Operation Temperature	Topr	-25	+25	+85	°C
Output Current (BD2200GUL)	I <sub>LO</sub>	0	-	500	mA
Output Current (BD2201GUL)	I <sub>LO</sub>	0	-	1000	mA

## **Electrical Characteristics**

BD2200GUL (Unless otherwise specified, V<sub>IN</sub>= 3.3V, Ta= 25°C)

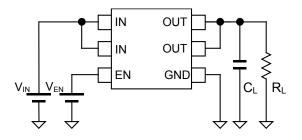
Decementar		Limit		1.1	O an dition a		
Parameter	Symbol	Min	Тур	Max	Unit	Conditions	
[Current Consumption]							
Operating Current	I <sub>DD</sub>	-	20	30	μA	V <sub>EN</sub> = 1.2V, V <sub>OUT</sub> = Open	
Standby Current	I <sub>STB</sub>	-	0.01	1	μA	V <sub>EN</sub> = 0V, V <sub>OUT</sub> = Open	
[I/O]							
EN Input Voltage	V <sub>ENH</sub>	1.2	-	-	V	High Level Input	
	V <sub>ENL</sub>	-	-	0.4	V	Low Level Input	
EN Input Current	I <sub>EN</sub>	-1	-	+1	μA	$V_{EN}$ = 0V or $V_{EN}$ = 1.2V	
[Power Switch]							
ON-Resistance	R <sub>ON</sub>	-	100	200	mΩ	I <sub>LO</sub> = 500mA	
Switch Leakage Current	I <sub>LEAK</sub>	-	0.01	1	μA	$V_{EN} = 0V, V_{OUT} = 0V$	
Output Rise Time	t <sub>ON1</sub>	-	1.0	2.0	ms	$R_L$ = 10 $\Omega$ , $V_{OUT}$ : 10% to 90%	
Output Turn-ON Time	t <sub>ON2</sub>	-	1.2	2.4	ms	$R_L$ = 10Ω, $V_{EN}$ : 50% to $V_{OUT}$ : 90%	
Output Fall Time	t <sub>OFF1</sub>	-	2.5	5.0	μs	$R_L$ = 10Ω, $V_{OUT}$ : 90% to 10%	
Output Turn-Off Time	t <sub>OFF2</sub>	-	4.5	9.0	μs	$R_L = 10\Omega, V_{EN}$ : 50% to $V_{OUT}$ : 10%	
[Discharge Circuit]							
Discharge ON-Resistance	R <sub>DISC</sub>	-	70	110	Ω	I <sub>LO</sub> = -1mA, V <sub>EN</sub> = 0V	
Discharge Current	I <sub>DISC</sub>	-	15	20	mA	V <sub>OUT</sub> = 3.3V, V <sub>EN</sub> = 0V	

## **Electrical Characteristics - continued**

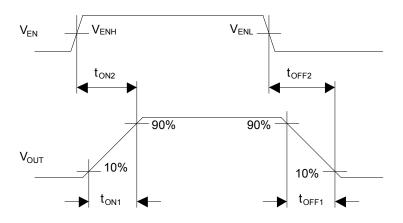
BD2201GUL (Unless otherwise specified, V<sub>IN</sub>= 3.3V, Ta= 25°C)

Deremeter	Sumbol		Limit		11	
Parameter	Symbol	Min	Тур	Max	Unit	Conditions
[Current Consumption]						
Operating Current	I <sub>DD</sub>	-	20	30	μA	V <sub>EN</sub> = 1.2V, V <sub>OUT</sub> = Open
Standby Current	I <sub>STB</sub>	-	0.01	1	μA	V <sub>EN</sub> = 0V, V <sub>OUT</sub> = Open
[I/O]						
	V <sub>ENH</sub>	1.2	-	-	V	High Level Input
EN Input Voltage	VENL	-	-	0.4	V	Low Level Input
EN Input Current	I <sub>EN</sub>	-1	-	+1	μA	$V_{EN} = 0V \text{ or } V_{EN} = 1.2V$
[Power Switch]						
ON-Resistance	Ron	-	100	180	mΩ	I <sub>LO</sub> = 500mA
Switch Leakage Current	I <sub>LEAK</sub>	-	0.01	1	μA	V <sub>EN</sub> = 0V, V <sub>OUT</sub> = 0V
Output Rise Time	t <sub>ON1</sub>	-	1.0	2.0	ms	$R_L = 10\Omega$ , $V_{OUT}$ : 10% to 90%
Output Turn-ON Time	t <sub>ON2</sub>	-	1.2	2.4	ms	$R_L$ = 10 $\Omega$ , V <sub>EN</sub> : 50% to V <sub>OUT</sub> : 90%
Output Fall Time	t <sub>OFF1</sub>	-	2.5	5.0	μs	$R_L = 10\Omega, V_{OUT}: 90\%$ to 10%
Output Turn-OFF Time	t <sub>OFF2</sub>	-	4.5	9.0	μs	$R_L$ = 10 $\Omega$ , $V_{EN}$ : 50% to $V_{OUT}$ : 10%
[Discharge Circuit]		-				·
Discharge ON-Resistance	R <sub>DISC</sub>	-	70	110	Ω	I <sub>LO</sub> = -1mA, V <sub>EN</sub> = 0V
Discharge Current	IDISC	-	15	20	mA	V <sub>OUT</sub> = 3.3V, V <sub>EN</sub> = 0V

## **Test Circuit**



## Switch Output Turn ON/OFF Timing



## Typical Performance Curves

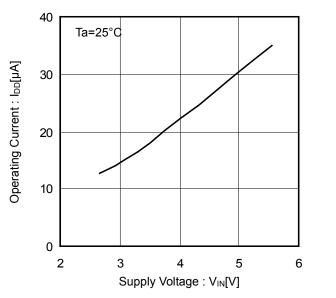
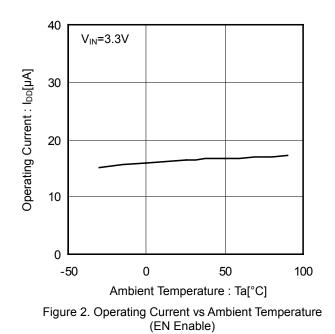
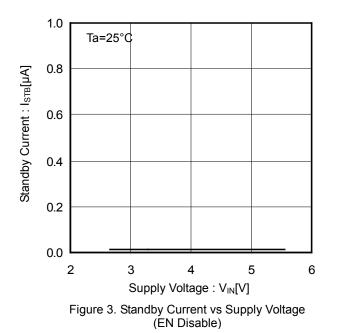
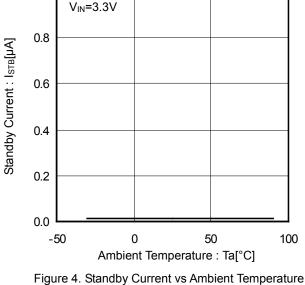


Figure 1. Operating Current vs Supply Voltage (EN Enable)





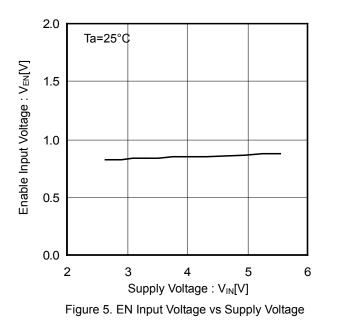


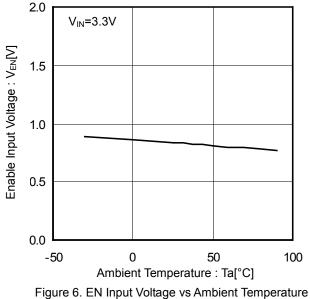
1.0

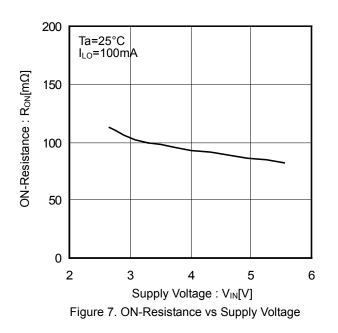
(EN Disable)

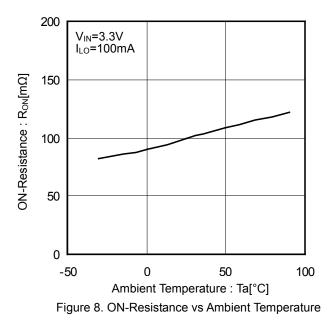
## **Typical Performance Curves - continued**

Datasheet









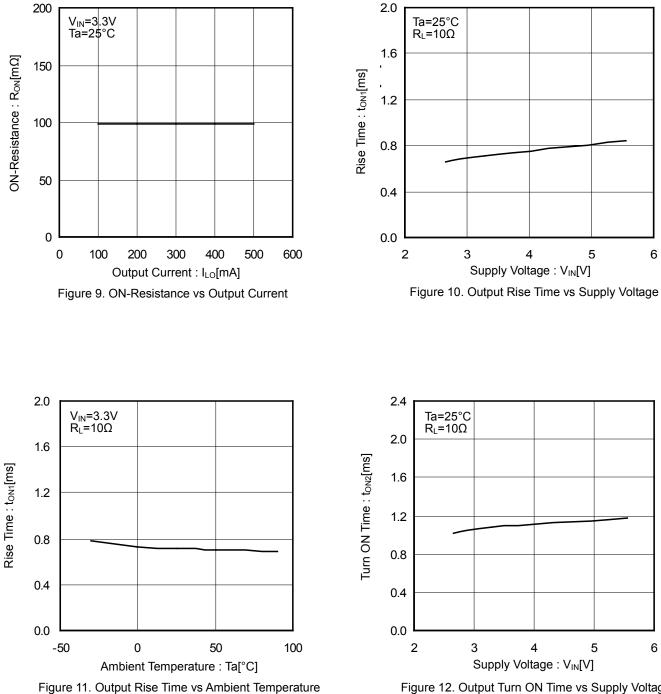


Figure 12. Output Turn ON Time vs Supply Voltage

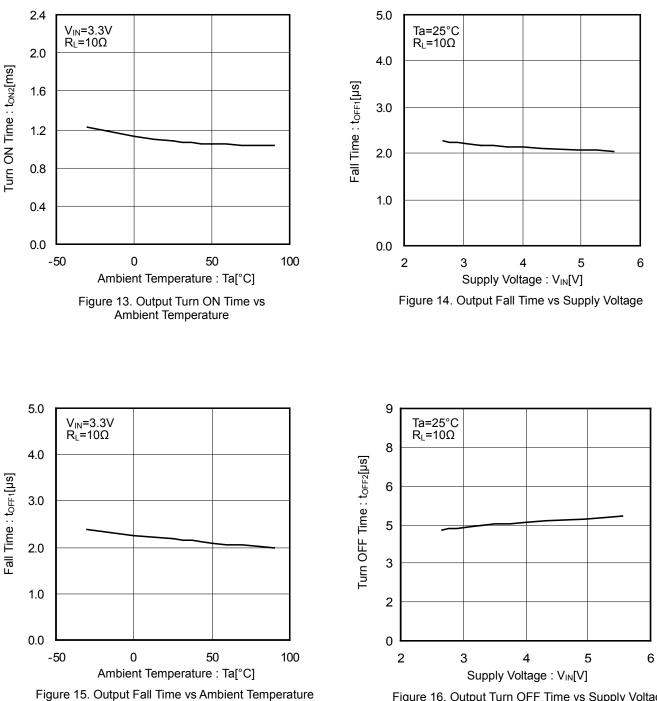
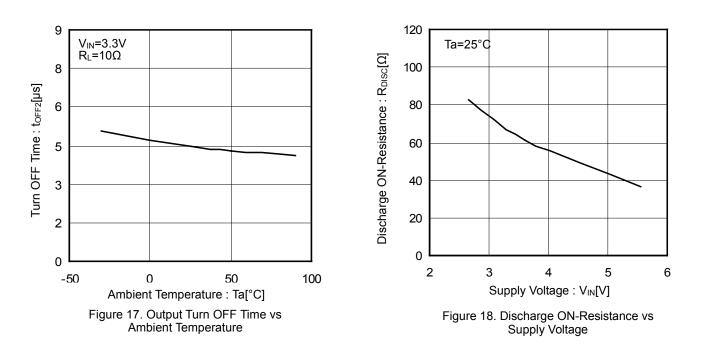
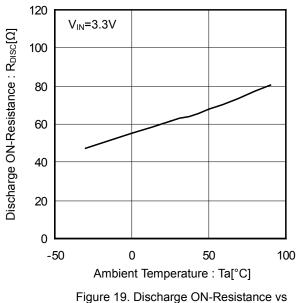


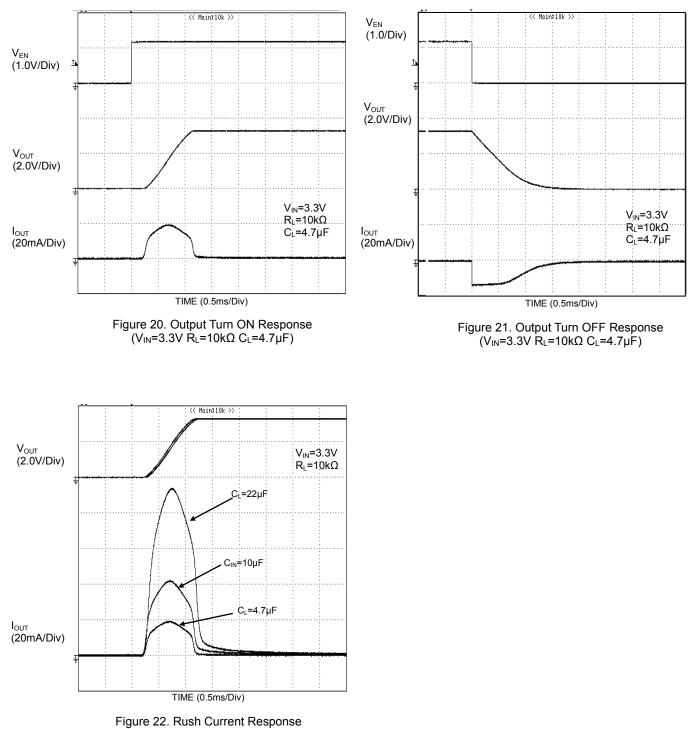
Figure 16. Output Turn OFF Time vs Supply Voltage





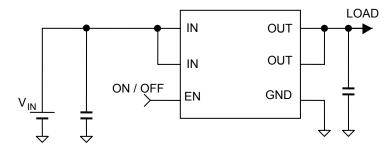
Ambient Temperature

#### Typical Wave Forms (BD2200GUL)



 $<sup>(</sup>V_{IN}=3.3V R_L=10k\Omega)$ 

## **Application Circuit Example**



This application circuit does not guarantee its operation. When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including AC/DC characteristics as well as dispersion of the IC.

#### Functional Description

1. Switch Operation

IN and OUT are connected to the MOSFET's drain and source. By setting the EN input to high, the internal charge pump operates and turns on the MOSFET. When the MOSFET is turned on, the switch becomes bi-directional. Consequently, when  $V_{IN} < V_{OUT}$ , the current is flowing from OUT to IN.

2. Output Discharge Circuit

The output discharge circuit operates when the switch is turned off. During its operation, a  $70\Omega$  (Typ) resistor is connected between OUT and GND. Unwanted electrical charge is discharged quickly into this resistor.

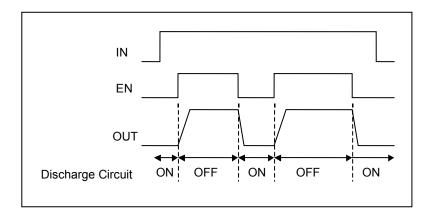


Figure 23. Timing Diagram

## **Power Dissipation**

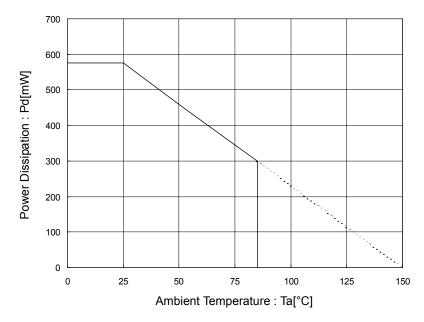




Figure 24. Power Dissipation Curve (Pd-Ta Curve)
(VCSP50L1 Package)

## I/O Equivalence Circuit

Pin Name	Pin Number	Equivalence Circuit
EN	A2	
IN OUT	A1, B1 B2, B3	

#### **Operational Notes**

#### 1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

#### 2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

#### 3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

#### 4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

#### 5. Thermal Consideration

Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. The absolute maximum rating of the Pd stated in this specification is when the IC is mounted on a 70mm x 70mm x 1.6mm glass epoxy board. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

#### 6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

#### 7. In rush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

#### 8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

#### 9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

#### 10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

## **Operational Notes - continued**

#### 11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

#### 12. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode. When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

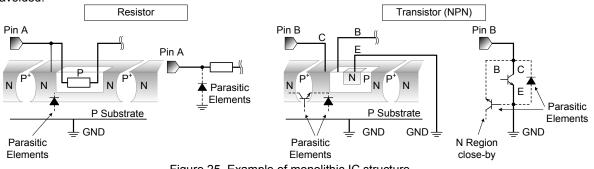


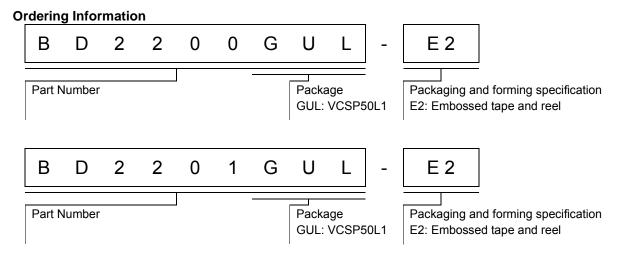
Figure 25. Example of monolithic IC structure

#### 13. Ceramic Capacitor

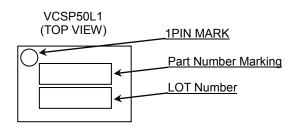
When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

#### 14. Disturbance light

In a device where a portion of silicon is exposed to light such as in a WL-CSP, IC characteristics may be affected due to photoelectric effect. For this reason, it is recommended to come up with countermeasures that will prevent the chip from being exposed to light.



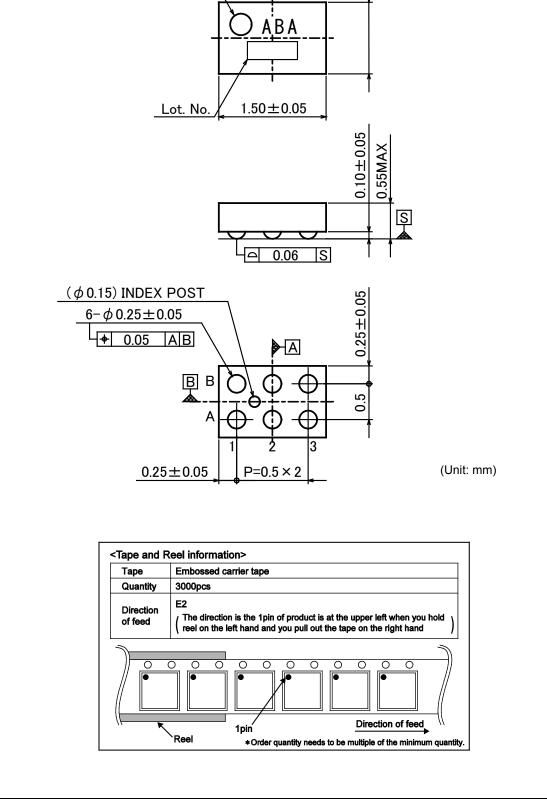
## **Marking Diagram**



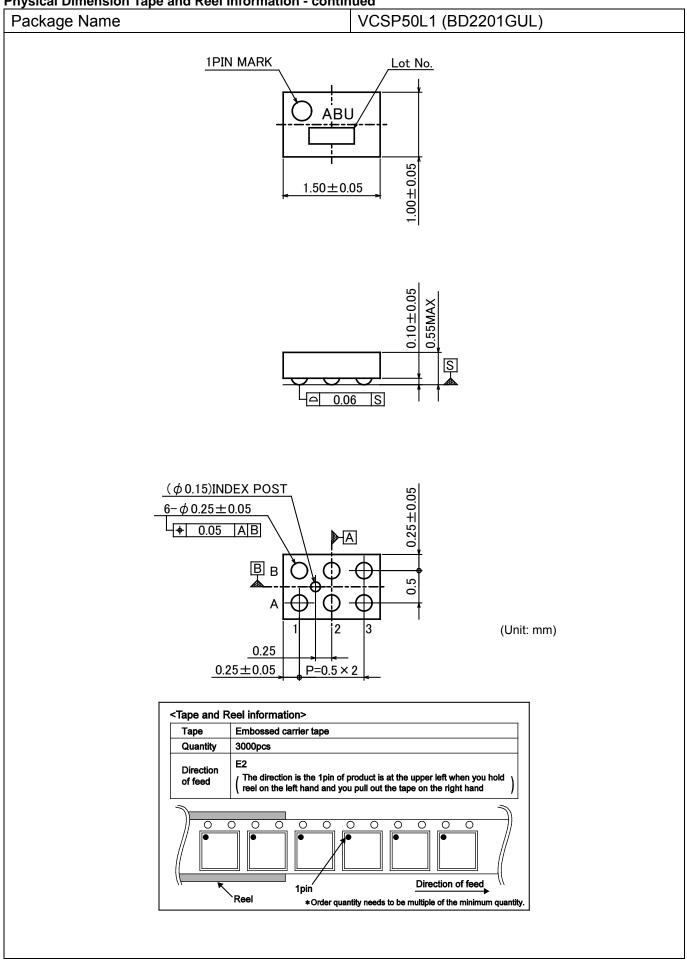
Part Number	Part Number Marking
BD2200GUL	ABA
BD2201GUL	ABU

## Physical Dimension Tape and Reel Information

Package Name	VCSP50L1 (BD2200GUL)	
	02	
1PIN MARK	0.0	
	0.	



#### Physical Dimension Tape and Reel Information - continued



## **Revision History**

Date	Revision	Changes	
11.Mar.2013	001	New Release	
21.Aug.2014	002	Applied the ROHM Standard Style and improved understandability.	

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(Note1) Medical Equipment Classification of the Specific Applications
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JAPAN	USA	EU	CHINA
CLASSⅢ			
CLASSⅣ	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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ROHM Semiconductor: BD2201GUL-E2 BD2200GUL-E2



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

**Телефон:** 8 (812) 309 58 32 (многоканальный) **Факс:** 8 (812) 320-02-42 **Электронная почта:** <u>org@eplast1.ru</u> **Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.