

# **TPD12S016 Evaluation Module**

# 1 Features

- Confirms to HDMI Compliance Tests without any External Components
- Supports HDMI1.4 Data Rate
- Match Class D and Class C pin mapping
- Eight channel ESD lines for four differential pairs with ultra-low differential capacitance matching (0.05pF)
- On-chip load switch with 55mA current limit feature at the HDMI 5V\_OUT pin.
- Auto-direction sensing I2C level shifter with one-shot circuit to drive long HDMI cable (750pF Load)
- Back-drive protection on HDMI connector side ports
- Integrated pull-up and pull-down resistors per HDMI specification
- IEC61000-4-2 (Level 4) ESD Performance at all external pins
- Space Saving 24-pin RKT Package (2mm x 4mm) and 24-TSSOP Package

# 2 Applications

- Cell Phones
- eBook
- Portable Media Players
- Set-top Box



# 3 EVM Description

The TPD12S016 is a single-chip HDMI interface device with auto-direction sensing I2C voltage level shift buffers, load switch, and integrated high-speed ESD protection clamps. The device pin mapping matches the HDMI Type D connector with four differential pairs. This device offers eight low-capacitance ESD clamps, allowing HDMI 1.4 data rates. The integrated ESD circuits provide good matching between each differential signal pair, which allows an advantage over discrete ESD solutions where variations between ESD protection clamps degrade the differential signal quality. The TPD12S016 provides a current limited 5 V output (5V\_OUT) for sourcing the HDMI power line. The current limited 5 V output supplies up to 55 mA to the HDMI receiver. The control of 5V\_OUT and the hot plug detect (HPD) circuitry is independent of the LS\_OE control signal and is controlled by the CT\_HPD pin. This independent control enables the detection scheme (5V\_OUT + HPD) to be active before enabling the HDMI link. An internal 3.3V node powers the CEC pin eliminating the need for an onboard 3.3V supply.

The TPD12S016 integrates all the external termination resistors at the HPD, CEC, SCL, and SDA lines. There are three non-inverting bi-directional translation circuits for the SDA, SCL, and CEC lines. Each have a common power rail (VCCA) on the A side from 1.1 V to 3.6V. On the B side, the SCL\_B and SDA\_B each have an internal 1.75 k $\Omega$  pullup connected to the regulated 5 V rail (5V\_OUT). The SCL and SDA pins meet the I2C specification and drive up to 750 pF capacitive loads exceeding the HDMI1.4 specifications. The CEC\_B pin has an internal 27 k $\Omega$  pullup to the internal 3.3 V supply rail. The HPD\_B port has a glitch filter to avoid false detection due to plug bouncing during the HDMI connector insertion.

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# 4 EVM Configuration

The TPD12S016 EVM has three separate sections, the TOP, MIDDLE, and BOTTOM sections. Each section includes a TPD12S016, input/output (IO) ports, and jumper switches for enabling or disabling the TPD12S016's Level Shifter (LS), Hot Plug Detect and 5VOUT. VCC can be powered by either an HDMI input or a 5V SUPPLY banana clip.

OUTPUT is side B; INPUT is side A. TOP, MIDDLE, and BOTTOM sections are all supplied with power using VBAT, VCCA, and GND.

- 1. The TOP section has Type-A HDMI male connectors for IO.
- 2. The MIDDLE section has Type-C HDMI male connectors for IO.
- The BOTTOM section has a Type-A male connector as input for the four differential pair signals only. For probe testing of the high-speed differential signals, test points are provided on the board. Configurable output loads using R1-R8 & C1-C4 are provided. This configuration provides a means for developing eye diagrams under no load or loaded conditions



Jumper Switch Configurations			
LS (LS_OE)	disabled	enabled	
CT_CP_HPD	disabled	enabled	
5V			
Selects source of output's +5 Power Signal (Pin 18)	Using the +5Vout of TPD12S016	Bypass +5VOUT of TPD12S016	

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TPD12S016 Evaluation Module



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# 5 TPD12S016EVM Schematic



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#### General Statement for EVMs including a radio

User Power/Frequency Use Obligations: This radio is intended for development/professional use only in legally allocated frequency and power limits. Any use of radio frequencies and/or power availability of this EVM and its development application(s) must comply with local laws governing radio spectrum allocation and power limits for this evaluation module. It is the user's sole responsibility to only operate this radio in legally acceptable frequency space and within legally mandated power limitations. Any exceptions to this are strictly prohibited and unauthorized by Texas Instruments unless user has obtained appropriate experimental/development licenses from local regulatory authorities, which is responsibility of user including its acceptable authorization.

## For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

#### Caution

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Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

#### FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### For EVMs annotated as IC – INDUSTRY CANADA Compliant

This Class A or B digital apparatus complies with Canadian ICES-003.

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#### Concerning EVMs including radio transmitters

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Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada.

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

#### Concernant les EVMs avec appareils radio

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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- 2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
- 3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

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