

## P-Channel 30 V (D-S) MOSFET



### FEATURES

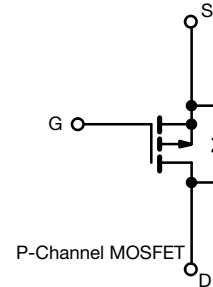
- TrenchFET® Gen III p-channel power MOSFET
- 100%  $R_g$  tested
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- Adapter switch
- Load switch
- Power management in battery-operated, mobile and wearable devices



PRODUCT SUMMARY	
$V_{DS}$ (V)	-30
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = -10$ V	0.0079
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = -4.5$ V	0.0108
$Q_g$ typ. (nC)	44
$I_D$ (A)	-16 <sup>a, e</sup>
Configuration	Single

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	Si4103DY-T1-GE3

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	$V_{DS}$	-30	V	
Gate-source voltage	$V_{GS}$	$\pm 20$		
Continuous drain current ( $T_J = 150$ °C)	$I_D$	$T_C = 25$ °C	-16 <sup>a</sup>	A
		$T_C = 70$ °C	-16 <sup>a</sup>	
		$T_A = 25$ °C	-14 <sup>b, c</sup>	
		$T_A = 70$ °C	-11.3 <sup>b, c</sup>	
Pulsed drain current ( $t = 100$ $\mu$ s)	$I_{DM}$	-80 <sup>a</sup>		
Continuous source-drain diode current	$I_S$	$T_C = 25$ °C	-4.3	
		$T_A = 70$ °C	-2.1 <sup>b, c</sup>	
Maximum power dissipation	$P_D$	$T_C = 25$ °C	5.2	W
		$T_C = 70$ °C	3.3	
		$T_A = 25$ °C	2.5 <sup>b, c</sup>	
		$T_A = 70$ °C	1.6 <sup>b, c</sup>	
Operating junction and storage temperature range	$T_J, T_{stg}$	-55 to +150	°C	
Soldering recommendations (peak temperature)		260		

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient <sup>b, d</sup>	$R_{thJA}$	40	50	°C/W	
Maximum junction-to-foot (drain)	$R_{thJF}$	20	24		

#### Notes

- Package limited
- Surface mounted on 1" x 1" FR4 board
- $t = 10$  s
- Maximum under steady state conditions is 85 °C/W
- $T_C = 25$  °C



SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA	-30	-	-	V
V <sub>DS</sub> temperature coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = -250 μA	-	-23	-	mV/°C
V <sub>GS(th)</sub> temperature coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>		-	5	-	
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-1	-	-2	V
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V	-	-	-1	μA
		V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	-10	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ -5 V, V <sub>GS</sub> = 0 V	-5	-	-	A
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -10 A	-	0.0067	0.0079	Ω
		V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -5 A	-	0.0090	0.0108	
Forward transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -20 A	-	60	-	S
<b>Dynamic <sup>b</sup></b>						
Input capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = -15 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	5200	-	pF
Output capacitance	C <sub>OSS</sub>		-	535	-	
Reverse transfer capacitance	C <sub>RSS</sub>		-	470	-	
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -10 A	-	92	140	nC
		V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10 A	-	44	66	
Gate-source charge	Q <sub>gs</sub>	V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -10 A	-	12.1	-	nC
Gate-drain charge	Q <sub>gd</sub>		-	14.8	-	
Gate resistance	R <sub>g</sub>	f = 1 MHz	0.8	3.8	7.6	Ω
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> = -15 V, R <sub>L</sub> = 3 Ω, I <sub>D</sub> ≅ -5 A, V <sub>GEN</sub> = -4.5 V, R <sub>g</sub> = 1 Ω	-	57	120	ns
Rise time	t <sub>r</sub>		-	38	80	
Turn-off delay time	t <sub>d(off)</sub>		-	55	110	
Fall time	t <sub>f</sub>		-	28	60	
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> = -15 V, R <sub>L</sub> = 3 Ω, I <sub>D</sub> ≅ -5 A, V <sub>GEN</sub> = -10 V, R <sub>g</sub> = 1 Ω	-	11	20	ns
Rise time	t <sub>r</sub>		-	17	35	
Turn-off delay time	t <sub>d(off)</sub>		-	78	160	
Fall time	t <sub>f</sub>		-	26	50	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous source-drain diode current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	-4.3	A
Pulse diode forward current	I <sub>SM</sub>		-	-	-80	
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = -5 A, V <sub>GS</sub> = 0 V	-	-0.8	-1.2	V
Body diode reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = -5 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C	-	31	60	ns
Body diode reverse recovery charge	Q <sub>rr</sub>		-	20	40	nC
Reverse recovery fall time	t <sub>a</sub>		-	13	-	ns
Reverse recovery rise time	t <sub>b</sub>		-	18	-	

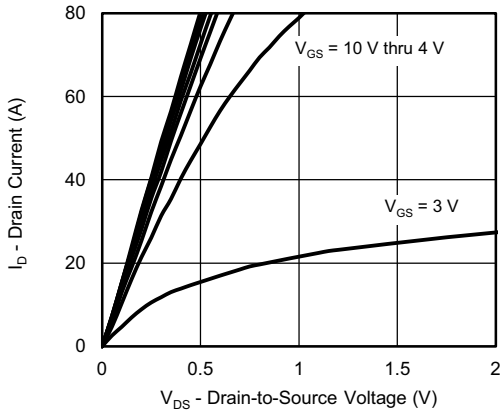
**Notes**

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b. Guaranteed by design, not subject to production testing

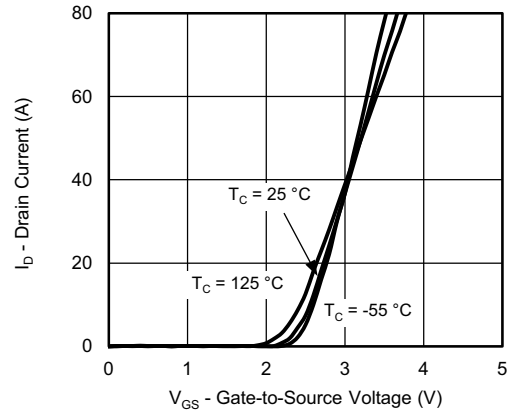
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



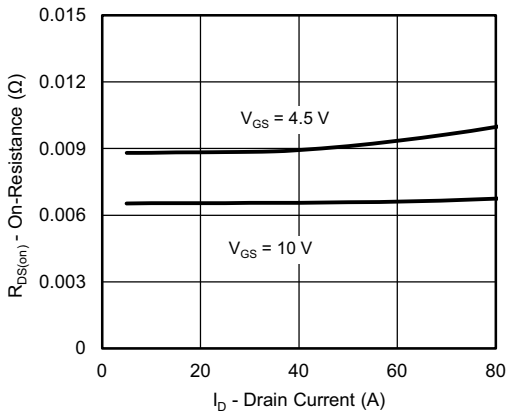
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



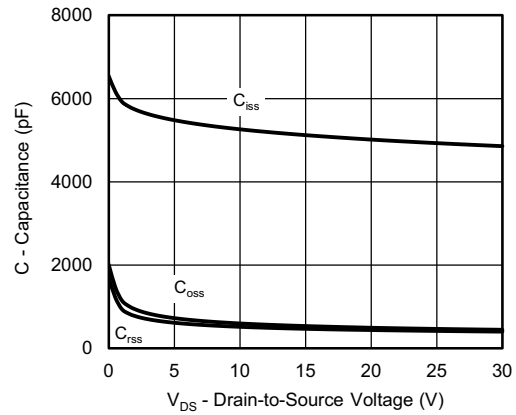
**Output Characteristics**



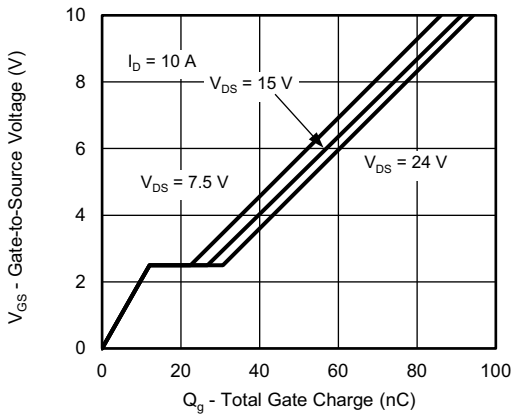
**Transfer Characteristics**



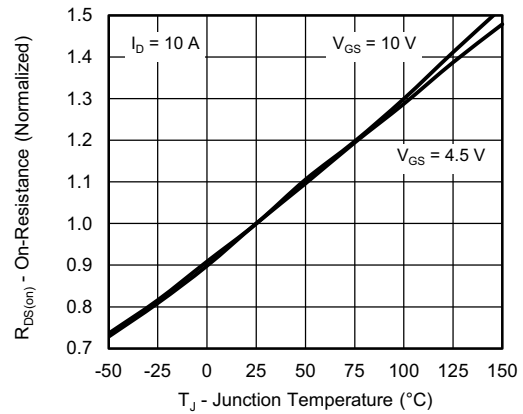
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**



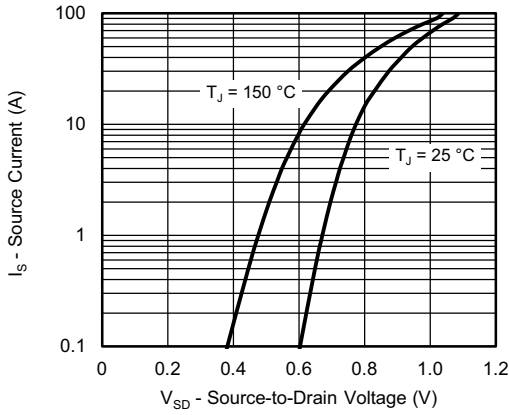
**Gate Charge**



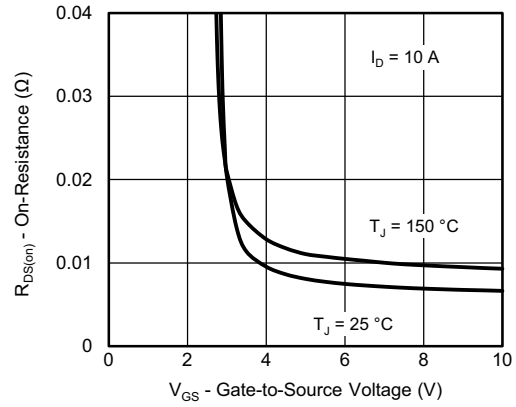
**On-Resistance vs. Junction Temperature**



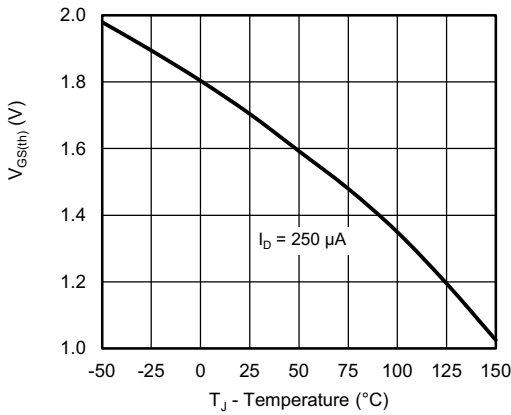
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



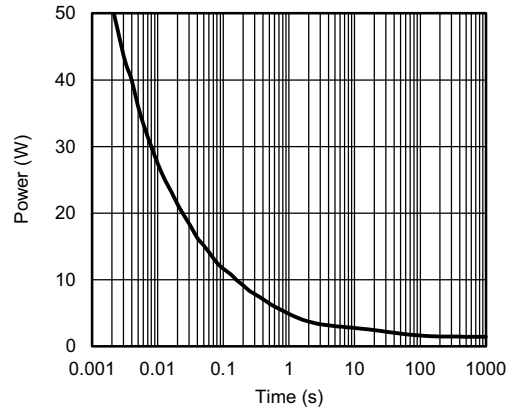
Source-Drain Diode Forward Voltage



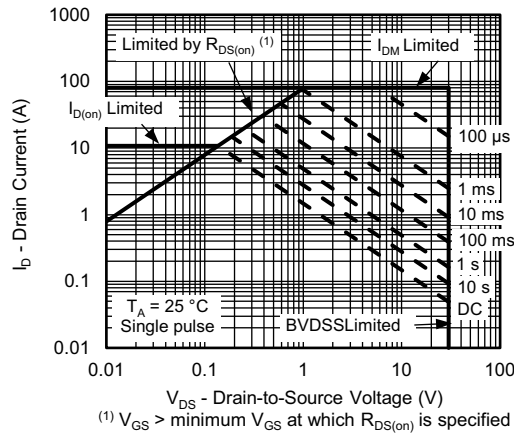
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



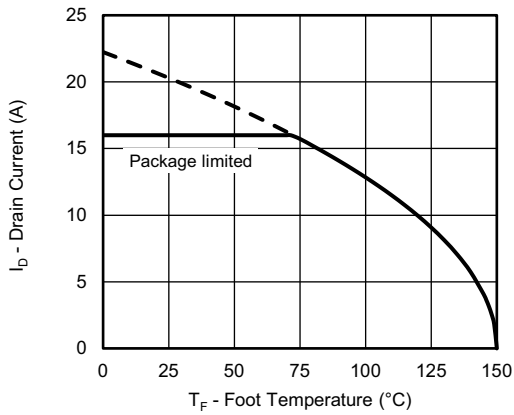
Single Pulse Power, Junction-to-Ambient



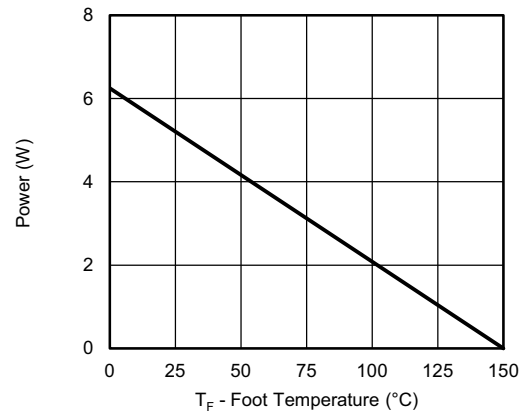
Safe Operating Area, Junction-to-Ambient



**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Current Derating <sup>a</sup>**



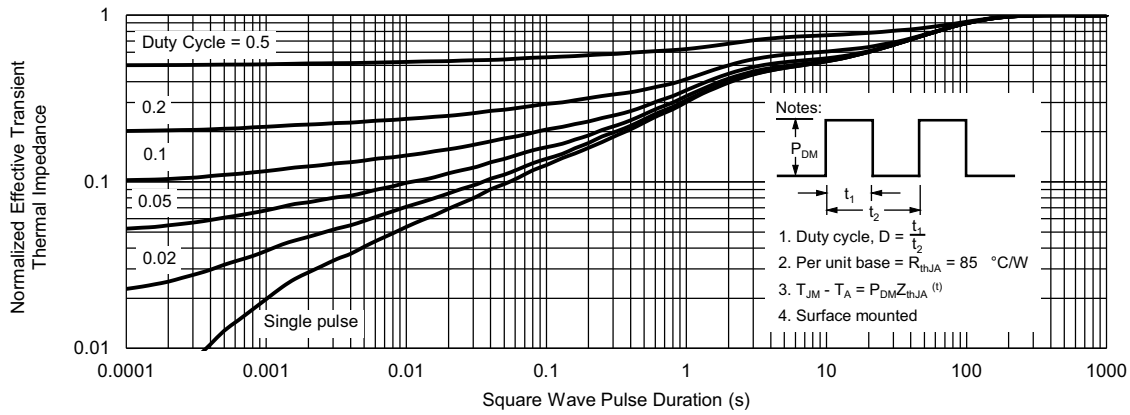
**Power, Junction-to-Foot**

**Note**

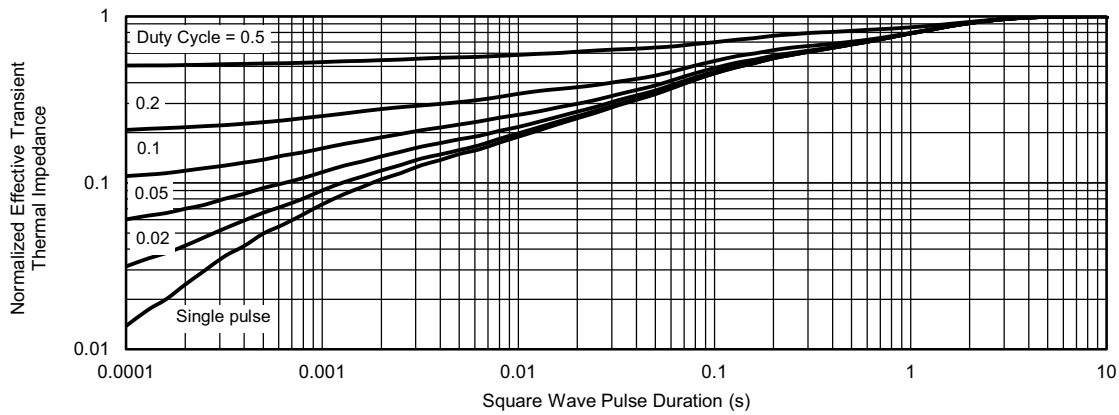
- a. The power dissipation  $P_D$  is based on  $T_J$  max. = 25 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Foot**

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- Техническая поддержка проекта;
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#### Как с нами связаться

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