

# Thick Film Resistor/Capacitor Networks, Single-In-Line, Conformal Coated SIP



## **FEATURES**

- · Isolated and bussed schematics available
- · Thick film resistors
- NP0 or X7R capacitors for line terminator
- Wide operating temperature range (- 55 °C to 125 °C)
- Custom Resistor/Capacitor schematics available



 Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### Note

\* Lead (Pb)-containing terminations are not RoHS-compliant. Exemptions may apply.

STANDARD ELECTRICAL SPECIFICATIONS											
MODEL	SCHEMATIC	RESISTOR CHARACTERISTICS				CAPACITOR CHARACTERISTICS					
		POWER RATING P <sub>70 °C</sub> W	RESISTANCE RANGE Ω	RESISTANCE TOLERANCE (1) ± %	TEMP. COEFF. ± ppm/°C	TYPE (2)	CAPACITANCE RANGE	CAPACITANCE TOLERANCE (3) ± %	CAPACITANCE VOLTAGE V <sub>DC</sub>		
TRC	01	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	50		
						X7R	470 pF to 0.1 μF	10, 20			
	02	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	- 50		
						X7R	470 pF to 0.1 μF	10, 20			
	09	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	- 50		
						X7R	470 pF to 0.1 μF	10, 20			

## Notes

- $^{(1)}$  ± 2 % standard, ± 1 % and ± 5 % available
- (2) NP0 capacitors may be substituted for X7R capacitors
- (3) Tighter tolerances available on request

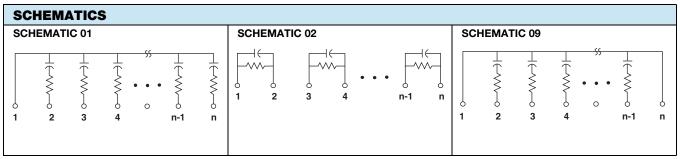
GLOBAL PART NUMBER INFORMATION												
New Global Part Numbering: TRC0801N101J560KTB (preferred part number format)												
	T    R	C   0	8   0   1	N   1	0   1	J   5	6   0	к   т	В			
								テデ	三,			
GLOBAL MODEL	PIN COUNT	SCHEMATIC	CHARACTERISTICS	RESISTANCE VALUE	RESISTANCE TOLERANCE	CAPACITANCE VALUE	CAPACITANCE TOLERANCE	TERMINAL FINISH	PACKAGING			
TRC	06 to 12	01	<b>N</b> = NP0	2 digit		(In picofarads)	<b>K</b> = 10 %	T =	<b>B</b> = Bulk			
	pin 02 available 09		X = X7R signification signific		<b>G</b> = 2 % <b>J</b> = 5 %	2 digit significant	<b>M</b> = 20 %	Sn90/Pb10 <b>C</b> =				
	06 =			followed by		figure,		Sn95.5/				
	6 pin			a multiplier		followed by a		Ag3.9/				
	08 =			<b>101</b> = $100 \Omega$		multiplier		Cu0.6				
	8 pin			<b>220</b> = $22 \Omega$		101 = 100 pF			-			
	12 =			<b>102</b> = 1 k $\Omega$		<b>392</b> = 3000 pF						
	12 pin					<b>104</b> = 0.1 μF						
Historic	al Part N	umbering: TF	C0801101J560KS10	) (will continu	e to be accep	ted)						
TRC 08 01 101 J 560 K S10									S10			
ΙΙ ΙΙΝΟΕΙΕΜΑΤΙΟΙ ΙΙΝΟΕΙΕΜΑΤΙΟΙ ΙΙ									ERMINAL FINISH			

#### **Notes**

Revision: 23-Jan-13

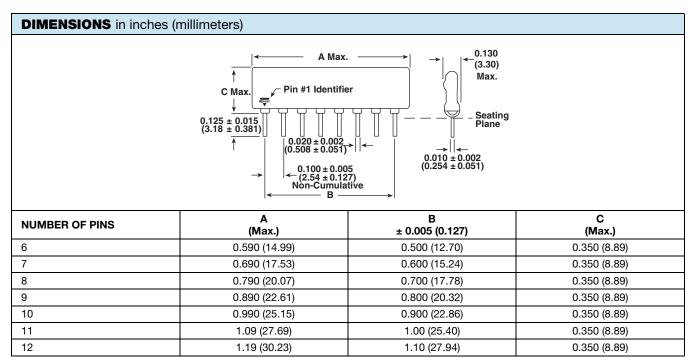
For additional information on packaging, refer to the Through-hole Network Packaging document (www.vishay.com/doc?31542).





#### Note

· Custom schematics available



### Note

Other sizes available



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