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Features

- This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from process 51
- Source & Drain are interchangeable.



Figure 1. J111 / J112 / J113 Device Package

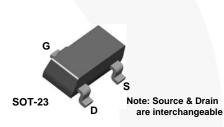


Figure 2. MMBFJ111 / MMBFJ112 / MMBFJ113 Device Package

Part Number	Top Mark	Package	Packing Method
J111	J111	TO-92 3L	Bulk
J111_D26Z	J111	TO-92 3L	Tape and Reel
J111_D74Z	J111	TO-92 3L	Ammo
J112	J112	TO-92 3L	Bulk
J112_D26Z J112		TO-92 3L	Tape and Reel
J112_D27Z	J112	TO-92 3L	Tape and Reel
J112_D74Z	J112	TO-92 3L	Ammo
J113	J113	TO-92 3L	Bulk
J113_D74Z	J113	TO-92 3L	Ammo
J113_D75Z J113		TO-92 3L	Ammo
MMBFJ111 6P		SOT-23 3L	Tape and Reel
MMBFJ112	6R	SOT-23 3L	Tape and Reel
MMBFJ113	6S	SOT-23 3L	Tape and Reel

Ordering Information

© 1997 Fairchild Semiconductor Corporation J111 / J112 / J113 / MMBFJ111 / MMBFJ112 / MMBFJ113 Rev. 1.5 January 2015

Absolute Maximum Ratings^{(1), (2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{DG}	Drain-Gate Voltage	35	V
V _{GS}	Gate-Source Voltage	-35	V
I _{GF}	Forward Gate Current	50	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

	Parameter	Ma		
Symbol		J111 / J112 / J113 ⁽³⁾	MMBFJ111 / MMBFJ112 / MMBFJ113 ⁽⁴⁾	Unit
PD	Total Device Dissipation	625	350	mW
۲D	Derate Above 25°C	5.0	2.8	mW/°C
R _{θJC}	Thermal Resistance, Junction-to-Case	125		°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient 200			°C/W

Notes:

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

4. Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm; mounting pad for the collector lead minimum 6cm².

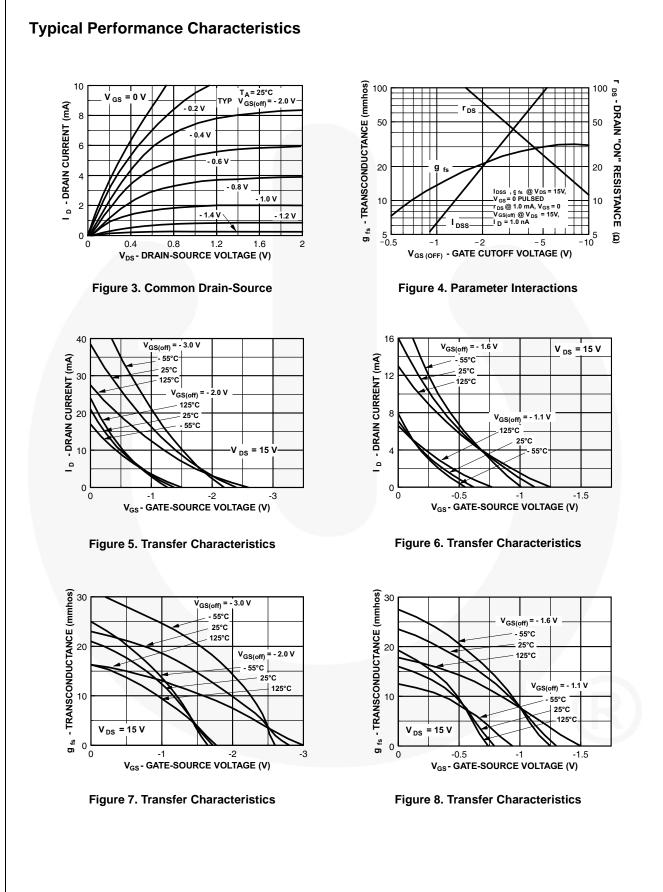
Electrical Characteristics

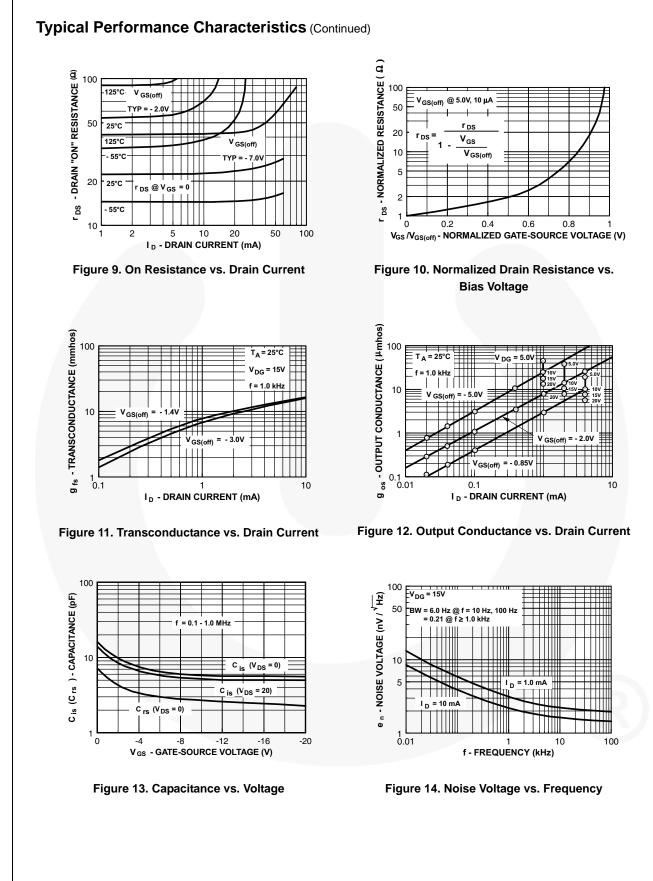
Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

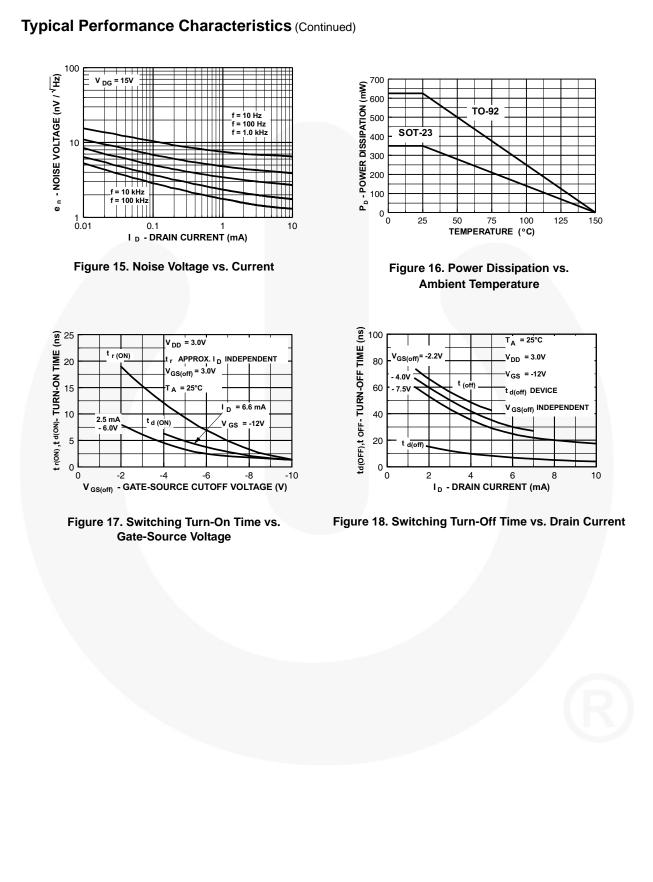
Symbol	Parameter	Conditions		Min.	Max.	Unit
Off Charac	teristics				I	
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = -1.0 \ \mu A, \ V_{DS} = 0$		-35		V
I _{GSS}	Gate Reverse Current	V _{GS} = -15 V, V _{DS} = 0			-1.0	nA
			111	-3.0	-10.0	
V _{GS} (off)	Gate-Source Cut-Off Voltage	V_{DS} = 15 V, I _D = 1.0 µA	112	-1.0	-5.0	V
			113	-0.5	-3.0	
I _D (off)	Drain Cutoff Leakage Current	V _{DS} = 5.0 V, V _{GS} = -10 V	•		1.0	nA
On Charac	teristics				•	•
I _{DSS}	Zero-Gate Voltage Drain Current ⁽⁵⁾	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0$	111	20		mA
			112	5.0		
			113	2.0		
			111		30	
r _{DS} (on)	Drain-Source On Resistance	1 03 - 01 1, 103	112		50	Ω
			113		100	
Small Sign	al Characteristics		ł		l.	
C _{dg} (on) C _{sg} (on)	Drain-Gate &Source-Gate On Capacitance	V _{DS} = 0, V _{GS} = 0, f = 1.0 M	ЛНz		28	pF
C _{dg} (off)	Drain-Gate Off Capacitance	V _{DS} = 0, V _{GS} = -10 V, f = 1	.0 MHz		5.0	pF
C _{sg} (off)	Source-Gate Off Capacitance	V _{DS} = 0, V _{GS} = -10 V, f = 1	.0 MHz		5.0	pF

Note:

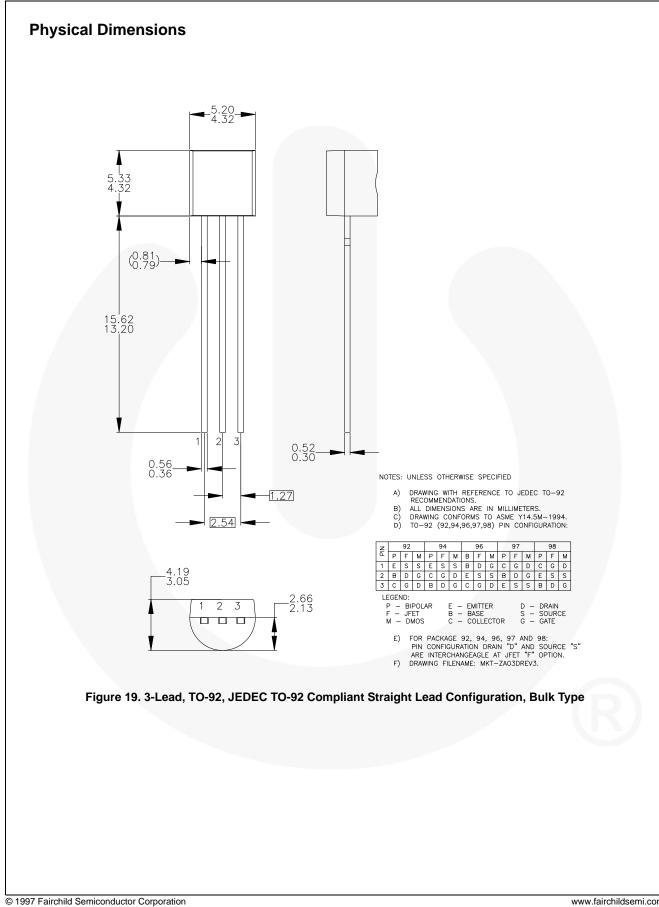
5. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2%.

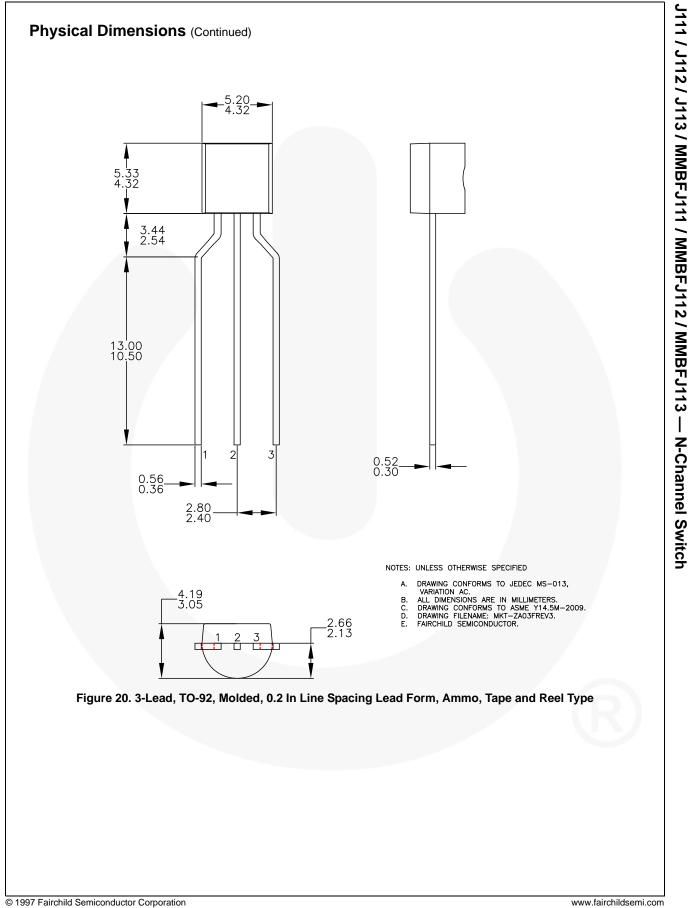


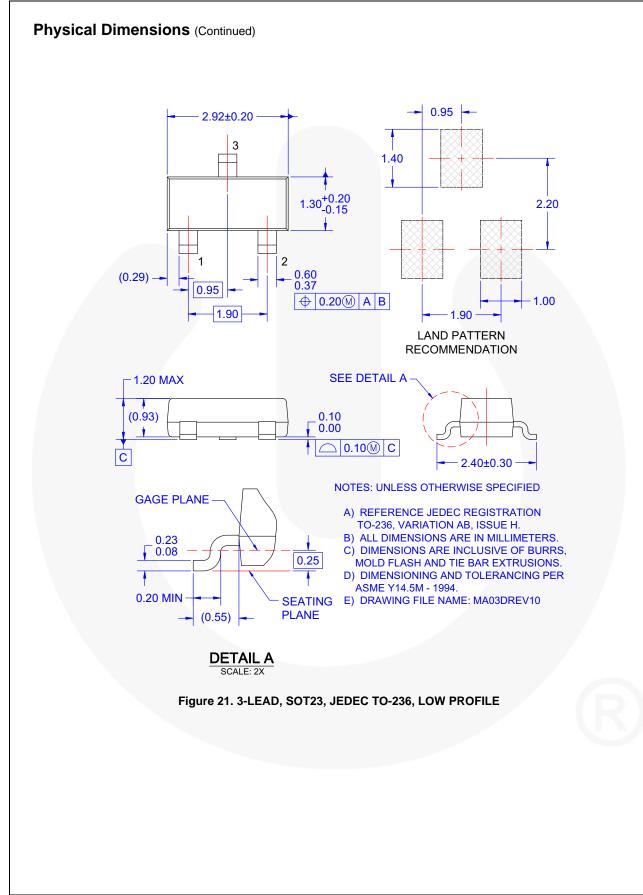




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