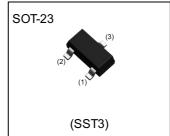


## NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

| Parameter            | Value |
|----------------------|-------|
| V <sub>CC</sub>      | 50V   |
| I <sub>C(MAX.)</sub> | 100mA |
| R <sub>1</sub>       | 10kΩ  |
| R <sub>2</sub>       | 47kΩ  |

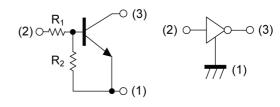
# ●Outline



### Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.

## •Inner circuit



- (1) GND (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

# Application

INVERTER, INTERFACE, DRIVER

## Packaging specifications

| Part No.  | Package                  | Package<br>size | Taping<br>code | Reel size<br>(mm) | Tape width (mm) | Basic<br>ordering<br>unit.(pcs) | Marking |
|-----------|--------------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| DTC114YCA | TC114YCA SOT-23 (SST3) 2 | 2924            | T116           | 180               | 8               | 3000                            | 64      |

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

| Parameter                    | Symbol                 | Values      | Unit |
|------------------------------|------------------------|-------------|------|
| Supply voltage               | V <sub>CC</sub>        | 50          | V    |
| Input voltage                | V <sub>IN</sub>        | -6 to 40    | V    |
| Output current               | Io                     | 70          | mA   |
| Collector current            | I <sub>C(MAX)</sub> *1 | 100         | mA   |
| Device a dispiration         | P <sub>D</sub> *2      | 200         | mW   |
| Power dissipation            | P <sub>D</sub> *3      | 350         | mW   |
| Junction temperature         | Tj                     | 150         | °C   |
| Range of storage temperature | T <sub>stg</sub>       | -55 to +150 | °C   |

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

| Davanastan           | C: resh al                     | Conditions  | Values |      |      | 11.4 |  |
|----------------------|--------------------------------|---|--------|------|------|------|--|
| Parameter            | Symbol                         | Conditions  | Min.   | Тур. | Max. | Unit |  |
| lance to college     | $V_{I(off)}$                   | V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA                | -      | -    | 0.3  | - v  |  |
| Input voltage        | V <sub>I(on)</sub>             | V <sub>O</sub> = 0.3V, I <sub>O</sub> = 1mA                 | 1.4    | -    | -    |      |  |
| Output voltage       | V <sub>O(on)</sub>             | $I_{O}/I_{I} = 5mA/0.25mA$                                  | -      | 100  | 300  | mV   |  |
| Input current        | I <sub>I</sub>                 | V <sub>I</sub> = 5V   | -      | -    | 880  | μA   |  |
| Output current       | I <sub>O(off)</sub>            | V <sub>CC</sub> = 50V, V <sub>I</sub> = 0V                  | -      | -    | 500  | nA   |  |
| DC current gain      | G <sub>I</sub>                 | V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA                   | 68     | -    | -    | -    |  |
| Input resistance     | R <sub>1</sub>                 | -   | 7      | 10   | 13   | kΩ   |  |
| Resistance ratio     | R <sub>2</sub> /R <sub>1</sub> | -   | 3.7    | 4.7  | 5.7  | -    |  |
| Transition frequency | f <sub>T</sub> *1              | V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA,<br>f = 100MHz | -      | 250  | -    | MHz  |  |

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<sup>\*1</sup> Characteristics of built-in transistor.

<sup>\*2</sup> Each terminal mounted on a reference land.

<sup>\*3</sup> Mounted on a ceramic board(7.0×5.0×0.6mm).

# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

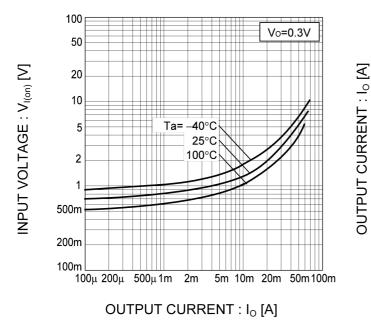


Fig.2 Output current vs. input voltage (OFF characteristics)

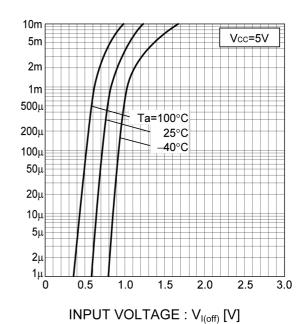


Fig.3 Output current vs. output voltage

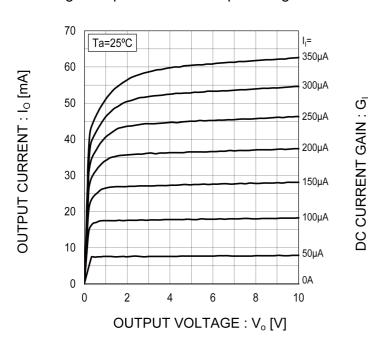
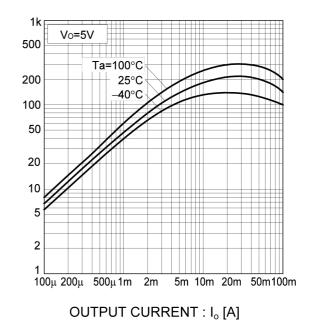


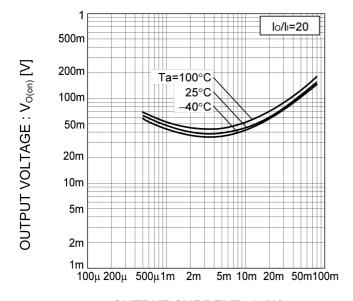
Fig.4 DC current gain vs. output current



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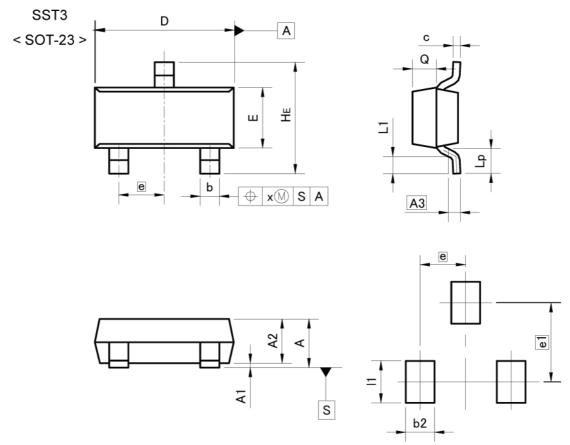
# ●Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT :  $I_o$  [A]

## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | TERS INCH     |       |  |
|-----|-------|-------|---------------|-------|--|
| DIM | MIN   | MAX   | MIN           | MAX   |  |
| Α   | 0.90  | 1.20  | 0.035         | 0.047 |  |
| A1  | 0.00  | 0.10  | 0.000         | 0.004 |  |
| A2  | 0.85  | 1.15  | 0.033         | 0.045 |  |
| A3  | 0.3   | 25    | 0.0           | 10    |  |
| b   | 0.35  | 0.50  | 0.014         | 0.020 |  |
| С   | 0.09  | 0.25  | 0.004         | 0.010 |  |
| D   | 2.70  | 3.10  | 0.106         | 0.122 |  |
| E   | 1.20  | 1.50  | 0.047         | 0.059 |  |
| е   | 0.9   | 95    | 0.0           | 37    |  |
| HE  | 2.20  | 2.60  | 0.087         | 0.102 |  |
| L1  | 0.20  | 00    | 0.008         | _     |  |
| Lp  | 0.30  | 2,-3  | 0.012         | -     |  |
| Q   | 0.40  | 0.60  | 0.016         | 0.024 |  |
| х   | - ,,  | 0.10  | e <del></del> | 0.004 |  |

| DIM  | MILIMETERS |        | INCHES |     |       |  |
|------|------------|--------|--------|-----|-------|--|
| DIM  |            | MIN    | MAX    | MIN | MAX   |  |
| b2   |            | -      | 0.60   | _   | 0.024 |  |
| e1   |            | 1.     | 70     | 0.0 | 67    |  |
| - 11 |            | - 0.90 |        | -   | 0.035 |  |

Dimension in mm/inches



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|----------|---------|------------|--------|
| CLASSIII | OL ACOM | CLASS II b | ОГУСОШ |
| CLASSIV  | CLASSⅢ  | CLASSIII   | CLASSⅢ |

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
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For details, please refer to ROHM Mounting specification

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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