

## Product Summary

V <sub>DSS</sub>	R <sub>DS(on)</sub>	Q <sub>g</sub>	Q <sub>gd</sub>	I <sub>D</sub>
12V	18mΩ	3.2nC	0.3nC	4.8A

Typ. @ V<sub>GS</sub> = 4.5V, T<sub>A</sub> = +25°C

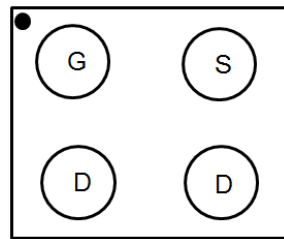
## Description

This 2<sup>nd</sup> generation Lateral MOSFET (LD-MOS) is engineered to minimize on-state losses and switch ultra-fast, making it ideal for high efficiency power transfer. It uses Chip-Scale Package (CSP) to increase power density by combining low thermal impedance with minimal R<sub>DS(on)</sub> per footprint area.

## Applications

- DC-DC Converters
- Battery Management
- Load Switch

U-WLB1010-4



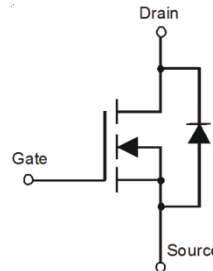
Top View

## Features

- LD-MOS technology with the lowest Figure of Merit:  
R<sub>DS(on)</sub> = 18mΩ to minimize on-state losses  
Q<sub>g</sub> = 3.2nC for ultra-fast switching
- V<sub>gs(th)</sub> = 0.8V typ. for a low turn-on potential
- CSP with Footprint 1.0mm × 1.0mm
- Height = 0.62mm for Low Profile
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: U-WLB1010-4
- Terminal Connections: See Diagram Below



Equivalent Circuit

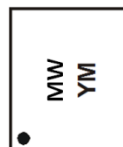
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1032UCB4-7	U-WLB1010-4	3,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information

U-WLB1010-4



MW = Product Type Marking Code  
YM = Date Code Marking  
Y or  $\bar{Y}$  = Year (ex: B = 2014)  
M or  $\bar{M}$  = Month (ex: 9 = September)

### Date Code Key

Year Code	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025
	B	C	D	E	F	G	H	I	J	K	L	M

Month Code	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	12	V
Gate-Source Voltage			V <sub>GSS</sub>	±8	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	Steady State	T <sub>A</sub> = +25°C	I <sub>D</sub>	4.8	A
		T <sub>A</sub> = +70°C		3.8	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 2.5V	Steady State	T <sub>A</sub> = +25°C	I <sub>D</sub>	4.5	A
		T <sub>A</sub> = +70°C		3.6	
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	15	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P <sub>D</sub>	0.9	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 7)	R <sub>θJA</sub>	138.81	°C/W
Thermal Resistance, Junction to Case @T <sub>C</sub> = +25°C (Note 7)	R <sub>θJC</sub>	31.77	°C/W
Power Dissipation (Note 5)	P <sub>D</sub>	1.16	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 5)	R <sub>θJA</sub>	107.59	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	12	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	1.0	μA	V <sub>DS</sub> = 9.6V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.4	0.8	1.2	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	18	26	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1A
		—	21	29		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1A
		—	27	38		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 1A
Forward Transfer Admittance	Y <sub>fs</sub>	—	8.1	—	S	V <sub>DS</sub> = 6V, I <sub>D</sub> = 1A
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
Reverse Recovery Charge	Q <sub>rr</sub>	—	1.2	—	nC	V <sub>dd</sub> = 5V, I <sub>F</sub> = 1A,
Reverse Recovery Time	t <sub>rr</sub>	—	10.5	—	ns	di/dt = 100A/μs
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>iss</sub>	—	325	450	pF	V <sub>DS</sub> = 6V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	183	250		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	31	47		
Series Gate Resistance	R <sub>G</sub>	—	3.1	—	Ω	f=1MHz, V <sub>gs</sub> =0V, V <sub>ds</sub> =0V
Total Gate Charge	Q <sub>g</sub>	—	3.2	4.5	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 6V, I <sub>D</sub> = 1A
Gate-Source Charge	Q <sub>gs</sub>	—	0.4	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	0.3	—		
Gate Charge at V <sub>th</sub>	Q <sub>g(th)</sub>	—	0.2	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	3.3	10	ns	V <sub>DS</sub> = 6V, V <sub>GS</sub> = 4.5V, R <sub>G</sub> = 20Ω, I <sub>D</sub> = 1A
Turn-On Rise Time	t <sub>r</sub>	—	5.6	—		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	24	36		
Turn-Off Fall Time	t <sub>f</sub>	—	9	—		

- Notes:
- Device mounted on FR4 material with 1-inch<sup>2</sup> (6.45-cm<sup>2</sup>), 2-oz. (0.071-mm thick) Cu.
  - Repetitive rating, pulse width limited by junction temperature.
  - Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

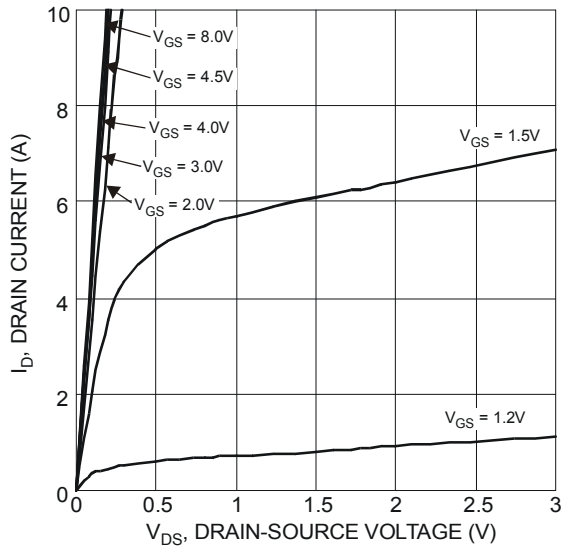


Figure 1 Typical Output Characteristics

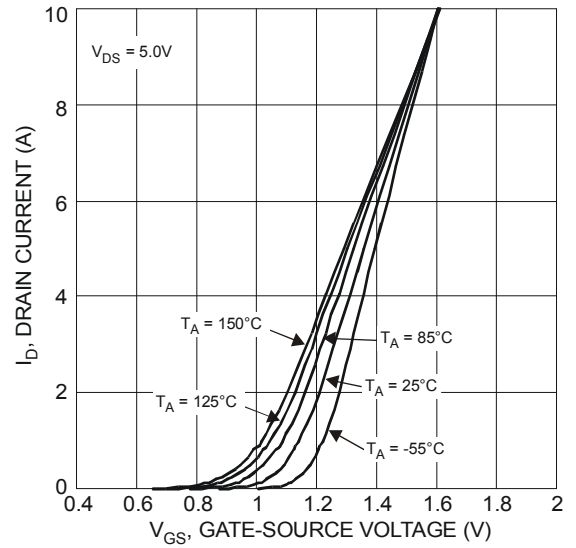


Figure 2 Typical Transfer Characteristics

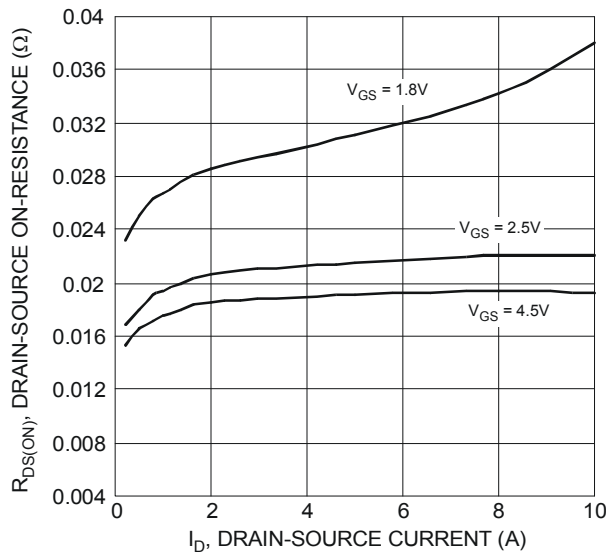


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

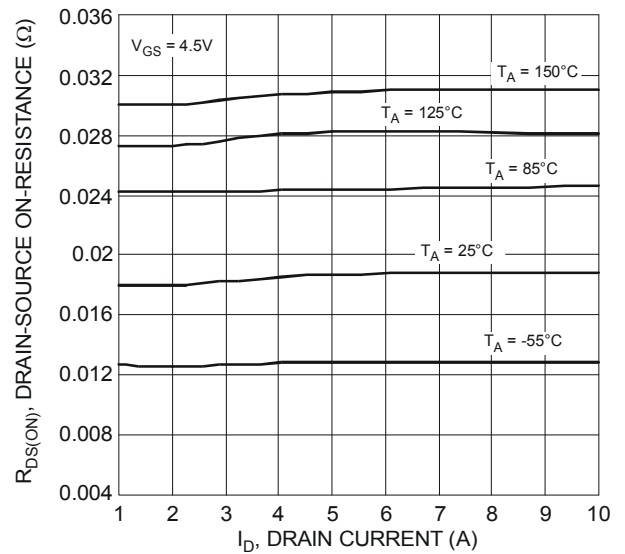


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

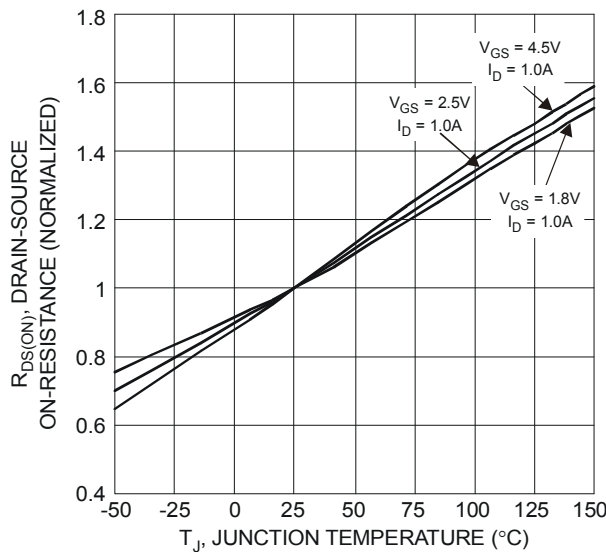


Figure 5 On-Resistance Variation with Temperature

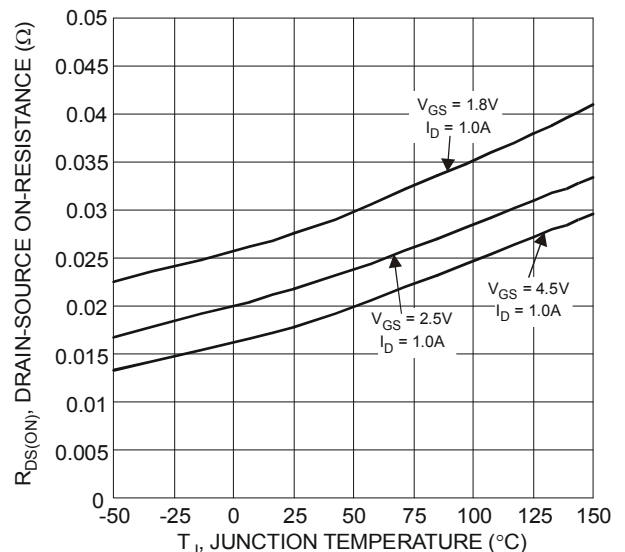


Figure 6 On-Resistance Variation with Temperature

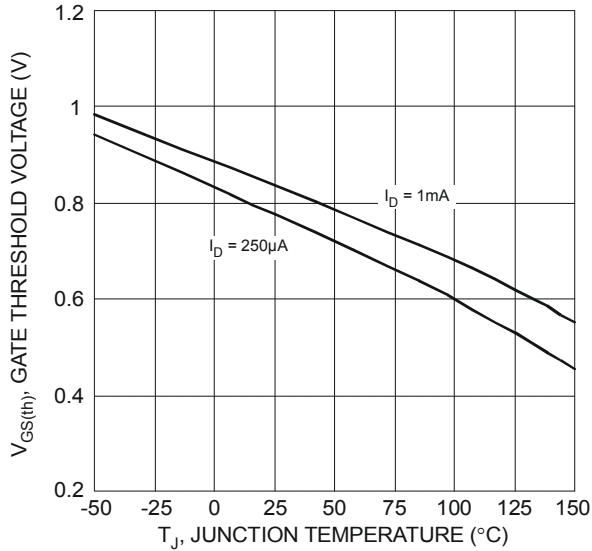


Figure 7 Gate Threshold Variation vs. Ambient Temperature

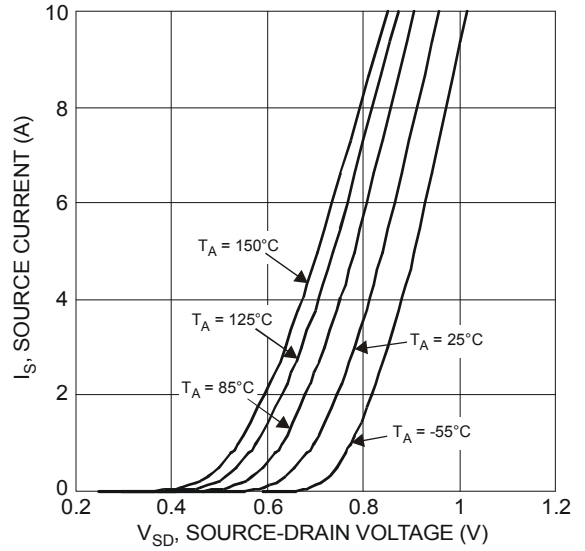


Figure 8 Diode Forward Voltage vs. Current

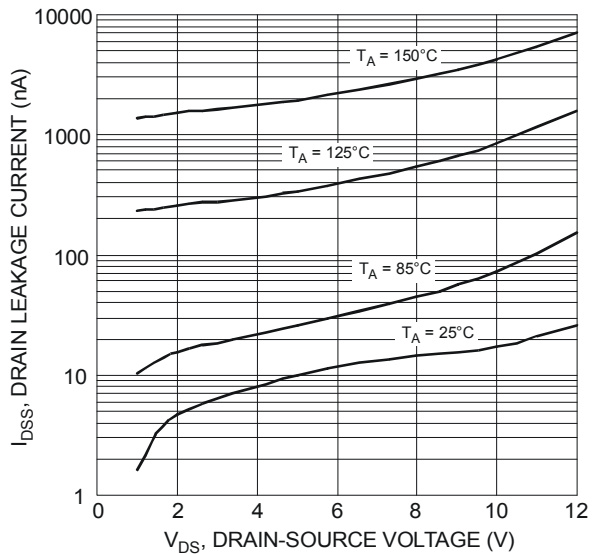


Figure 9 Typical Drain-Source Leakage Current vs. Voltage

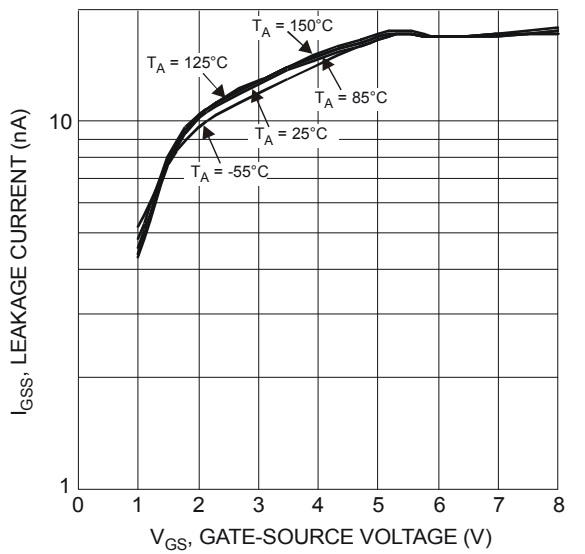


Figure 10 Gate-Source Leakage Current vs. Voltage

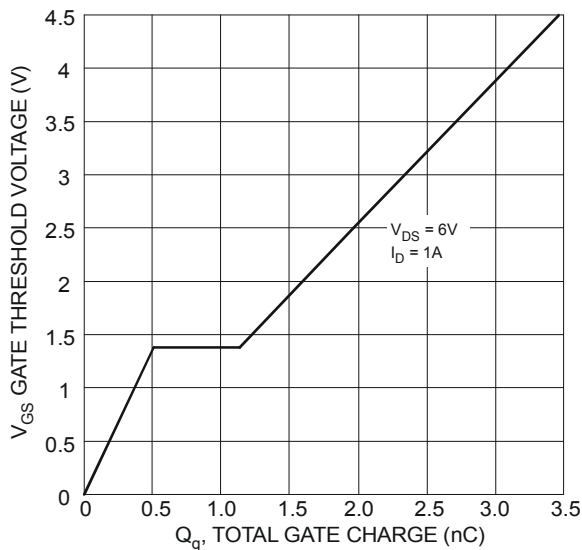


Figure 11 Gate Charge

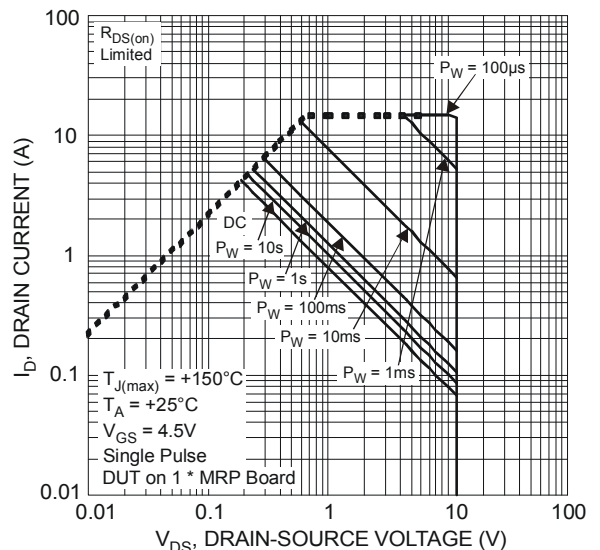


Figure 12 SOA, Safe Operation Area

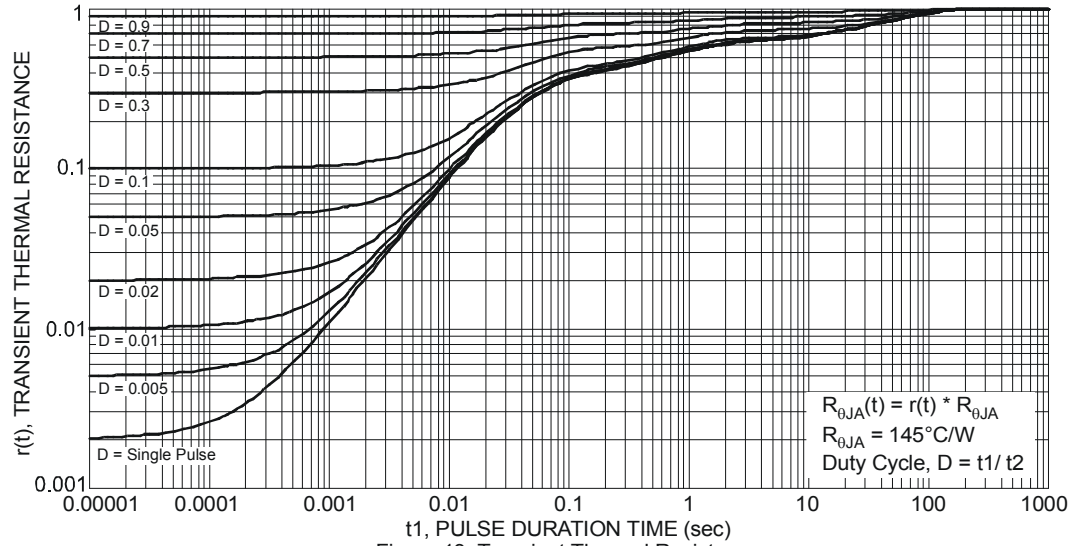
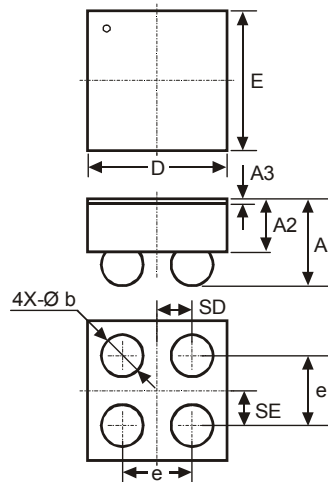


Figure 13 Transient Thermal Resistance

### Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

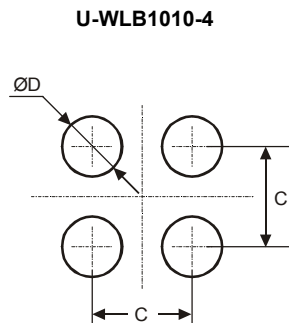


U-WLB1010-4			
Dim	Min	Max	Typ
D	0.95	1.05	1.00
E	0.95	1.05	1.00
A	-	0.62	-
A2	-	-	0.38
A3	0.015	0.025	0.025
b	0.25	0.35	0.30
e	-	-	0.50
SD	-	-	0.25
SE	-	-	0.25

All Dimensions in mm

### Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.50
D	0.25

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