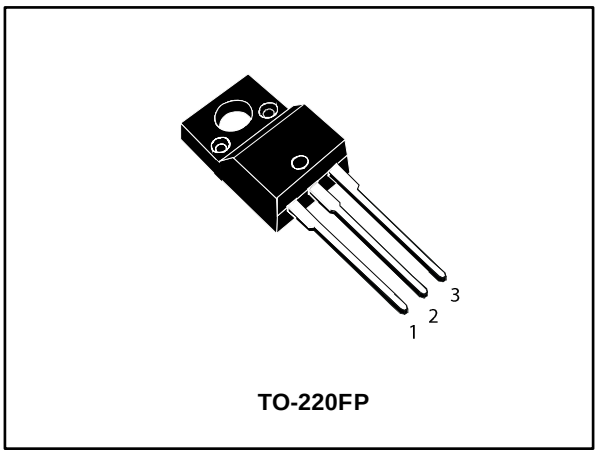
# STF10N60M2

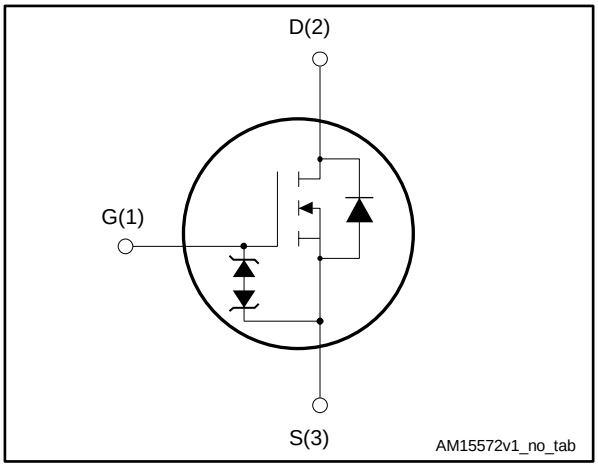
## N-channel 600 V, 0.55 Ω typ., 7.5 A MDmesh™ M2 Power MOSFET in a TO-220FP package

Datasheet - production data




**TO-220FP**

**Figure 1: Internal schematic diagram**




## Features

| Order code | $V_{DS}@T_{Jmax.}$ | $R_{DS(on)}$ max. | $I_D$ |
|---|---|---|---|
| STF10N60M2 | 650 V | 0.60 Ω | 7.5 A |

- Extremely low gate charge
- Excellent output capacitance ($C_{OSS}$) profile
- 100% avalanche tested
- Zener-protected

## Applications

- Switching applications

## Description

This device is an N-channel Power MOSFET developed using MDmesh™ M2 technology. Thanks to its strip layout and an improved vertical structure, the device exhibits low on-resistance and optimized switching characteristics, rendering it suitable for the most demanding high efficiency converters.

**Table 1: Device summary**

| Order code | Marking | Package | Packing |
|---|---|---|---|
| STF10N60M2 | 10N60M2 | TO-220FP | Tube |

# Contents

# 1 Electrical ratings

**Table 2: Absolute maximum ratings**

| Symbol | Parameter | Value | Unit |
|---|---|---|---|
| $V_{GS}$ | Gate-source voltage | ±25 | V |
| $I_D$(1) | Drain current (continuous) at $T_{case}$ = 25 °C | 7.5 | A |
| | Drain current (continuous) at $T_{case}$ = 100 °C | 4.9 | |
| $I_{DM}$(2) | Drain current (pulsed) | 30 | A |
| $P_{TOT}$ | Total dissipation at $T_{case}$ = 25 °C | 25 | W |
| dv/dt(3) | Peak diode recovery voltage slope | 15 | V/ns |
| dv/dt(4) | MOSFET dv/dt ruggedness | 50 | |
| $V_{ISO}$(5) | Insulation withstand voltage (RMS) from all three leads to external heat sink | 2500 | V |
| $T_{stg}$ | Storage temperature range | -55 to 150 | °C |
| $T_j$ | Operating junction temperature range | | |

**Notes:**

(1)Limited by package.

(2)Pulse limited by safe operating area.

(3) $I_{SD}$ ≤ 7.5 A, di/dt ≤ 400 A/µs; $V_{DS}$ peak < $V_{(BR)DSS}$, $V_{DD}$ = 400 V

(4) $V_{DS}$ ≤ 480 V.

(5)t = 1 s; $T_C$ = 25 °C.

**Table 3: Thermal data**

| Symbol | Parameter | Value | Unit |
|---|---|---|---|
| $R_{thj\text{-}case}$ | Thermal resistance junction-case | 5 | °C/W |
| $R_{thj\text{-}amb}$ | Thermal resistance junction-ambient | 62.5 | |

**Table 4: Avalanche characteristics**

| Symbol | Parameter | Value | Unit |
|---|---|---|---|
| $I_{AR}$(1) | Avalanche current, repetitive or not repetitive | 1.5 | A |
| $E_{AS}$(2) | Single pulse avalanche energy | 110 | mJ |

**Notes:**

(1) Pulse width limited by $T_{jmax}$.

(2) Starting $T_j$ = 25 °C, $I_D$ = $I_{AR}$, $V_{DD}$ = 50 V.

# 2 Electrical characteristics

($T_{case}$ = 25 °C unless otherwise specified)

**Table 5: Static**

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|---|
| $V_{(BR)DSS}$ | Drain-source breakdown voltage | $V_{GS}$ = 0 V, $I_D$ = 1 mA | 600 | | | V |
| $I_{DSS}$ | Zero gate voltage drain current | $V_{GS}$ = 0 V, $V_{DS}$ = 600 V | | | 1 | µA |
| | | $V_{GS}$ = 0 V, $V_{DS}$ = 600 V, $T_{case}$ = 125 °C(1) | | | 100 | |
| $I_{GSS}$ | Gate-body leakage current | $V_{DS}$ = 0 V, $V_{GS}$ = ±25 V | | | ±10 | µA |
| $V_{GS(th)}$ | Gate threshold voltage | $V_{DS}$ = $V_{GS}$, $I_D$ = 250 µA | 2 | 3 | 4 | V |
| $R_{DS(on)}$ | Static drain-source on-resistance | $V_{GS}$ = 10 V, $I_D$ = 3 A | | 0.55 | 0.60 | Ω |

**Notes:**

(1)Defined by design, not subject to production test.

**Table 6: Dynamic**

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|---|
| $C_{iss}$ | Input capacitance | $V_{DS}$ = 100 V, f = 1 MHz, $V_{GS}$ = 0 V | - | 400 | - | pF |
| $C_{oss}$ | Output capacitance | | - | 22 | - | |
| $C_{rss}$ | Reverse transfer capacitance | | - | 0.84 | - | |
| $C_{oss\ eq.}$(1) | Equivalent output capacitance | $V_{DS}$ = 0 to 480 V, $V_{GS}$ = 0 V | - | 83 | - | pF |
| $R_G$ | Intrinsic gate resistance | f = 1 MHz, $I_D$ = 0 A | - | 6.4 | - | Ω |
| $Q_g$ | Total gate charge | $V_{DD}$ = 480 V, $I_D$ = 7.5 A, $V_{GS}$ = 0 to 10 V (see *Figure 15: "Test circuit for gate charge behavior"*) | - | 13.5 | - | nC |
| $Q_{gs}$ | Gate-source charge | | - | 2.1 | - | |
| $Q_{gd}$ | Gate-drain charge | | - | 7.2 | - | |

**Notes:**

(1) $C_{oss\ eq.}$ is defined as a constant equivalent capacitance giving the same charging time as $C_{oss}$ when $V_{DS}$ increases from 0 to 80% $V_{DSS}$.

**Table 7: Switching times**

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|---|
| $t_{d(on)}$ | Turn-on delay time | $V_{DD}$ = 300 V, $I_D$ = 3.75 A, $R_G$ = 4.7 Ω, $V_{GS}$ = 10 V (see *Figure 14: "Test circuit for resistive load switching times"* and *Figure 19: "Switching time waveform"*) | - | 8.8 | - | ns |
| $t_r$ | Rise time | | - | 8 | - | |
| $t_{d(off)}$ | Turn-off delay time | | - | 32.5 | - | |
| $t_f$ | Fall time | | - | 13.2 | - | |

**Table 8: Source-drain diode**

| Symbol | Parameter | Test conditions | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|---|
| $I_{SD}$ (1) | Source-drain current | | - | | 7.5 | A |
| $I_{SDM}$ (2) | Source-drain current (pulsed) | | - | | 30 | A |
| $V_{SD}$ (3) | Forward on voltage | $V_{GS}$ = 0 V, $I_{SD}$ = 7.5 A | - | | 1.6 | V |
| $t_{rr}$ | Reverse recovery time | $I_{SD}$ = 7.5 A, di/dt = 100 A/µs, $V_{DD}$ = 60 V (see *Figure 16: "Test circuit for inductive load switching and diode recovery times"*) | - | 270 | | ns |
| $Q_{rr}$ | Reverse recovery charge | | - | 2 | | µC |
| $I_{RRM}$ | Reverse recovery current | | - | 14.4 | | A |
| $t_{rr}$ | Reverse recovery time | $I_{SD}$ = 7.5 A, di/dt = 100 A/µs, $V_{DD}$ = 60 V, $T_j$ = 150 °C (see *Figure 16: "Test circuit for inductive load switching and diode recovery times"*) | - | 376 | | ns |
| $Q_{rr}$ | Reverse recovery charge | | - | 2.8 | | µC |
| $I_{RRM}$ | Reverse recovery current | | - | 15 | | A |

**Notes:**

(1) Limited by package.

(2) Pulse width is limited by safe operating area.

(3) Pulse test: pulse duration = 300 µs, duty cycle 1.5%.

## 2.2 Electrical characteristics (curves)

**Figure 2: Safe operating area**

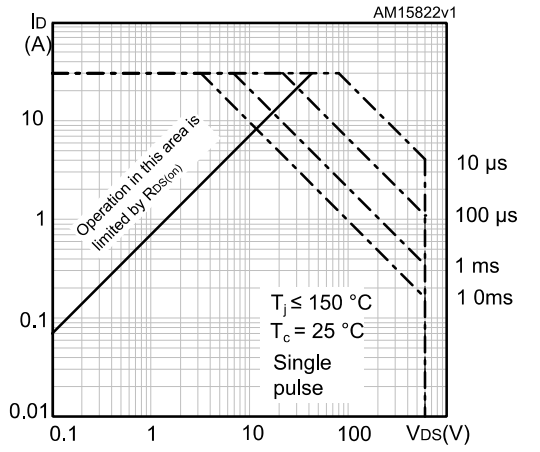



**Figure 3: Thermal impedance**

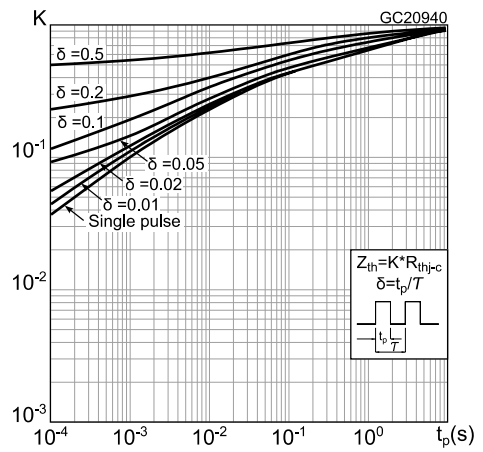



**Figure 4: Output characteristics**

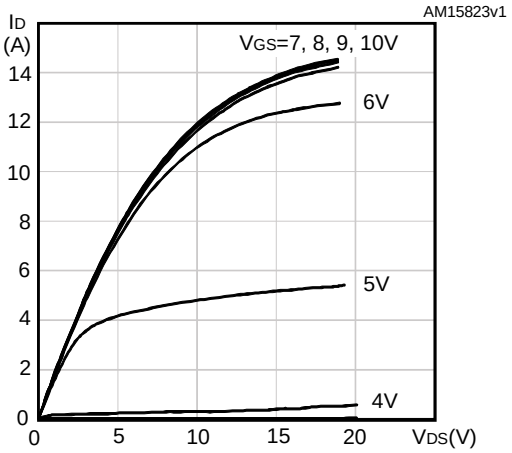



**Figure 5: Transfer characteristics**

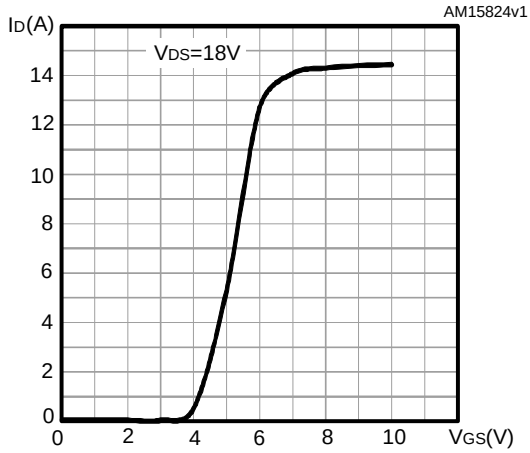



**Figure 6: Gate charge vs gate-source voltage**

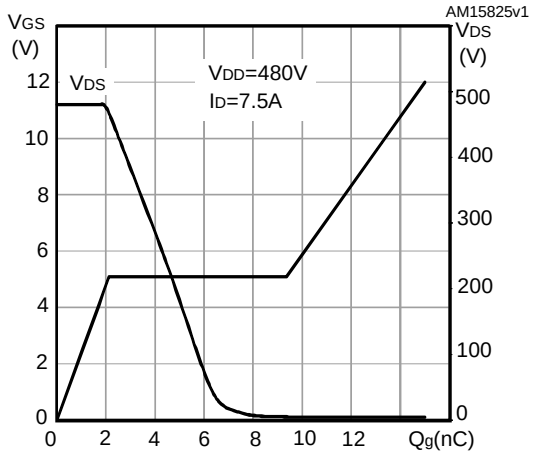



**Figure 7: Static drain-source on-resistance**

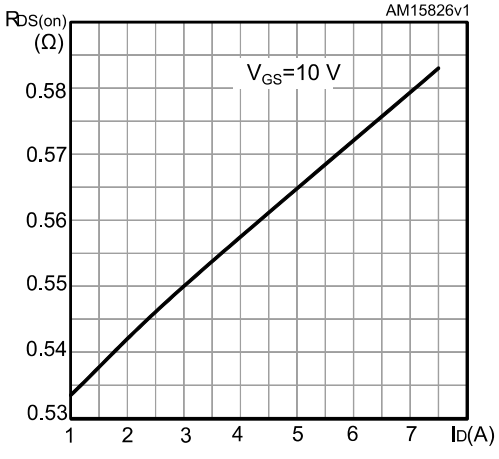


**Figure 8: Capacitance variations**

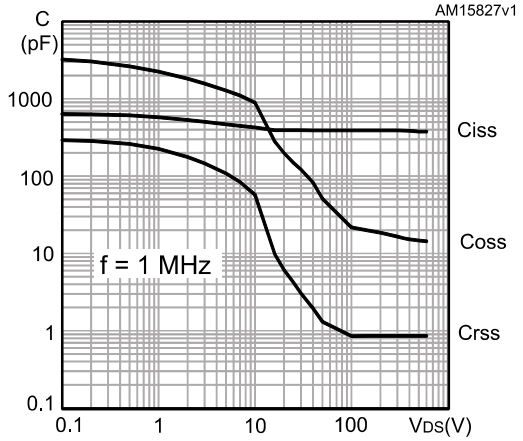



**Figure 9: Normalized gate threshold voltage vs temperature**

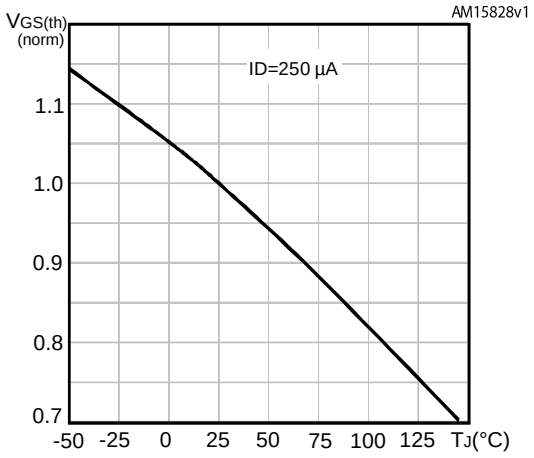



**Figure 10: Normalized on-resistance vs temperature**

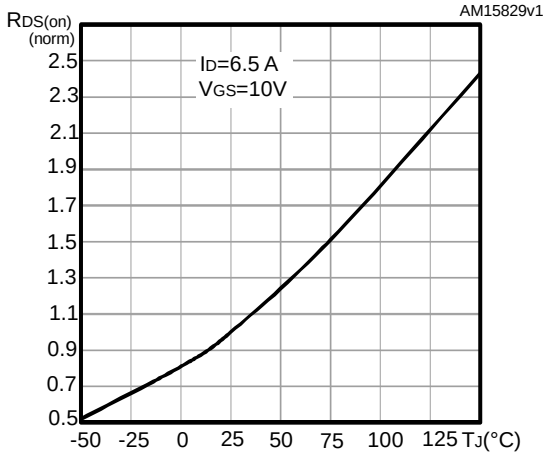



**Figure 11: Normalized $V_{(BR)DSS}$ vs temperature**

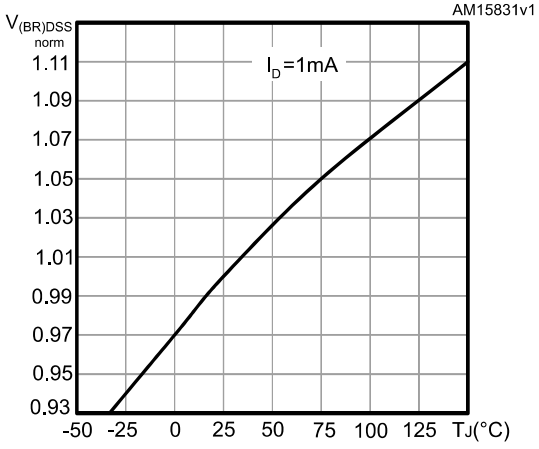



**Figure 12: Source-drain diode forward characteristics**

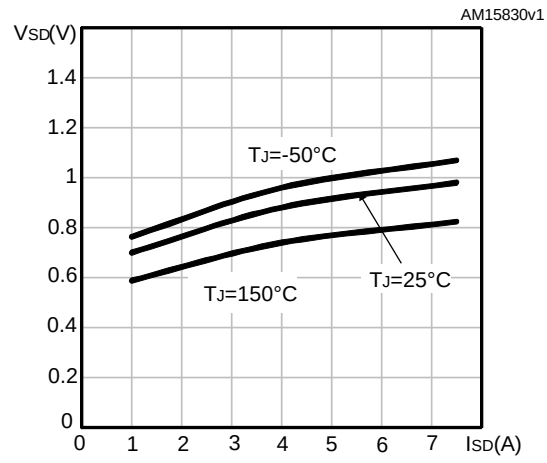
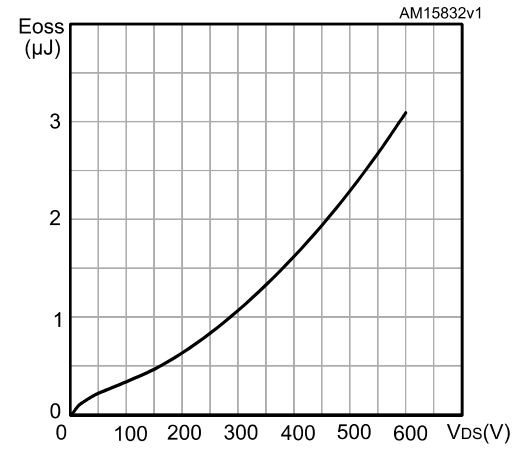



**Figure 13: Output capacitance stored energy**



# 3 Test circuits

**Figure 14: Test circuit for resistive load switching times**

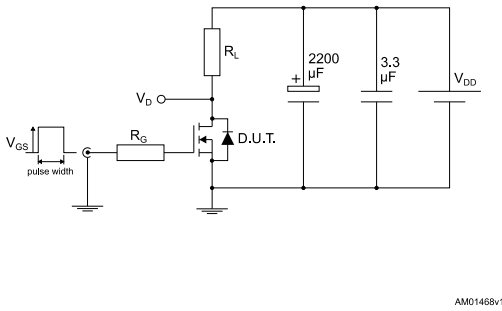



**Figure 15: Test circuit for gate charge behavior**

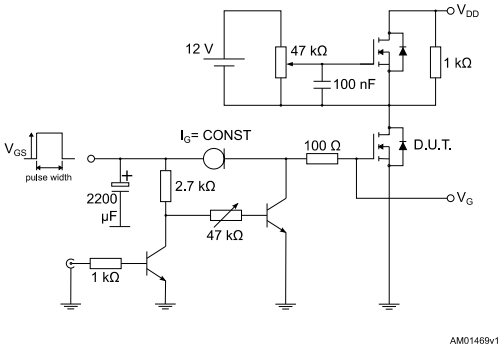



**Figure 16: Test circuit for inductive load switching and diode recovery times**

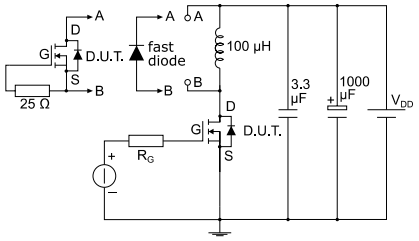



**Figure 17: Unclamped inductive load test circuit**

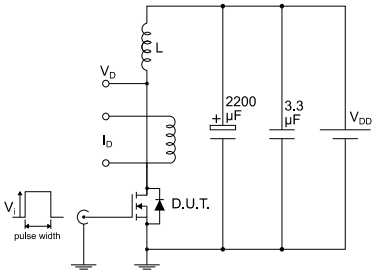



**Figure 18: Unclamped inductive waveform**

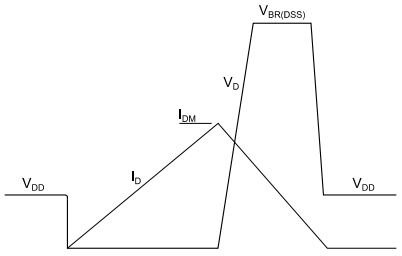
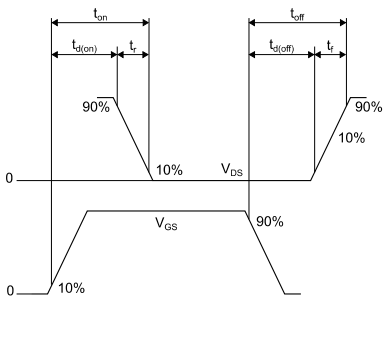



**Figure 19: Switching time waveform**



# 4 Package information

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## 4.1 TO-220FP package information

Figure 20: TO-220FP package outline

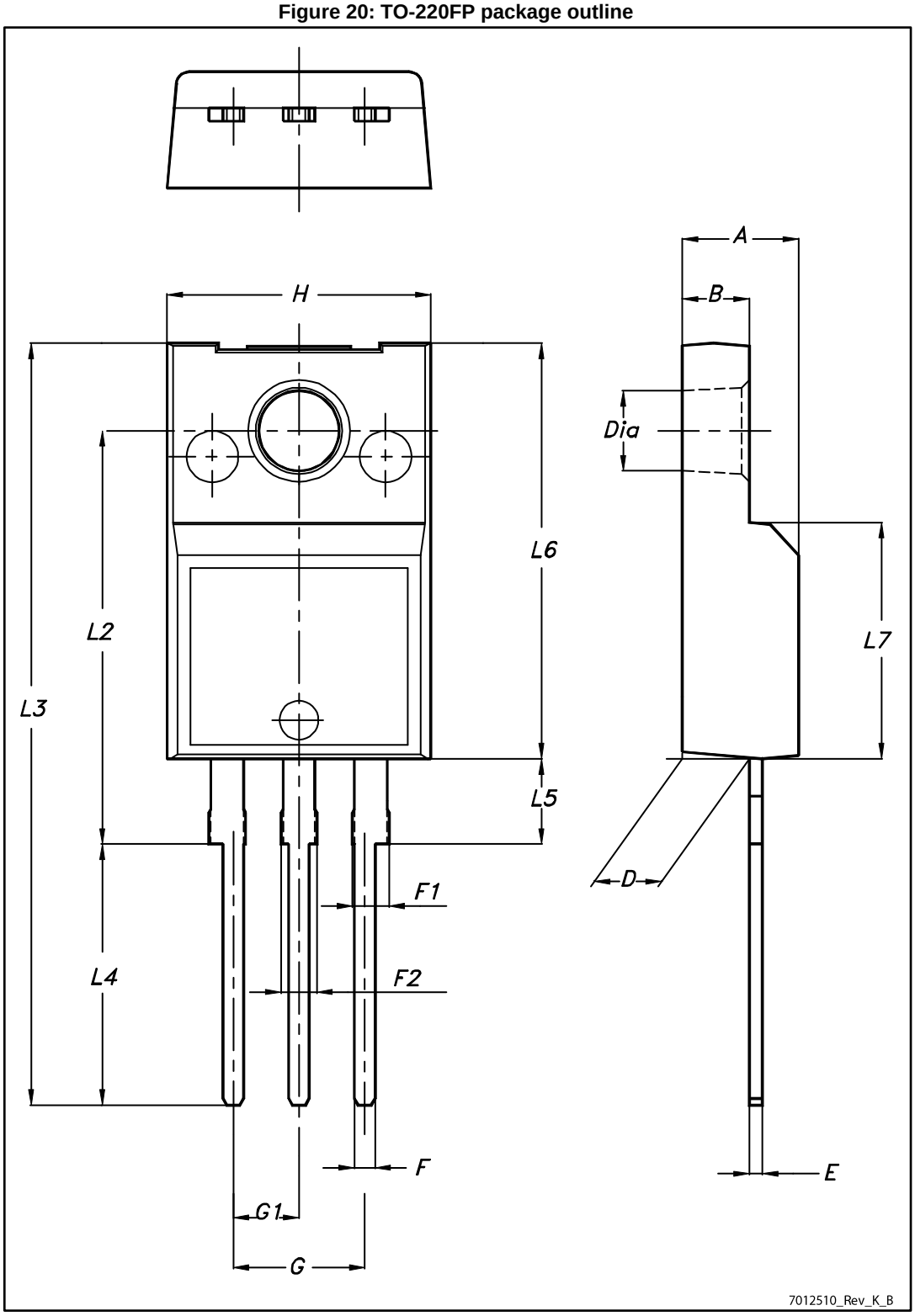

Table 9: TO-220FP package mechanical data

| Dim. | mm | | |
|---|---|---|---|
| | Min. | Typ. | Max. |
| A | 4.4 | | 4.6 |
| B | 2.5 | | 2.7 |
| D | 2.5 | | 2.75 |
| E | 0.45 | | 0.7 |
| F | 0.75 | | 1 |
| F1 | 1.15 | | 1.70 |
| F2 | 1.15 | | 1.70 |
| G | 4.95 | | 5.2 |
| G1 | 2.4 | | 2.7 |
| H | 10 | | 10.4 |
| L2 | | 16 | |
| L3 | 28.6 | | 30.6 |
| L4 | 9.8 | | 10.6 |
| L5 | 2.9 | | 3.6 |
| L6 | 15.9 | | 16.4 |
| L7 | 9 | | 9.3 |
| Dia | 3 | | 3.2 |

# 5 Revision history

**Table 10: Document revision history**

| Date | Revision | Changes |
|---|---|---|
| 29-May-2013 | 1 | First release. |
| 14-Oct-2013 | 2 | Modified: $R_G$ value in *Table 6*<br>Minor text changes |
| 06-Dec-2013 | 3 | Added: $I^2PAKFP$ package<br>– Modified: title<br>– Modified: $R_{DS(on)}$ typical values in *Table 5*<br>– Modified: $R_G$ value in *Table 6*<br>– Modified: *Figure 7* and $I_D$ value in *Figure 10*<br>– Added: *Table 10*, and *Figure 21*<br>– Minor text changes |
| 09-Mar-2017 | 4 | The device in $I^2PAKFP$ has been removed and this document has been updated accordingly.<br>Updated the title and the description in cover page.<br>Updated *Table 4: "Avalanche characteristics"*.<br>Minor text changes. |

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- Консультации по применению компонента;
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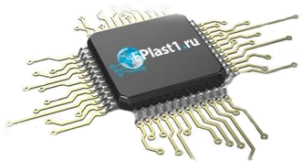
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.

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