

CD4051BM/CD4051BC Single 8-Channel Analog Multiplexer/Demultiplexer **CD4052BM/CD4052BC Dual 4-Channel Analog Multiplexer/Demultiplexer** **CD4053BM/CD4053BC Triple 2-Channel Analog Multiplexer/Demultiplexer**

General Description

These analog multiplexers/demultiplexers are digitally controlled analog switches having low "ON" impedance and very low "OFF" leakage currents. Control of analog signals up to $15V_{p-p}$ can be achieved by digital signal amplitudes of 3–15V. For example, if $V_{DD}=5V$, $V_{SS}=0V$ and $V_{EE}=-5V$, analog signals from $-5V$ to $+5V$ can be controlled by digital inputs of 0–5V. The multiplexer circuits dissipate extremely low quiescent power over the full $V_{DD}-V_{SS}$ and $V_{DD}-V_{EE}$ supply voltage ranges, independent of the logic state of the control signals. When a logical "1" is present at the inhibit input terminal all channels are "OFF".

CD4051BM/CD4051BC is a single 8-channel multiplexer having three binary control inputs, A, B, and C, and an inhibit input. The three binary signals select 1 of 8 channels to be turned "ON" and connect the input to the output.

CD4052BM/CD4052BC is a differential 4-channel multiplexer having two binary control inputs, A and B, and an inhibit input. The two binary input signals select 1 or 4 pairs of channels to be turned on and connect the differential analog inputs to the differential outputs.

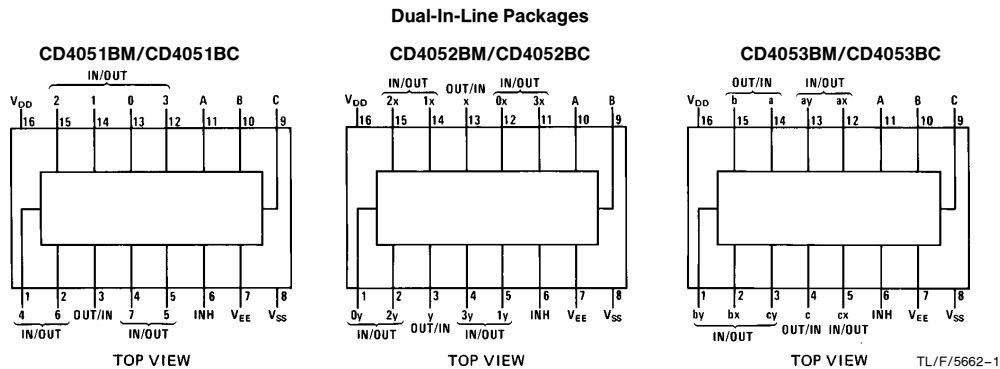
CD4053BM/CD4053BC is a triple 2-channel multiplexer having three separate digital control inputs, A, B, and C, and

an inhibit input. Each control input selects one of a pair of channels which are connected in a single-pole double-throw configuration.

Features

- Wide range of digital and analog signal levels: digital 3–15V, analog to $15V_{p-p}$
- Low "ON" resistance: 80Ω (typ.) over entire $15V_{p-p}$ signal-input range for $V_{DD}-V_{EE}=15V$
- High "OFF" resistance: channel leakage of ± 10 pA (typ.) at $V_{DD}-V_{EE}=10V$
- Logic level conversion for digital addressing signals of 3–15V ($V_{DD}-V_{SS}=3-15V$) to switch analog signals to $15V_{p-p}$ ($V_{DD}-V_{EE}=15V$)
- Matched switch characteristics: $\Delta R_{ON}=5\Omega$ (typ.) for $V_{DD}-V_{EE}=15V$
- Very low quiescent power dissipation under all digital-control input and supply conditions: $1\mu W$ (typ.) at $V_{DD}-V_{SS}=V_{DD}-V_{EE}=10V$
- Binary address decoding on chip

Connection Diagrams



Order Number CD4051B, CD4052B, or CD4053B

CD4051BM/CD4051BC, CD4052BM/CD4052BC, CD4053BM/CD4053BC
Analog Multiplexer/Demultiplexers

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

DC Supply Voltage (V_{DD})	$-0.5 V_{DC}$ to $+18 V_{DC}$
Input Voltage (V_{IN})	$-0.5 V_{DC}$ to $V_{DD} + 0.5 V_{DC}$
Storage Temperature Range (T_S)	-65°C to $+150^{\circ}\text{C}$
Power Dissipation (P_D)	
Dual-In-Line	700 mW
Small Outline	500 mW
Lead Temp. (T_L) (soldering, 10 sec.)	260°C

Recommended Operating Conditions

DC Supply Voltage (V_{DD})	$+5 V_{DC}$ to $+15 V_{DC}$
Input Voltage (V_{IN})	0V to $V_{DD} V_{DC}$
Operating Temperature Range (T_A)	
4051BM/4052BM/4053BM	-55°C to $+125^{\circ}\text{C}$
4051BC/4052BC/4053BC	-40°C to $+85^{\circ}\text{C}$

DC Electrical Characteristics (Note 2)

Symbol	Parameter	Conditions		− 55°C		+ 25°			+ 125°C		Units
				Min	Max	Min	Typ	Max	Min	Max	
I _{DD}	Quiescent Device Current	V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V			5 10 20			5 10 20		150 300 600	μA μA μA
Signal Inputs (V _{IS}) and Outputs (V _{OS})											
R _{ON}	“ON” Resistance (Peak for V _{EE} ≤ V _{IS} ≤ V _{DD})	R _L = 10 kΩ (any channel selected)	V _{DD} = 2.5V, V _{EE} = −2.5V or V _{DD} = 5V, V _{EE} = 0V		800		270	1050		1300	Ω
			V _{DD} = 5V V _{EE} = −5V or V _{DD} = 10V, V _{EE} = 0V		310		120	400		550	Ω
			V _{DD} = 7.5V, V _{EE} = −7.5V or V _{DD} = 15V, V _{EE} = 0V		200		80	240		320	Ω
ΔR _{ON}	Δ“ON” Resistance Between Any Two Channels	R _L = 10 kΩ (any channel selected)	V _{DD} = 2.5V, V _{EE} = −2.5V or V _{DD} = 5V, V _{EE} = 0V				10				Ω
			V _{DD} = 5V, V _{EE} = −5V or V _{DD} = 10V, V _{EE} = 0V				10				Ω
			V _{DD} = 7.5V, V _{EE} = −7.5V or V _{DD} = 15V, V _{EE} = 0V				5				Ω
	“OFF” Channel Leakage Current, any channel “OFF”	V _{DD} = 7.5V, V _{EE} = −7.5V O/I = ± 7.5V, I/O = 0V		± 50		± 0.01	± 50		± 500	nA	
	“OFF” Channel Leakage Current, all channels “OFF” (Common OUT/IN)	Inhibit = 7.5V	CD4051	± 200		± 0.08	± 200		± 2000	nA	
		V _{DD} = 7.5V, V _{EE} = −7.5V, O/I = 0V,	CD4052	± 200		± 0.04	± 200		± 2000	nA	
I/O = ± 7.5V		CD4053	± 200		± 0.02	± 200		± 2000	nA		
Control Inputs A, B, C and Inhibit											
V _{IL}	Low Level Input Voltage	V _{EE} = V _{SS} R _L = 1 kΩ to V _{SS} I _{IS} < 2 μA on all OFF channels V _{IS} = V _{DD} thru 1 kΩ V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V			1.5 3.0 4.0			1.5 3.0 4.0		1.5 3.0 4.0	V V V
V _{IH}	High Level Input Voltage	V _{DD} = 5 V _{DD} = 10 V _{DD} = 15		3.5 7 11		3.5 7 11			3.5 7 11		V V V

Note 1: “Absolute Maximum Ratings” are those values beyond which the safety of the device cannot be guaranteed. Except for “Operating Temperature Range” they are not meant to imply that the devices should be operated at these limits. The table of “Electrical Characteristics” provides conditions for actual device operation.

Note 2: All voltages measured with respect to V_{SS} unless otherwise specified.

DC Electrical Characteristics (Note 2) (Continued)

Symbol	Parameter	Conditions		−40°C		+25°C			+85°C		Units
				Min	Max	Min	Typ	Max	Min	Max	
I _{IN}	Input Current	V _{DD} = 15V, V _{IN} = 0V V _{DD} = 15V, V _{IN} = 15V	V _{EE} = 0V V _{EE} = 0V		−0.1 0.1		−10 ^{−5} 10 ^{−5}	−0.1 0.1		−1.0 1.0	μA μA
I _{DD}	Quiescent Device Current	V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V			20 40 80			20 40 80		150 300 600	μA μA μA
Signal Inputs (V _{IS}) and Outputs (V _{OS})											
R _{ON}	“ON” Resistance (Peak for V _{EE} ≤ V _{IS} ≤ V _{DD})	R _L = 10 kΩ (any channel selected)	V _{DD} = 2.5V, V _{EE} = −2.5V or V _{DD} = 5V, V _{EE} = 0V V _{DD} = 5V, V _{EE} = −5V or V _{DD} = 10V, V _{EE} = 0V V _{DD} = 7.5V, V _{EE} = −7.5V or V _{DD} = 15V, V _{EE} = 0V		850 330 210		270 120 80	1050 400 240		1200 520 300	Ω Ω Ω
ΔR _{ON}	Δ“ON” Resistance Between Any Two Channels	R _L = 10 kΩ (any channel selected)	V _{DD} = 2.5V, V _{EE} = −2.5V or V _{DD} = 5V, V _{EE} = 0V				10				Ω
			V _{DD} = 5V V _{EE} = −5V or V _{DD} = 10V, V _{EE} = 0V				10				Ω
			V _{DD} = 7.5V, V _{EE} = −7.5V or V _{DD} = 15V, V _{EE} = 0V				5				Ω
	“OFF” Channel Leakage Current, any channel “OFF”	V _{DD} = 7.5V, O/I = ±7.5V, I/O = 0V		±50		±0.01	±50		±500	nA	
	“OFF” Channel Leakage Current, all channels “OFF” (Common OUT/IN)	Inhibit = 7.5V V _{DD} = 7.5V, V _{EE} = −7.5V, O/I = 0V I/O = ±7.5V	CD4051 CD4052 CD4053	±200 ±200 ±200		±0.08 ±0.04 ±0.02	±200 ±200 ±200		±2000 ±2000 ±2000	nA nA nA	
Control Inputs A, B, C and Inhibit											
V _{IL}	Low Level Input Voltage	V _{EE} = V _{SS} R _L = 1 kΩ to V _{SS} I _{IS} < 2 μA on all OFF Channels V _{IS} = V _{DD} thru 1 kΩ V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V			1.5 3.0 4.0			1.5 3.0 4.0		1.5 3.0 4.0	V V V
V _{IH}	High Level Input Voltage	V _{DD} = 5 V _{DD} = 10 V _{DD} = 15		3.5 7 11		3.5 7 11			3.5 7 11		V V V
I _{IN}	Input Current	V _{DD} = 15V, V _{IN} = 0V V _{DD} = 15V, V _{IN} = 15V	V _{EE} = 0V V _{EE} = 0V		−0.1 0.1		−10 ^{−5} 10 ^{−5}	−0.1 0.1		−1.0 1.0	μA μA

Note 1: “Absolute Maximum Ratings” are those values beyond which the safety of the device cannot be guaranteed. Except for “Operating Temperature Range” they are not meant to imply that the devices should be operated at these limits. The table of “Electrical Characteristics” provides conditions for actual device operation.

Note 2: All voltages measured with respect to V_{SS} unless otherwise specified.

AC Electrical Characteristics* $T_A = 25^\circ\text{C}$, $t_r = t_f = 20\text{ ns}$, unless otherwise specified.

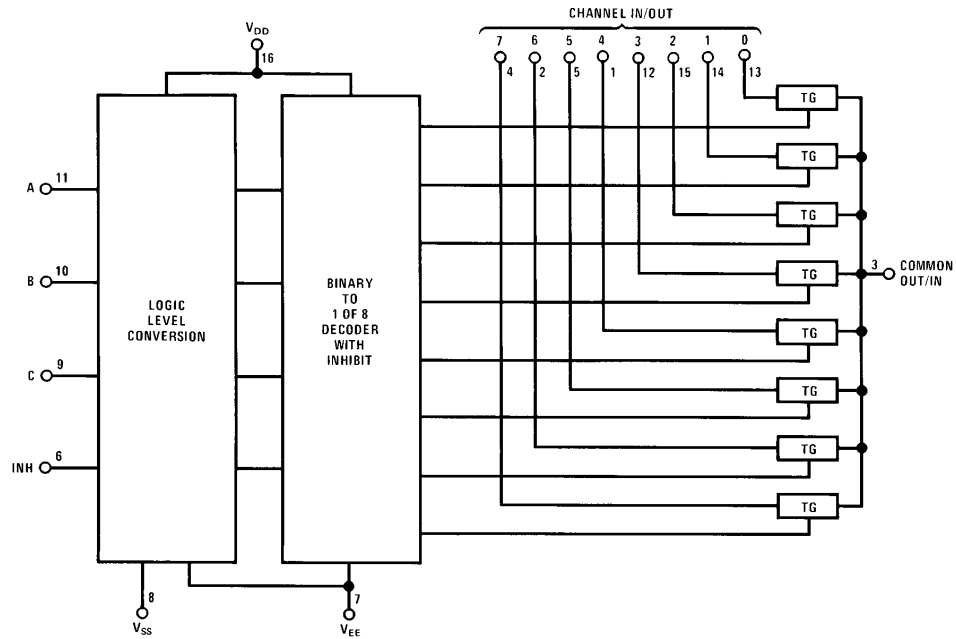
Symbol	Parameter	Conditions	V_{DD}	Min	Typ	Max	Units
t_{PZH} , t_{PZL}	Propagation Delay Time from Inhibit to Signal Output (channel turning on)	$V_{EE} = V_{SS} = 0V$ $R_L = 1\text{ k}\Omega$ $C_L = 50\text{ pF}$	5V 10V 15V		600 225 160	1200 450 320	ns ns ns
t_{PHZ} , t_{PLZ}	Propagation Delay Time from Inhibit to Signal Output (channel turning off)	$V_{EE} = V_{SS} = 0V$ $R_L = 1\text{ k}\Omega$ $C_L = 50\text{ pF}$	5V 10V 15V		210 100 75	420 200 150	ns ns ns
C_{IN}	Input Capacitance Control input Signal Input (IN/OUT)				5 10	7.5 15	pF pF
C_{OUT}	Output Capacitance (common OUT/IN)						
	CD4051 CD4052 CD4053	$V_{EE} = V_{SS} = 0V$	10V 10V 10V		30 15 8		pF pF pF
C_{IOS}	Feedthrough Capacitance				0.2		pF
C_{PD}	Power Dissipation Capacitance						
	CD4051 CD4052 CD4053				110 140 70		pF pF pF
Signal Inputs (V_{IS}) and Outputs (V_{OS})							
	Sine Wave Response (Distortion)	$R_L = 10\text{ k}\Omega$ $f_{IS} = 1\text{ kHz}$ $V_{IS} = 5\text{ V}_{p-p}$ $V_{EE} = V_{SI} = 0V$	10V		0.04		%
	Frequency Response, Channel "ON" (Sine Wave Input)	$R_L = 1\text{ k}\Omega$, $V_{EE} = 0V$, $V_{IS} = 5\text{ V}_{p-p}$, $20 \log_{10} V_{OS}/V_{IS} = -3\text{ dB}$	10V		40		MHz
	Feedthrough, Channel "OFF"	$R_L = 1\text{ k}\Omega$, $V_{EE} = V_{SS} = 0V$, $V_{IS} = 5\text{ V}_{p-p}$, $20 \log_{10} V_{OS}/V_{IS} = -40\text{ dB}$	10V		10		MHz
	Crosstalk Between Any Two Channels (frequency at 40 dB)	$R_L = 1\text{ k}\Omega$, $V_{EE} = V_{SS} = 0V$, $V_{IS}(A) = 5\text{ V}_{p-p}$, $20 \log_{10} V_{OS}(B)/V_{IS}(A) = -40\text{ dB}$ (Note 3)	10V		3		MHz
t_{PHL} , t_{PLH}	Propagation Delay Signal Input to Signal Output	$V_{EE} = V_{SS} = 0V$ $C_L = 50\text{ pF}$	5V 10V 15V		25 15 10	55 35 25	ns ns ns
Control Inputs, A, B, C and Inhibit							
	Control Input to Signal Crosstalk	$V_{EE} = V_{SS} = 0V$, $R_L = 10\text{ k}\Omega$ at both ends of channel. Input Square Wave Amplitude = 10V	10V		65		mV (peak)
t_{PHL} , t_{PLH}	Propagation Delay Time from Address to Signal Output (channels "ON" or "OFF")	$V_{EE} = V_{SS} = 0V$ $C_L = 50\text{ pF}$	5V 10V 15V		500 180 120	1000 360 240	ns ns ns

*AC Parameters are guaranteed by DC correlated testing.

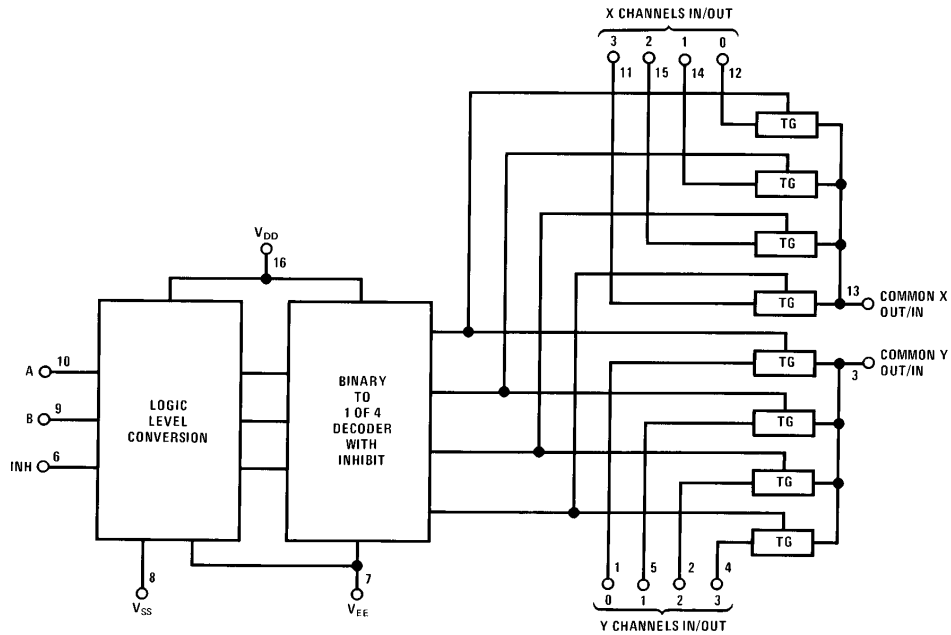
Note 3: A, B are two arbitrary channels with A turned "ON" and B "OFF".

Block Diagrams

CD4051BM/CD4051BC



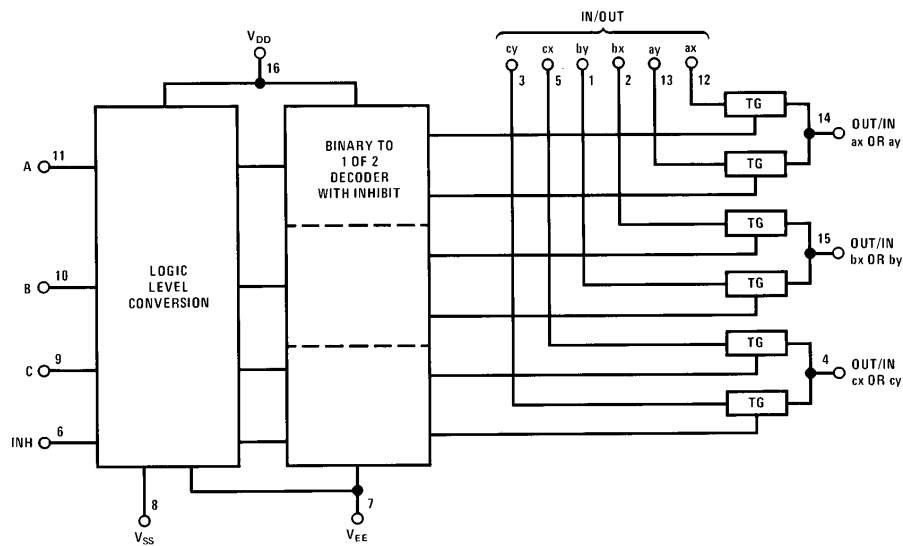
CD4052BM/CD4052BC



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Block Diagrams (Continued)

CD4053BM/CD4053BC



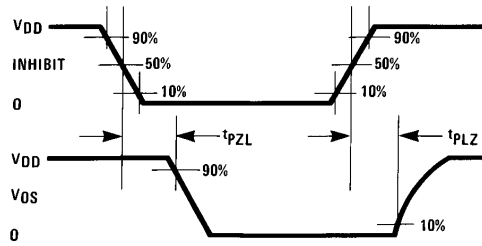
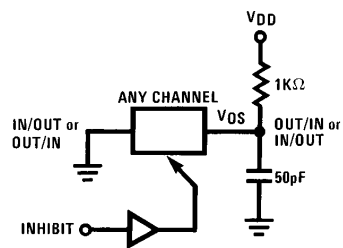
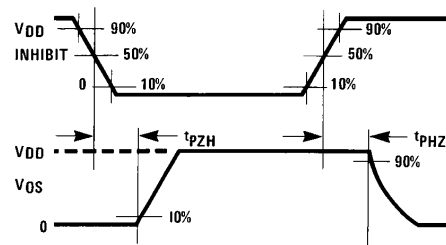
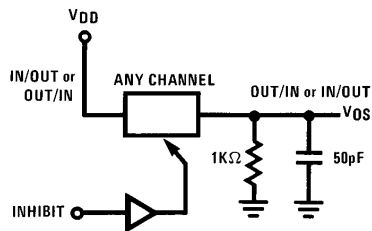
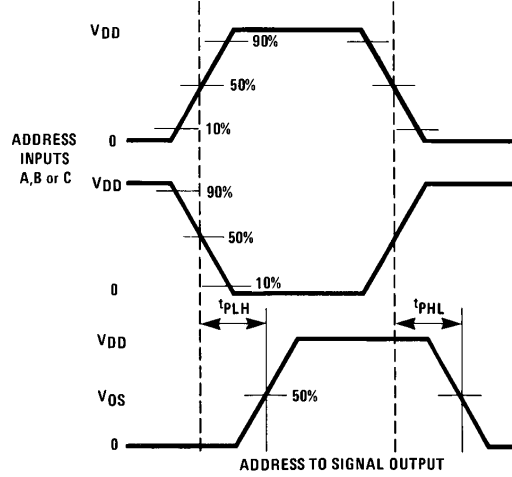
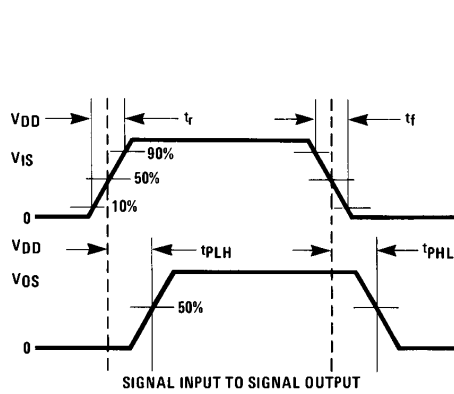
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Truth Table

INPUT STATES				"ON" CHANNELS		
INHIBIT	C	B	A	CD4051B	CD4052B	CD4053B
0	0	0	0	0	0X, 0Y	cx, bx, ax
0	0	0	1	1	1X, 1Y	cx, bx, ay
0	0	1	0	2	2X, 2Y	cx, by, ax
0	0	1	1	3	3X, 3Y	cx, by, ay
0	1	0	0	4		cy, bx, ax
0	1	0	1	5		cy, bx, ay
0	1	1	0	6		cy, by, ax
0	1	1	1	7		cy, by, ay
1	*	*	*	NONE	NONE	NONE

*Don't Care condition.

Switching Time Waveforms



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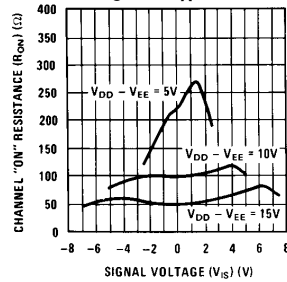
Special Considerations

In certain applications the external load-resistor current may include both V_{DD} and signal-line components. To avoid drawing V_{DD} current when switch current flows into IN/OUT pin, the voltage drop across the bidirectional switch must

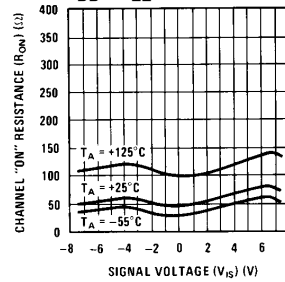
not exceed 0.6V at $T_A \leq 25^\circ\text{C}$, or 0.4V at $T_A > 25^\circ\text{C}$ (calculated from R_{ON} values shown). No V_{DD} current will flow through R_L if the switch current flows into OUT/IN pin.

Typical Performance Characteristics

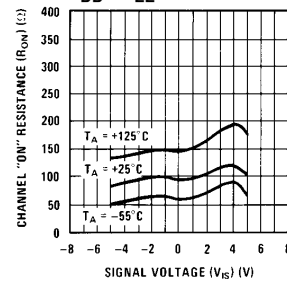
“ON” Resistance vs Signal Voltage for $T_A = 25^\circ\text{C}$



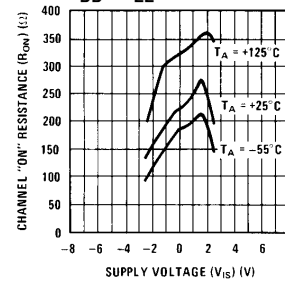
“ON” Resistance as a Function of Temperature for $V_{DD} - V_{EE} = 15\text{V}$



“ON” Resistance as a Function of Temperature for $V_{DD} - V_{EE} = 10\text{V}$



“ON” Resistance as a Function of Temperature for $V_{DD} - V_{EE} = 5\text{V}$



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Physical Dimensions inches (millimeters)

The diagram shows the physical dimensions of the Cavity Dual-In-Line Package (J) in inches and millimeters. The top view shows a rectangular package with pins 1 through 16. Dimensions include a maximum width of 0.785 inches (19.94 mm), a pin pitch of 0.037 inches (0.94 mm), and a maximum height of 0.220 to 0.310 inches (5.59 to 7.87 mm). The side view shows a package with a glass sealant, a maximum height of 0.290 to 0.320 inches (7.37 to 8.13 mm), and a pin angle of 95° ± 5°. The bottom view shows a package with a maximum width of 0.200 inches (5.08 mm) and a pin pitch of 0.037 inches (0.94 mm).

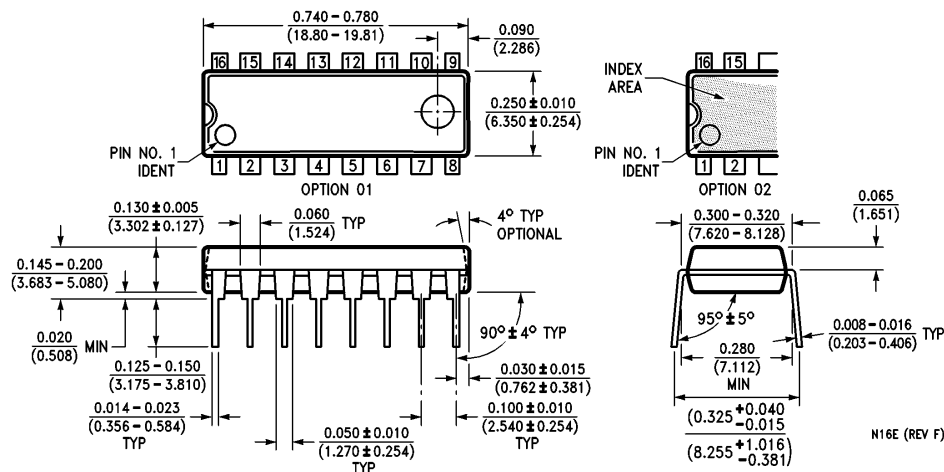
Cavity Dual-In-Line Package (J)
Order Number CD4051BMJ, CD4051BCJ, CD4052BMJ, CD4052BCJ, CD4053BMJ or CD4053BCJ
NS Package Number J16A

The diagram shows the physical dimensions of the Small Outline Package (M) in inches and millimeters. The top view shows a rectangular package with pins 1 through 16. Dimensions include a maximum width of 0.385 to 0.394 inches (9.804 to 10.00 mm), a pin pitch of 0.028 to 0.044 inches (5.791 to 6.198 mm), and a maximum height of 0.010 to 0.020 inches (0.254 to 0.508 mm). The side view shows a package with a maximum height of 0.053 to 0.069 inches (1.346 to 1.753 mm), a pin pitch of 0.014 inches (0.356 mm), and a maximum width of 0.008 to 0.010 inches (0.203 to 0.254 mm). The bottom view shows a package with a maximum width of 0.008 to 0.010 inches (0.203 to 0.254 mm) and a pin pitch of 0.014 inches (0.356 mm).

Small Outline Package (M)
Order Number CD4051BCM, CD4052BCM or CD4053BCM
NS Package Number M16A

**Small Outline Package (M)
Order Number CD4051BCM,
CD4052BCM or CD4053BCM
NS Package Number M16A**

Physical Dimensions inches (millimeters) (Continued)



Molded Dual-In-Line Package (N)
Order CD4051BM, CD4051BC,
CD4052BM, CD4052BC, CD4053BM, CD4053BC
NS Package Number N16E

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



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Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
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- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
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Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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