74ALVC125-Q100

Quad buffer/line driver; 3-state Rev. 2 — 20 January 2014

Product data sheet

1. **General description**

The 74ALVC125-Q100 is a quad non-inverting buffer/line driver with 3-state outputs. The output enable input (nOE) controls the 3 state outputs (nY). A HIGH on the nOE pin causes the outputs to assume a high-impedance OFF-state.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 3) and is suitable for use in automotive applications.

2. **Features and benefits**

- Automotive product qualification in accordance with AEC-Q100 (Grade 3)
 - ◆ Specified from -40 °C to +85 °C
- Wide supply voltage range from 1.65 V to 3.6 V
- 3.6 V tolerant inputs/outputs
- CMOS low power consumption
- Direct interface with TTL levels (2.7 V to 3.6 V)
- Power-down mode
- Latch-up performance exceeds 250 mA
- Complies with JEDEC standards:
 - ◆ JESD8-7 (1.65 V to 1.95 V)
 - ◆ JESD8-5 (2.3 V to 2.7 V)
 - ◆ JESD8B/JESD36 (2.7 V to 3.6 V)
- ESD protection:
 - MIL-STD-883, method 3015 exceeds 2000 V
 - HBM JESD22-A114F exceeds 2000 V
 - ♦ MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)

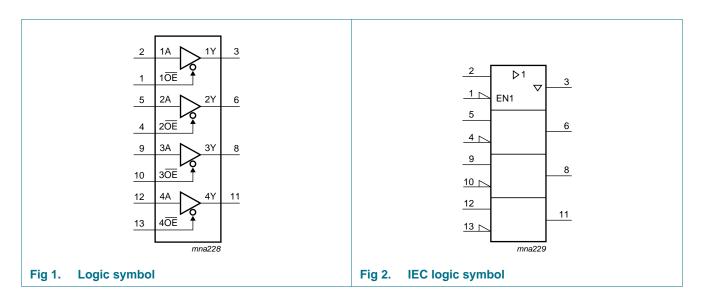


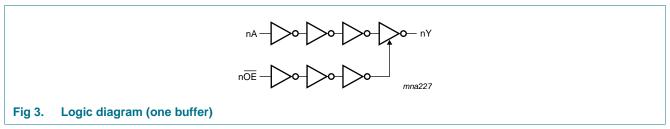
3. Ordering information

Table 1. Ordering information

Type number	Package										
	Temperature range	Name	Description	Version							
74ALVC125D-Q100	-40 °C to +85 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1							
74ALVC125PW-Q100	–40 °C to +85 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1							
74ALVC125BQ-Q100	–40 °C to +85 °C	DHVQFN14	plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;14 terminals; body 2.5 x 3 x 0.85 mm	SOT762-1							

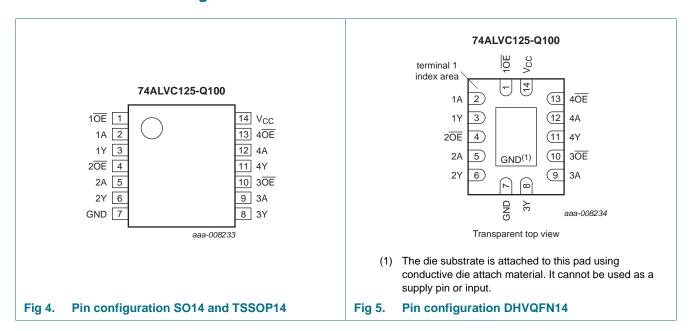
4. Functional diagram





5. Pinning information

5.1 Pinning



5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
nA	2, 5, 9, 12	data input
nY	3, 6, 8, 11	bus output
nOE	1, 4, 10, 13	output enable (active LOW)
V _{CC}	14	supply voltage
GND	7	ground (0 V)

6. Functional description

Table 3. Function table[1]

Input nOE	put				
nOE	nA	nY			
L	L	L			
L	Н	Н			
Н	X	Z			

[1] H = HIGH voltage level

L = LOW voltage level

X= don't care

Z = high-impedance OFF-state

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

						-
Symbol	Parameter	Conditions		Min	Max	Unit
V_{CC}	supply voltage			-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V		-50	-	mA
VI	input voltage		<u>[1]</u>	-0.5	+4.6	V
I _{OK}	output clamping current	$V_O > V_{CC}$ or $V_O < 0 V$		-	±50	mA
Vo	output voltage	output HIGH or LOW state	[1] [2]	-0.5	$V_{CC} + 0.5$	V
		output 3-state		-0.5	+4.6	V
		Power-down mode, $V_{CC} = 0 V$	[2]	-0.5	+4.6	V
Io	output current	$V_O = 0 V \text{ to } V_{CC}$		-	±50	mA
I _{CC}	supply current			-	100	mA
I _{GND}	ground current			-100	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}$	[3]	-	500	mW

^[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

For DHVQFN20 packages: above 60 °C derate linearly with 4.5 mW/K.

^[2] When $V_{CC} = 0 \text{ V}$ (power-down mode), the output voltage can be 3.6 V in normal operation.

^[3] For SO14 packages: above 70 °C derate linearly with 8 mW/K. For TSSOP14 packages: above 60 °C derate linearly with 5.5 mW/K.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		1.65	3.6	V
VI	input voltage		0	3.6	V
V _O ou	output voltage	output HIGH or LOW state	0	V_{CC}	V
		output 3-state	0	3.6	V
		power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature	in free air	-40	+85	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 1.65 \text{ V to } 2.7 \text{ V}$	0	20	ns/V
		V _{CC} = 2.7 V to 3.6 V	0	10	ns/V

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40	0 °C to +8	5 °C	Unit
			Min	Typ[1]	Max	1
V_{IH}	HIGH-level input voltage	V _{CC} = 1.65 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.7	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.65 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	8.0	V
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = -100 \ \mu A; \ V_{CC} = 1.65 \ V \ to \ 3.6 \ V$	$V_{CC}-0.2$	-	-	V
		$I_O = -6 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.25	1.51	-	V
		$I_{O} = -12 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.8	2.10	-	V
		$I_O = -18 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.7	2.01	-	V
		$I_{O} = -12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	2.2	2.53	-	V
		$I_{O} = -18 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.4	2.76	-	V
		$I_{O} = -24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.2	2.68	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_O = 100 μ A; V_{CC} = 1.65 V to 3.6 V	-	-	0.2	V
		$I_O = 6 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	0.11	0.3	V
		$I_{O} = 12 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	0.17	0.4	V
		I_{O} = 18 mA; V_{CC} = 2.3 V	-	0.25	0.6	V
		$I_{O} = 12 \text{ mA}; V_{CC} = 2.7 \text{ V}$	-	0.16	0.4	V
		I_{O} = 18 mA; V_{CC} = 3.0 V	-	0.23	0.4	V
		I_{O} = 24 mA; V_{CC} = 3.0 V	-	0.30	0.55	V
I _I	input leakage current	$V_{CC} = 3.6 \text{ V}; V_{I} = 3.6 \text{ V or GND}$	-	±0.1	±5	μΑ

 Table 6.
 Static characteristics ...continued

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-4	0 °C to +8	5 °C	Unit
			Min	Typ[1]	Max	
I _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 1.65$ V to 3.6 V; $V_O = 3.6$ V or GND;	-	±0.1	±10	μΑ
I _{OFF}	power-off leakage current	$V_{CC} = 0 \text{ V}$; $V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}$	-	±0.1	±10	μΑ
I _{CC}	supply current	$V_{CC} = 3.6 \text{ V}; V_I = V_{CC} \text{ or GND};$ $I_O = 0 \text{ A}$	-	0.2	10	μΑ
ΔI_{CC}	additional supply current	per input pin; V_{CC} = 3.0 V to 3.6 V; V_{I} = V_{CC} - 0.6 V; I_{O} = 0 A	-	5	750	μΑ
C _I	input capacitance		-	3.5	-	pF

^[1] All typical values are measured at V_{CC} = 3.3 V (unless stated otherwise) and T_{amb} = 25 °C.

10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit, see Figure 8.

Symbol	Parameter	Conditions		-40 °C to +85 °C			Unit
				Min	Typ[1]	Max	
t _{pd}	propagation delay	nA to nY; see Figure 6	[2]		'		'
		V _{CC} = 1.65 V to 1.95 V		1.3	2.4	5.3	ns
		V _{CC} = 2.3 V to 2.7 V		1.0	1.7	3.2	ns
		V _{CC} = 2.7 V		-	2.0	3.1	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.1	1.8	2.8	ns
t _{en}	enable time	nOE to nY; see Figure 7	[2]				
		V _{CC} = 1.65 V to 1.95 V		1.4	3.9	6.4	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.0	2.2	4.1	ns
		V _{CC} = 2.7 V		-	2.7	4.3	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.0	1.9	3.5	ns
t _{dis}	disable time	nOE to nY; see Figure 7	[2]				
		V _{CC} = 1.65 V to 1.95 V		1.8	3.9	5.9	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.0	2.1	3.4	ns
		V _{CC} = 2.7 V		-	2.9	4.0	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.4	2.7	4.0	ns

 Table 7.
 Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V). For test circuit, see Figure 8.

Symbol	Parameter	Conditions		-40	°C to +85	S °C	Unit
				Min	Typ[1]	Max	
C_{PD}	power dissipation	per buffer; $V_I = GND$ to V_{CC} ; $V_{CC} = 3.3 \text{ V}$	[3]				
	capacitance	outputs HIGH or LOW state		-	27	-	pF
		outputs 3-state		-	5	-	pF

- [1] Typical values are measured at $T_{amb} = 25 \, ^{\circ}C$
- [2] t_{pd} is the same as t_{PHL} and t_{PLH} .

ten is the same as tPZH and tPZL.

 t_{dis} is the same as t_{PHZ} and t_{PLZ} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}{}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}{}^2 \times f_o) \text{ where:}$

 f_i = input frequency in MHz; f_o = output frequency in MHz

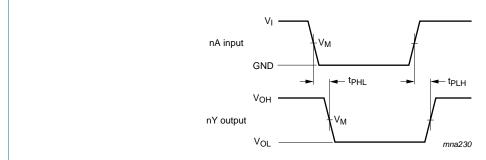
C_L = output load capacitance in pF

V_{CC} = supply voltage in Volts

N = number of inputs switching

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs

11. Waveforms



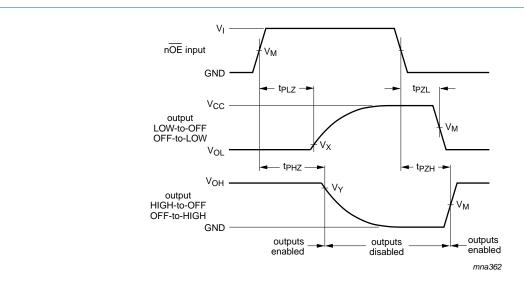
Measurement points are given in <u>Table 8</u>.

 V_{OL} and V_{OH} are the typical output voltage levels that occur with the output load.

Fig 6. Input nA to output nY propagation delay times

Table 8. Measurement points

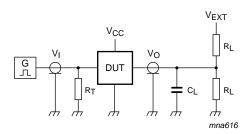
Supply voltage	Input	Output	Output						
V _{CC}	V _M	V _M	V _x	V_y					
1.65 V to 1.95 V	0.5V _{CC}	0.5V _{CC}	V _{OL} + 0.15 V	$V_{OH}-0.15\ V$					
2.3 V to 2.7 V	0.5V _{CC}	0.5V _{CC}	V _{OL} + 0.15 V	V _{OH} – 0.15 V					
2.7 V	1.5 V	1.5 V	V _{OL} + 0.3 V	$V_{OH} - 0.3 V$					
3.0 V to 3.6 V	1.5 V	1.5 V	V _{OL} + 0.3 V	$V_{OH} - 0.3 V$					



Measurement points are given in Table 8.

V_{OL} and V_{OH} are the typical output voltage levels that occur with the output load.

Fig 7. Enable and disable times



Test data is given in Table 9.

Definitions for test circuit:

 R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator.

V_{EXT} = External voltage for measuring switching times.

Fig 8. Test circuitry for switching times

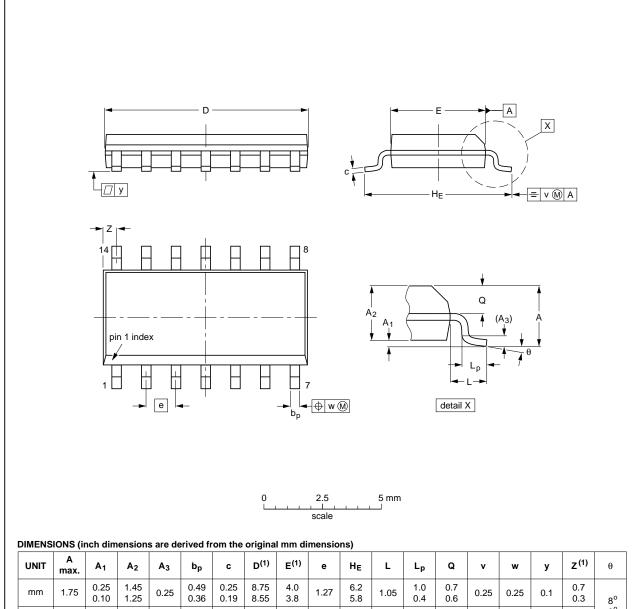
Table 9. Test data

Supply voltage	Input		Load		V _{EXT}	V _{EXT}			
	VI	t _r , t _f	CL	R_L	t_{PLH}, t_{PHL}	t_{PLZ}, t_{PZL}	t _{PHZ} , t _{PZH}		
1.65 V to 1.95 V	V_{CC}	\leq 2.0 ns	30 pF	1 k Ω	open	$2\times V_{CC}$	GND		
2.3 V to 2.7 V	V_{CC}	≤ 2.0 ns	30 pF	500Ω	open	$2\times V_{CC}$	GND		
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500Ω	open	6 V	GND		
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	6 V	GND		

12. Package outline

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



UNIT	A max.	A ₁	A ₂	А3	bp	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Q	v	w	у	z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	8.75 8.55	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.010 0.004	0.057 0.049	0.01	ı	0.0100 0.0075		0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016		0.01	0.01	0.004	0.028 0.012	0°

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

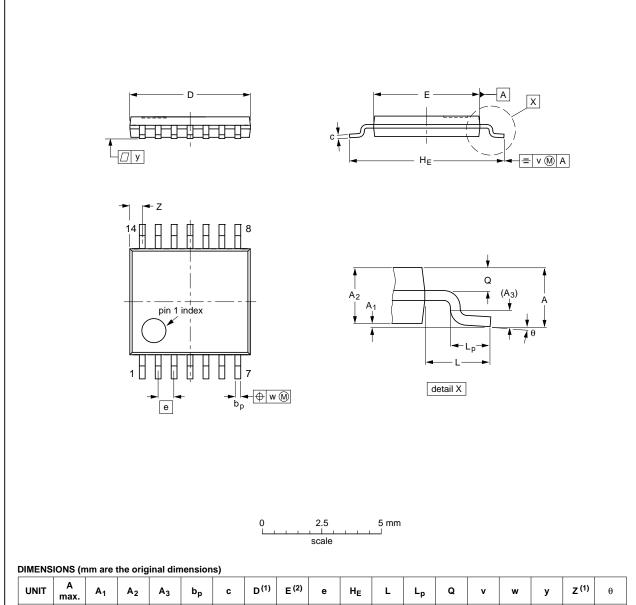
OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT108-1	076E06	MS-012				99-12-27 03-02-19	

Package outline SOT108-1 (SO14)

74ALVC125_Q100

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽²⁾	е	HE	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.19	0.2 0.1	5.1 4.9	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.72 0.38	8° 0°

Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT402-1		MO-153				99-12-27 03-02-18	

Fig 10. Package outline SOT402-1 (TSSOP14)

74ALVC125_Q100

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DHVQFN14: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 x 3 x 0.85 mm SOT762-1

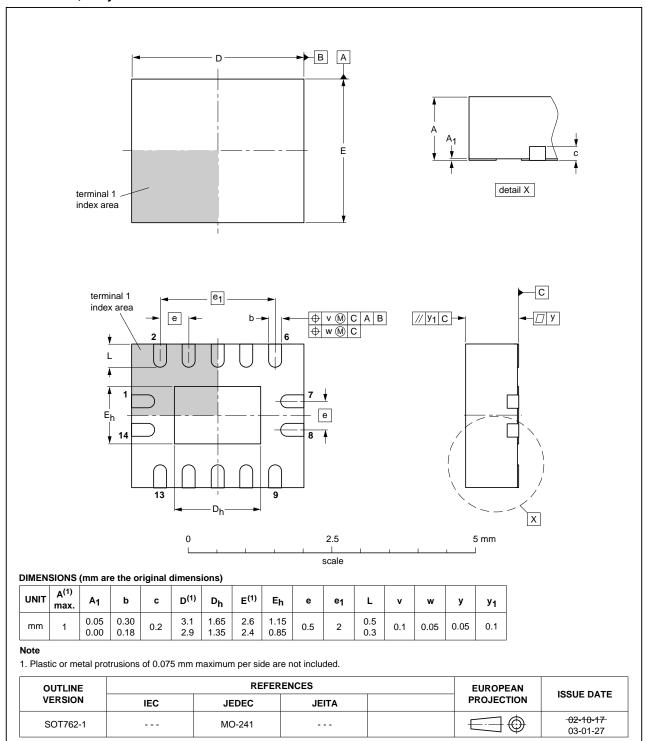


Fig 11. Package outline SOT762-1 (DHVQFN14)

13. Abbreviations

Table 10. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MIL	Military
MM	Machine Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 11. Revision history

Document ID Release date		Data sheet status	Change notice	Supersedes		
74ALVC125_Q100 v.2	20140120	Product data sheet	-	74ALVC125_Q100 v.1		
Modifications:	 Feature list correct 	ted (errata).				
74ALVC125_Q100 v.1	20130628	Product data sheet	-	-		

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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17. Contents

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- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов:
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001:
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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