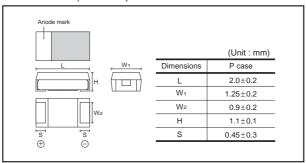
Chip tantalum capacitors TC Series P Case

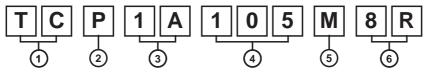
●Features (P)

- 1) Vital for all hybrid integrated circuits board application.
- 2) Wide capacitance range.
- 3) Screening by thermal shock.

●Dimensions (Unit: mm)



●Part No. Explanation



- 1 Series name
- 2 Case style
- 3 Rated voltage

Rated voltage (V)	4	6.3	10	16	20	25
CODE	0G	0J	1A	1C	1D	1E

(4) Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

(5) Capacitance tolerance

M: ±20% K: ±10%

- **6** Taping
 - 8 : Tape width
 - R : Positive electrode on the side opposite to sprocket hole

Rated table

	Rated voltage (V)									
(μF)	4 0G	6.3 0J	10 1A	16 1C	20 1D	25 1E				
1 (105)			Р	Р	Р	Р				
1.5 (155)		Р	Р	*P						
2.2 (225)	Р	Р	Р	*P						
3.3 (335)	Р	Р	Р	*P						
4.7 (475)	Р	Р	Р							
6.8 (685)	Р	Р	Р							
10 (106)	Р	Р	Р							
15 (156)	Р	Р								
22 (226)	Р	Р								
33 (336)										
47 (476)										
68 (686)										

Remark) Case size codes (P) in the above show products line-up.

Marking

The indications listed below should be given on the surface of a capacitor.

- (1) Polarity : The polarity should be shown by □ bar. (on the anode side)
 (2) Rated DC voltage : Due to the small size of P case, a voltage code is used as shown below.
 (3) Visual typical example (1) voltage code (2) capacitance code

Voltage Code	Rated DC Voltage (V)
g	4
j	6.3
А	10
С	16
D	20
Е	25

Capacitance Code	Nominal Capacitance (μF)
А	1.0
Е	1.5
J	2.2
N	3.3
S	4.7
W	6.8
а	10
е	15
j	22

[P case] note 1)



note 2) voltage code and capacitance code are variable with parts number

^{*} Under development

Characteristics

Iter	m	Performance				Test	Test conditions (based on JIS C 5101-1 and JIS C 5101-3)								
Operating Temp	perature	-5	5°C	to +	-125	°C				Volta	Voltage reduction when temperature exceeds +85°C			ceeds +85°C	
Maximum operatemperature with derating	ting no voltage	+8	5°C												
Rated voltage (VDC)	4	4 6.3 10 16 20 25			at 85	at 85°C								
Category voltag	ge (VDC)	2.5	4	6.3	10	13	16	;		at 12	25°C				
Surge voltage (VDC)	5.0	8	13	20	26	32	:		at 85	5°C				
DC Leakage cu	ırrent						which ard lis		ver is greater	As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage: Rated voltage for 1min					
Capacitance tol	erance	1	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.D Measuring circuit : DC Equivalent series												
Tangent of loss (Df, tan δ)	Fangent of loss angle Df, $\tan\delta$) Shall be satisfied the voltage on " Standard list "				As p Mea Mea	As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit									
Impedance	pedance			Shall be satisfied the voltage on "Standard list"						As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage: 0.5Vrms or less Measuring circuit: DC Equivalent series circuit			eries circuit		
Resistance to Soldering heat	Appearance		There should be no significant abnormality. The indications should be clear.				As p	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3 Dis in the colder both							
	L.C.	Le	ss th	nan	initia	al lir	nit			Dip in the solder bath Solder temp : 260±5°C					
	ΔC / C	TCP0J226□: Within ±20% of initial value TCP1A106□: Within ±20% of initial value Others: Within ±10% of initial value					0% of initial value	Rep After	Duration : 5±0.5s Repetition : 1 After the specimens, leave it at room temperature for over 24h and then measure the sample.						
	Df (tan δ)	Less than 150% of initial limit						tial	limit	and the same state of the same					
Temperature cycle	Appearance								icant abnormality.	As p	er 4.	16 JIS C 5101 10 JIS C 5101			
	L.C.	1	P0J hers						150% of initial limit nitial limit		Repetition : 5 cycles (1 cycle : steps 1 to 4) without discontinuation.				
	ΔC / C	1 t	o 10	μF :	: Wi	thin	±10	%	of initial value		1	Temp. -55±3°C	Time 30±3min.		
				•					of initial value		2	Room temp.	30±3min. 3min.or less		
		-		-	_				0% of initial value	4	3	125±2°C	30±3min.		
	Df (tan δ)	Le	ss th	nan	150	% c	of init	tial	limit		4	Room temp.	3min.or less		
											After the specimens, leave it at room temperature for over 24h and then measure the sample.				
Moisture resistance	Appearance								icant abnormality.	As p	er 4.	22 JIS C 5101- 12 JIS C 5101-	-3		
L.C.		TCP0J226⊡: Less than 150% of initial limit Others : Less than initial limit				After leaving the sample under such atmospheric condition that the temperature and humidity are 60±2°C and 90 to 95% RH,respectively, for 500±12h									
	L.C.		hers	6	_:	Les	s tha	an	nitial limit	60+3	2°C:				
	L.C. ΔC/C	Ot					s tha			leav	e it a	and 90 to 95% l at room		for 500±12h	

Item		Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3				
Temperature	Temp.	_55°C	As per 4.29 JIS C 5101-1				
Stability	ΔC / C	Within 0/–15% of initial value	As per 4.13 JIS C 5101-3				
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	-					
	Temp.	+85°C					
	ΔC / C	Within +15/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	5μA or 0.1CV whichever is greater					
	Temp.	+125°C					
	ΔC / C	Within +20/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	6.3μA or 0.125CV whichever is greater					
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1				
	L.C.	Shall be satisfied the voltage on " Standard list "	As per 4.14JIS C 5101-3 Apply the specified surge voltage every 5±0.5 min.				
	ΔC / C	TCP0J226□: Within ±20% of initial value Others: Within ±10% of initial value	for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times. After the specimens, leave it at room temperature for				
	Df (tan δ)	Less than 150% of initial limit	over 24h and then measure the sample.				
Loading at	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1				
High temperature	L.C.	TJP0J226□: Less than 150% of initial limit Others : Less than initial limit	As per 4.15 JIS C 5101-3 After applying the rated voltage for 1000+36/0 h without discontinuation via the serial resistance of 3Ω or less				
	ΔC / C	TJP0J226□: Within ±20% of initial value Others : Within ±10% of initial value	at a temperature of 85±2°C, leave the sample at room temperature / humidity for over 24h and measure the value.				
	Df (tan δ)	Less than 150% of initial limit					
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1				
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure below) (Unit: mm) F (Apply force) thickness=1.6mm				

Ite	em	Performance	Test conditions (JIS C 5101-1 and JIS C 5101-3)			
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.			
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.			
Resistance	e to solvents	The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.			
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed=25±2.5mm / s Pre-treatment(accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration : 3±0.5s Solder : M705 Flux : Rosin 25% IPA 75%			
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm			
	Appearance	There should be no significant abnormality.	Time: 2h each in X and Y directions Mounting: The terminal is soldered on a print circuit board.			

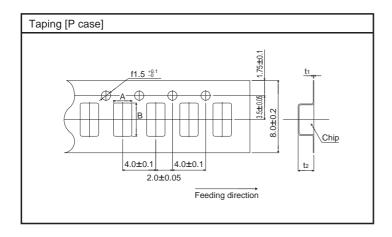
• Standard products list, TC series A case

Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)	:	Impedance 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.60s (μA)	–55°C	25°C 85°C	125°C	(Ω)
TC P 0G 225 □	4	2.5	5	2.2	±20, 10	0.5	15	10	15	17.5
TC P 0G 335 □	4	2.5	5	3.3	±20, 10	0.5	30	20	30	17.5
TC P 0G 475 □	4	2.5	5	4.7	±20, 10	0.5	30	20	30	14.4
TC P 0G 685 □	4	2.5	5	6.8	±20, 10	0.5	30	20	30	11.8
TC P 0G 106 □	4	2.5	5	10	±20, 10	0.5	30	20	30	9.3
TC P 0G 156 □	4	2.5	5	15	±20, 10	0.6	30	20	30	8.3
TC P 0G 226 □	4	2.5	5	22	±20, 10	0.9	30	20	30	7.7
TC P 0J 155 □	6.3	4	8	1.5	±20, 10	0.5	15	10	15	17.5
TC P 0J 225 □	6.3	4	8	2.2	±20, 10	0.5	30	20	30	17.5
TC P 0J 335 □	6.3	4	8	3.3	±20, 10	0.5	30	20	30	14.4
TC P 0J 475 □	6.3	4	8	4.7	±20, 10	0.5	30	20	30	11.8
TC P 0J 685 □	6.3	4	8	6.8	±20, 10	0.5	30	20	30	9.3
TC P 0J 106 □	6.3	4	8	10	±20, 10	0.6	30	20	30	8.3
TC P 0J 156 □	6.3	4	8	15	±20, 10	0.9	30	20	30	7.7
TC P 0J 226 □	6.3	4	8	22	±20, 10	1.4	38	25	38	5.0
TC P 1A 105 □	10	6.3	13	1.0	±20, 10	0.5	15	10	15	17.5
TC P 1A 155 □	10	6.3	13	1.5	±20, 10	0.5	30	20	30	16.1
TC P 1A 225 □	10	6.3	13	2.2	±20, 10	0.5	30	20	30	14.4
TC P 1A 335 □	10	6.3	13	3.3	±20, 10	0.5	30	20	30	11.8
TC P 1A 475 □	10	6.3	13	4.7	±20, 10	0.5	30	20	30	9.3
TC P 1A 685 □	10	6.3	13	6.8	±20, 10	0.7	30	20	30	9.3
TC P 1A 106 □	10	6.3	13	10	±20, 10	1.0	30	20	30	8.3
TC P 1C 105 □	16	10	20	1.0	±20, 10	0.5	15	10	15	16.1
TC P 1D 105 □	20	13	26	1.0	±20, 10	0.5	15	10	15	16.1
TC P 1E 105 □	25	16	32	1.0	±20, 10	0.6	30	20	30	9.3

□=Tolerance (M : ±20%, K : ±10%)

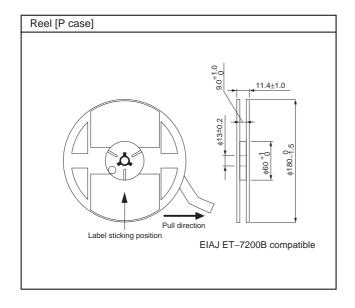
Packaging specifications

Case code	A±0.1	B±0.1	t ₁ ± 0.05	t2±0.1
Р	1.55	2.3	0.25	1.5



Packaging style

Case code	Packaging	Packaç	ging style	Symbol	Basic ordering units
P case	Taping	plastic taping	∮180mm Reel	R	3,000pcs



•Recommended condition of reflow soldering

(1) Leakage current-to-voltage ratio

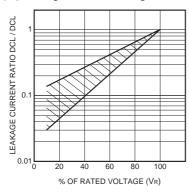
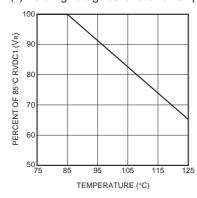


Fig.1

(2) Derating voltage as function of temperature



85	5°C	125°C				
Rated Voltage	Surge Voltage	Category Voltage	Surge Voltage			
(V.DC)	(V.DC)	(V.DC)	(V.DC)			
4	5	2.5	3.2			
6.3	8	4	5			
10	13	6.3	8			
16	20	10	13			
20	26	13	16			

Fig.2

(3) Reliability

The malfunction rate of tantalum solid state electrolytic capacitors varies considerably depending on the conditions of usage (ambient temperature, applied voltage, circuit resistance).

Formula for calculating malfunction rate

 $\lambda p = \lambda b \times (\pi E \times \pi SR \times \pi Q \times \pi CV)$

λp : Malfunction rate stemming from operation

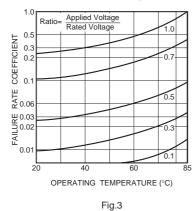
 $\begin{array}{lll} \lambda b & : \mbox{Basic malfunction rate} \\ \pi E & : \mbox{Environmental factors} \\ \pi S R & : \mbox{Series resistance} \end{array}$

 $\pi \mbox{\scriptsize Q}$: Level of malfunction rate

 πcv : Capacitance

For details on how to calculate the malfunction rate stemming from operation, see the tantalum solid state electrolytic capacitors column in MIL-HDBK-217.

Malfunction rate as function of operating temperature and rated voltage



Malfunction rate as function of circuit resistance (Ω /V)

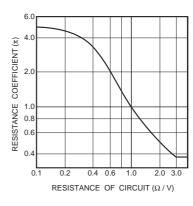


Fig.4

(4) Maximum power dissipation

Warming of the capacitor due to ripple voltage balances with warming caused by Joule heating and by radiated heat. Maximum allowable warming of the capacitor is to 5°C above ambient temperature. When warming exceeds 5°C, it can damage the dielectric and cause a short circuit.

Power dissipation (P) = $I^2 \bullet R$

Ripple current

P: As shown in table at right

R: Equivalent series resistance

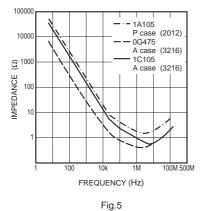
Notes

- 1. Please be aware that when case size is changed, maximum allowable power dissipation is reduced.
- 2. Maximum power dissipation varies depending on the package. Be sure to use a case which will keep warming within the limits shown in the table below.

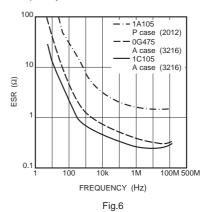
Allowable power dissipation (W) and maximum temperature rising

Temp.	+25°C	+55°C	+85°C	+125°C
P case (2012)	0.025	0.022	0.020	0.010
A case (3216)	0.070	0.063	0.056	0.028
Max. Temp Rise [°C]	5	5	5	2

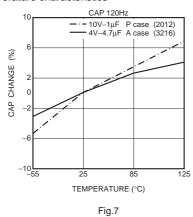
(5) Impedance frequency characteristics

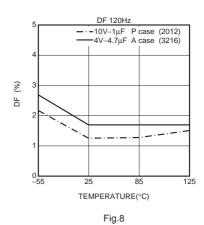


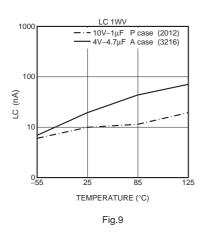
(6) ESR frequency characteristics

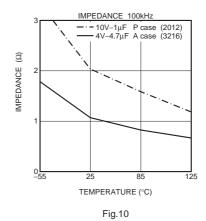


(7) Temperature characteristics









Rush current

The rush current is in inverse proportion to the ESR. The excessive rush current may cause a damage.

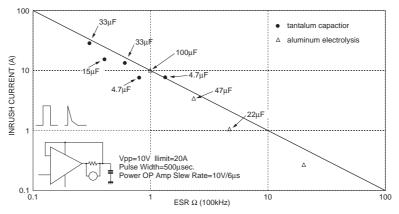


Fig. 11 Max. rush current and ESR

The rush current may be reduced by the protection resistors

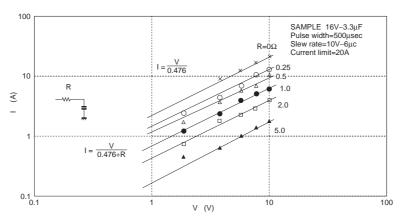


Fig. 12 Change in I max by protection resistors

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- Техническая поддержка проекта;
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