CMOS Digital Integrated Circuits Silicon Monolithic

# 74VHC21FT

## 1. Functional Description

• Dual 4-Input AND Gate

#### 2. General

The 74VHC21FT is an advanced high speed CMOS 4-INPUT AND GATE fabricated with silicon gate  $C^2MOS$  technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

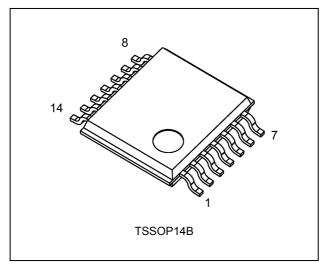
The internal circuit is composed of 4 stages including a buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

### 3. Features

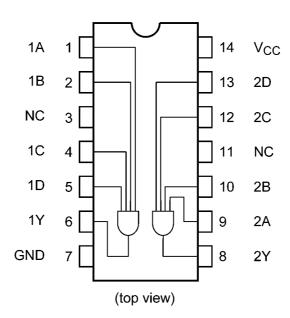
- (1) AEC-Q100 (Rev. H) (Note 1)
- (2) Wide operating temperature:  $T_{opr} = -40$  to 125 °C
- (3) High speed:  $t_{pd}$  = 3.3 ns (typ.) at V<sub>CC</sub> = 5.0 V
- (4) Low power dissipation:  $I_{CC}$  = 2.0  $\mu$ A (max) at  $T_a$  = 25 °C
- (5) High noise immunity:  $V_{\text{NIH}} = V_{\text{NIL}} = 28 \% V_{\text{CC}}$  (min)
- (6) Power down protection is provided on all inputs.
- (7) Balanced propagation delays:  $t_{PLH} \approx t_{PHL}$
- (8) Wide operating voltage range:  $V_{CC(opr)} = 2.0 \text{ V to } 5.5 \text{ V}$
- (9) Pin and function compatible with the 74 series (AC/HC/AHC/LV etc.) 21 type.
- Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

### 4. Packaging

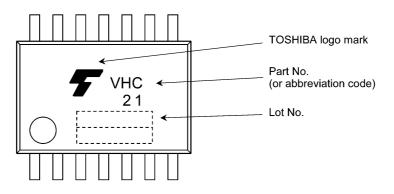


## 5. Pin Assignment

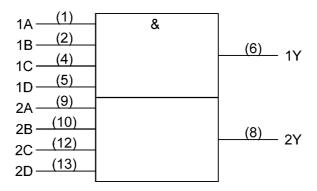
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## 6. Marking



7. IEC Logic Symbol



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#### 8. Truth Table

А	В	С	D	Y
L	Х	Х	Х	L
Х	L	Х	Х	L
Х	Х	L	Х	L
Х	Х	Х	L	L
Н	Н	Н	Н	Н

X: Don't care

## 9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 7.0	V
Input voltage	V <sub>IN</sub>		-0.5 to 7.0	V
Output voltage	V <sub>OUT</sub>		-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	l <sub>iK</sub>		-20	mA
Output diode current	I <sub>ОК</sub>		±20	mA
Output current	I <sub>OUT</sub>		±25	mA
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	mA
Power dissipation	PD	(Note 1)	180	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: 180 mW in the range of  $T_a = -40$  to 85 °C. From  $T_a = 85$  to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.

## 10. Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Rating	Unit
Supply voltage	V <sub>CC</sub>		2.0 to 5.5	V
Input voltage	V <sub>IN</sub>		0 to 5.5	V
Output voltage	V <sub>OUT</sub>		0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>		-40 to 125	°C
Input rise and fall times	dt/dv	$V_{CC}$ = 3.3 $\pm$ 0.3 V	0 to 100	ns/V
		$V_{CC}$ = 5.0 $\pm$ 0.5 V	0 to 20	1

Note: The operating ranges are required to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

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#### **11. Electrical Characteristics**

## 11.1. DC Characteristics (Unless otherwise specified, $T_a = 25 \text{ °C}$ )

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
High-level input voltage	VIH	—		2.0	1.50	_	—	V
				3.0 to 5.5	$V_{CC} \times 0.7$	—	—	
Low-level input voltage	V <sub>IL</sub>	—		2.0	—	_	0.50	V
				3.0 to 5.5	—	_	$V_{CC} \times 0.3$	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> = -50 μA	2.0	1.9	2.0	—	V
				3.0	2.9	3.0	—	
				4.5	4.4	4.5	—	
			I <sub>OH</sub> = -4 mA	3.0	2.58	_	—	
			I <sub>OH</sub> = -8 mA	4.5	3.94	-	—	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 50 μA	2.0	—	0.0	0.1	V
				3.0	—	0.0	0.1	
				4.5	—	0.0	0.1	
			I <sub>OL</sub> = 4 mA	3.0	—	_	0.36	
			I <sub>OL</sub> = 8 mA	4.5	—	_	0.36	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	—	_	±0.1	μA
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		5.5	_	_	2.0	μA

## 11.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	—		2.0	1.50	—	V
				3.0 to 5.5	$V_{CC} \times 0.7$	_	
Low-level input voltage	VIL	_		2.0	—	0.50	V
				3.0 to 5.5	—	$V_{CC} \times 0.3$	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IH}$	I <sub>OH</sub> = -50 μA	2.0	1.9	—	V
				3.0	2.9	—	
				4.5	4.4	_	
			I <sub>OH</sub> = -4 mA	3.0	2.48	_	
			I <sub>OH</sub> = -8 mA	4.5	3.80	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 50 μA	2.0	—	0.1	V
				3.0	—	0.1	
				4.5	—	0.1	
			I <sub>OL</sub> = 4 mA	3.0	—	0.44	
			I <sub>OL</sub> = 8 mA	4.5	—	0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5		±1.0	μA
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	20.0	μA

### 11.3. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 125 °C)

Characteristics	Symbol	Test Cond	dition	V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	VIH	—		2.0	1.50	—	V
				3.0 to 5.5	$V_{CC} \times 0.7$	—	
Low-level input voltage	VIL	—		2.0	—	0.50	V
				3.0 to 5.5	—	$V_{CC} \times 0.3$	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> = -50 μA	2.0	1.9	—	V
				3.0	2.9	—	
				4.5	4.4	—	
			I <sub>OH</sub> = -4 mA	3.0	2.40	—	
			I <sub>OH</sub> = -8 mA	4.5	3.70	—	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 50 μA	2.0	—	0.1	V
				3.0	—	0.1	
				4.5	—	0.1	
			I <sub>OL</sub> = 4 mA	3.0	—	0.55	
			I <sub>OL</sub> = 8 mA	4.5	—	0.55	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	±2.0	μA
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	40.0	μA

## 11.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		$\textbf{3.3}\pm\textbf{0.3}$	15	_	4.8	7.0	ns
				50	_	7.3	10.5	
			$5.0\pm0.5$	15	_	3.3	5.0	
				50	_	4.8	7.0	
Input capacitance	C <sub>IN</sub>				_	4	10	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)				20	_	

Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/2$  (per gate)

#### 11.5. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$\textbf{3.3}\pm\textbf{0.3}$	15	1.0	8.5	ns
			50	1.0	12.0	
		$5.0\pm0.5$	15	1.0	6.0	
			50	1.0	8.0	
Input capacitance	C <sub>IN</sub>			_	10	pF

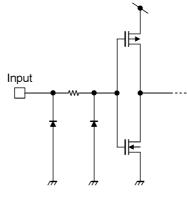
#### 11.6. AC Characteristics

#### (Unless otherwise specified, $T_a = -40$ to 125 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	V <sub>CC</sub> (V)	$C_L (pF)$	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	$3.3\pm0.3$	15	1.0	10.0	ns
			50	1.0	13.5	
		$5.0\pm0.5$	15	1.0	7.0	
			50	1.0	9.0	
Input capacitance	C <sub>IN</sub>				10	pF

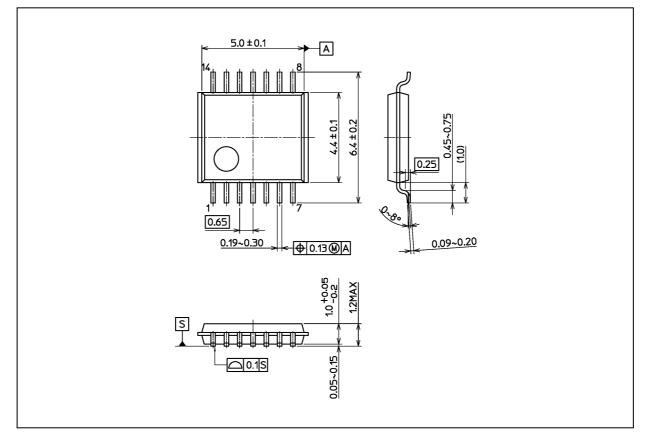
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## 11.7. Input Equivalent Circuit



## Package Dimensions

Unit: mm



#### Weight: 0.054 g (typ.)

	Package Name(s)
Nickname: TSSOP14B	

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