DISCRETE SEMICONDUCTORS

DATA SHEET

BUJ403ASilicon Diffused Power Transistor

Product specification

October 2018



BUJ403A

GENERAL DESCRIPTION

High-voltage, high-speed planar-passivated npn power switching transistor in TO220AB envelope intended for use in high frequency electronic lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

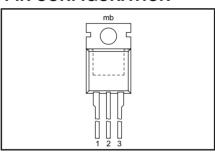
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{BF} = 0 V$	-	1200	V
V _{CBO}	Collector-Base voltage (open emitter)		-	1200	V
V _{CEO}	Collector-emitter voltage (open base)		-	550	V
I _C	Collector current (DC)		-	6	Α
1 1	Collector current peak value		-	10	l a l
P	Total power dissipation	$T_{mb} \le 25 ^{\circ}C$	-	100	W
P _{tot} V _{CEsat}	Collector-emitter saturation voltage	$I_{\rm C} = 2 \text{A}; I_{\rm B} = 0.4 \text{A}$	0.15	1.0	V
h _{FEsat}	DC current gain	$I_{\rm C} = 3 \text{A}; V_{\rm CE} = 5 \text{V}$	15.5		
t, Loan	Fall time	I _C =2.5 A; I _{B1} =0.5 A	170	300	ns

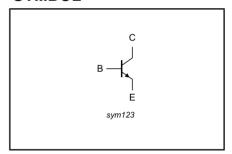
PINNING - TO220AB

PIN	DESCRIPTION					
1	base					
2	collector					
3	emitter					
tab	collector					

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CESM}	Collector to emitter voltage	$V_{BE} = 0 \text{ V}$	-	1200	V
V _{CEO}	Collector to emitter voltage (open base)		-	550	V
$egin{array}{c} V_{CEO} \ V_{CBO} \end{array}$	Collector to base voltage (open emitter)		-	1200	V
l _C	Collector current (DC)		-	6	Α
I I _{CM}	Collector current peak value		-	10	Α
I I _B	Base current (DC)		-	3	Α
l I _{BM}	Base current peak value		-	5	Α
P _{tot}	Total power dissipation	T _{mb} ≤ 25 °C	-	100	W
T _{stq}	Storage temperature		-65	150	°C
T _i	Junction temperature		-	150	°C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R _{th i-mb}	Junction to mounting base		-	1.25	K/W
R _{th i-a}	Junction to ambient	in free air	60	-	K/W

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Silicon Diffused Power Transistor

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STATIC CHARACTERISTICS

 T_{mb} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES} ,I _{CBO}	Collector cut-off current ¹	$ \begin{aligned} &V_{\text{BE}} = 0 \text{ V; } V_{\text{CE}} = V_{\text{CESMmax}} \\ &V_{\text{BE}} = 0 \text{ V; } V_{\text{CE}} = V_{\text{CESMmax}}; \\ &T_{j} = 125 \text{ °C} \end{aligned} $		1 1	1.0 2.0	mA mA
I _{CEO} I _{EBO} V _{CEOsust}	Collector cut-off current ¹ Emitter cut-off current Collector-emitter sustaining voltage	$V_{CEO} = V_{CEOMmax}(550V)$ $V_{EB} = 7 \text{ V; } I_{C} = 0 \text{ A}$ $I_{B} = 0 \text{ A; } I_{C} = 10 \text{ mA;}$ $I_{C} = 25 \text{ mH}$	- - 550		0.1 0.1 -	mA mA V
V _{CEsat} V _{BEsat} h _{FE} h _{FE} h _{FEsat} h _{FEsat}	Collector-emitter saturation voltage Base-emitter saturation voltage DC current gain DC current gain	$\begin{split} &I_{C} = 2.0 \text{ A; } I_{B} = 0.4 \text{ A} \\ &I_{C} = 2.0 \text{ A; } I_{B} = 0.4 \text{ A} \\ &I_{C} = 1 \text{ mA; } V_{CE} = 5 \text{ V} \\ &I_{C} = 500 \text{ mA; } V_{CE} = 5 \text{ V} \\ &I_{C} = 2.0 \text{ A; } V_{CE} = 5 \text{ V} \\ &I_{C} = 3.0 \text{ A; } V_{CE} = 5 \text{ V} \end{split}$	- 13 20 13 -	0.15 0.91 25 30 18.5 15.5	1.0 1.5 - 47 25 -	V

DYNAMIC CHARACTERISTICS

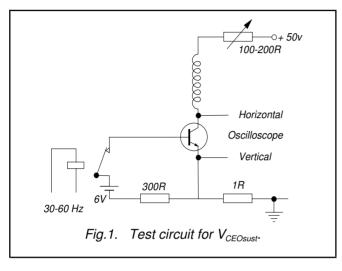
 T_{mb} = 25 °C unless otherwise specified

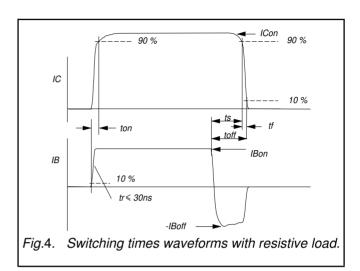
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	$I_{Con} = 2.5 \text{ A}; I_{Bon} = -I_{Boff} = 0.5 \text{ A}; R_L = 75 \text{ ohms}; V_{BB2} = 4 \text{ V};$			
t _{on}	Turn-on time		-	0.5	μs
t _s	Turn-off storage time Turn-off fall time		-	3	μs
Lf			-	0.3	μs
	Switching times (inductive load)	$I_{Con} = 2.5 \text{ A}; I_{Bon} = 0.5 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}$			
ts	Turn-off storage time	- BB	-	1.5	μs
ι _f	Turn-off fall time		170	300	ns
	Switching times (inductive load)	$I_{Con} = 2.5 \text{ A}; I_{Bon} = 0.5 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}; T_{i} = 100 ^{\circ}\text{C}$			
t _s	Turn-off storage time		-	1.8	μs
t _f	Turn-off fall time		-	300	ns

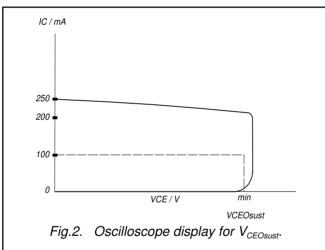
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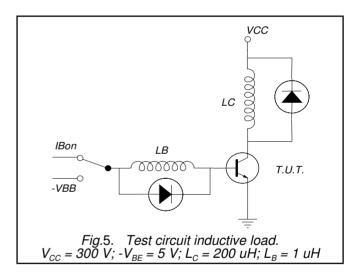
¹ Measured with half sine-wave voltage (curve tracer).

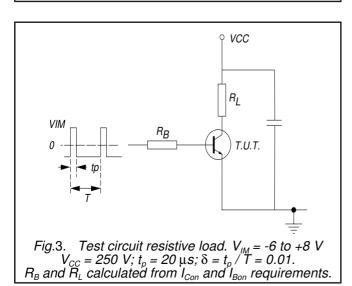
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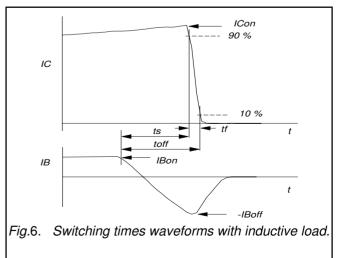




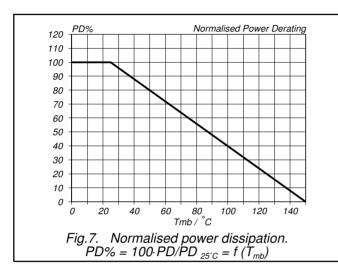


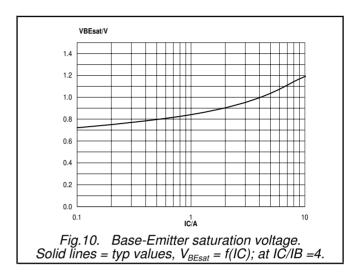


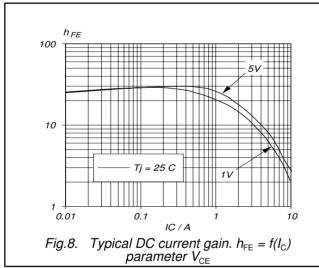


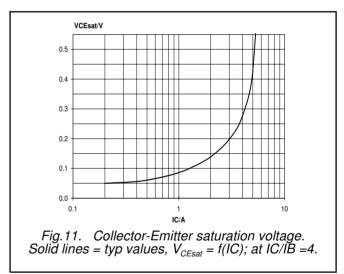


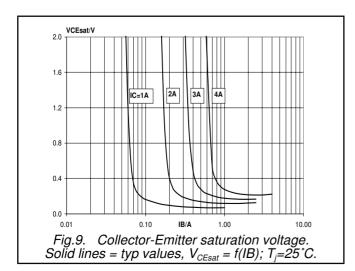
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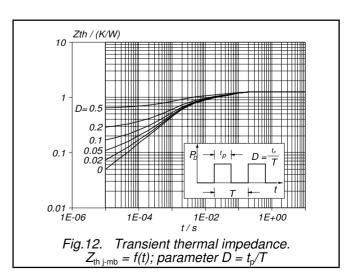




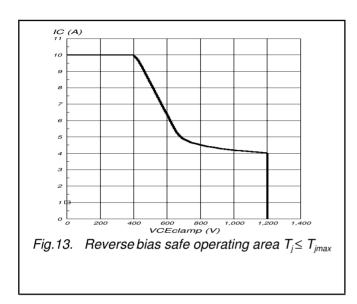








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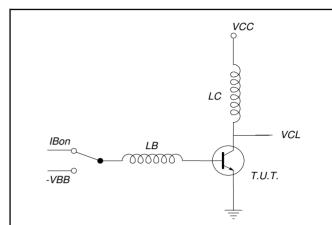


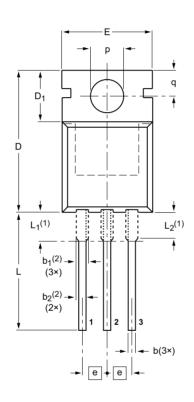
Fig.14. Test Circuit for reverse bias safe operating area $V_{cl} \leq 1000V; \ V_{cc} = 150V; \ V_{BB} = -5V; L_B = 1\mu H; \ L_c = 200\mu H$

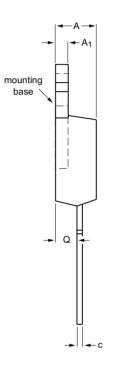
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MECHANICAL DATA

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB

SOT78





0 5 10 mm scale

DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁	b	b ₁ ⁽²⁾	b ₂ ⁽²⁾	С	D	D ₁	E	е	L	L ₁ ⁽¹⁾	L ₂ ⁽¹⁾ max.	р	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

Notes

- Lead shoulder designs may vary.
 Dimension includes excess dambar.

OUTLINE		REFER	ENCES	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT78		3-lead TO-220AB	SC-46		08-04-23 08-06-13

Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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