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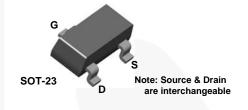
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FAIRCHILD

# MMBF5460 / MMBF5461 / MMBF5462 P-Channel General-Purpose Amplifier

# Description

This device is designed primarily for low level audio and general-purpose applications with high impedance signal sources. Sourced from process 89.



# **Ordering Information**

Part Number	umber Top Mark Package		Packing Method	
MMBF5460	6E	SOT-23 3L	Tape and Reel	
MMBF5461	61U	SOT-23 3L	Tape and Reel	
MMBF5462	61V	SOT-23 3L	Tape and Reel	

# Absolute Maximum Ratings<sup>(1), (2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>DG</sub>	Drain-Gate Voltage	-40	V
V <sub>GS</sub>	Gate-Source Voltage	40	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C

# Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

March 2015

# Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
р	Total Device Dissipation	225	mW
PD	Derate Above 25°C	1.8	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	556	°C/W

Note:

3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

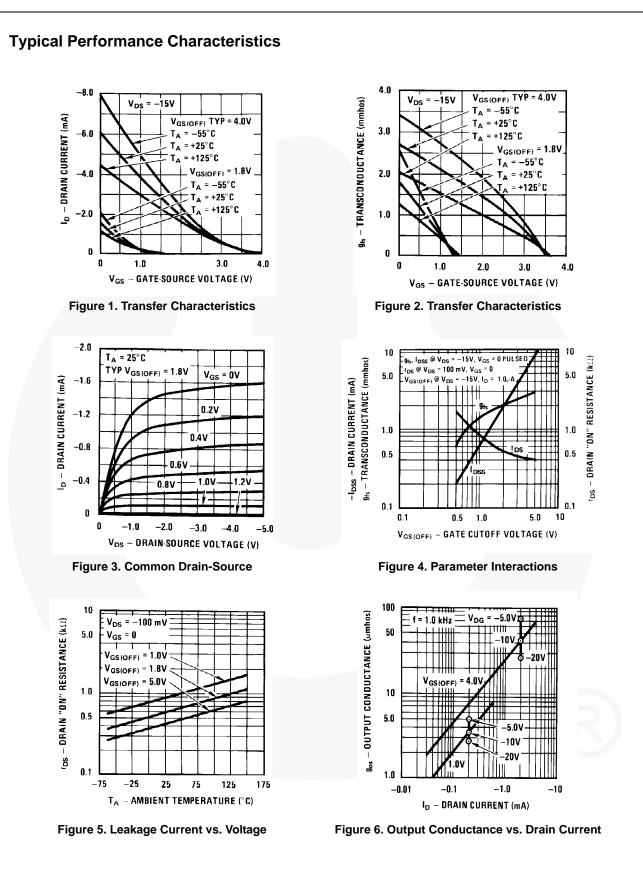
# **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

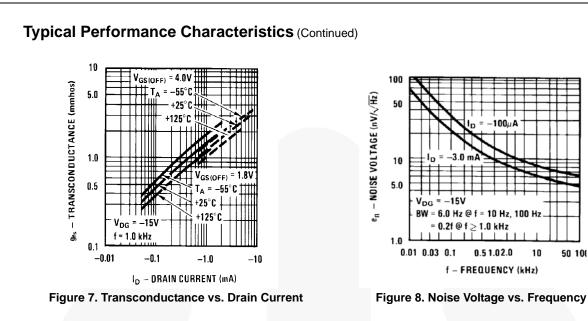
Symbol	Parameter	Conditions	5	Min.	Тур.	Max.	Unit
Off Chara	acteristics						
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_{G} = 10 \ \mu A, \ V_{DS} = 0$		40			V
	Gate Reverse Current	$V_{GS} = 20 \text{ V}, V_{DS} = 0$				5.0	nA
		$V_{GS} = 20 \text{ V}, V_{DS} = 0, T_{A}$	= 100°C			1.0	μA
V <sub>GS(off)</sub>	Gate-Source Cut-Off Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1.0 μA	MMBF5460	0.75		6.0	v
			MMBF5461	1.0		7.5	
			MMBF5462	1.8		9.0	
V <sub>GS</sub> Gate-	Gate-Source Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.1 mA	MMBF5460	0.5		4.0	v
		V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.2 mA	MMBF5461	0.8		4.5	
		V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.4 mA	MMBF5462	1.5		6.0	
On Chara	cteristics		•				
		$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0$ MME	MMBF5460	-1.0		-5.0	mA
	Zero-Gate Voltage Drain Current <sup>(4)</sup>		MMBF5461	-2.0		-9.0	
			MMBF5462	-4.0		-16.0	
Small Sig	nal Characteristics		•				/
g <sub>fs</sub> F	Forward Transfer Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz	MMBF5460	1000		4000	μmhos
			MMBF5461	1500		5000	
			MMBF5462	2000		6000	
9 <sub>os</sub>	Output Conductance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 kHz				75	μmhos
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz			5.0	7.0	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1.0 MHz			1.0	2.0	pF
NF	Noise Figure	$V_{DS}$ = 15 V, $V_{GS}$ = 0, $R_{G}$ = 1.0 MΩ, f = 100 Hz, BW = 1.0 Hz			1.0	2.5	dB
e <sub>n</sub>	Equivalent Short-Circuit Input Noise Voltage	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 100 Hz, BW = 1.0 Hz			60	115	nV/√Hz

## Note:

4. Pulse test: pulse width  $\leq$  300 ms, duty cycle  $\leq$  2.0%



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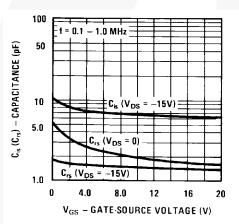
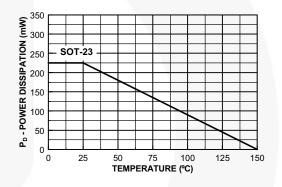
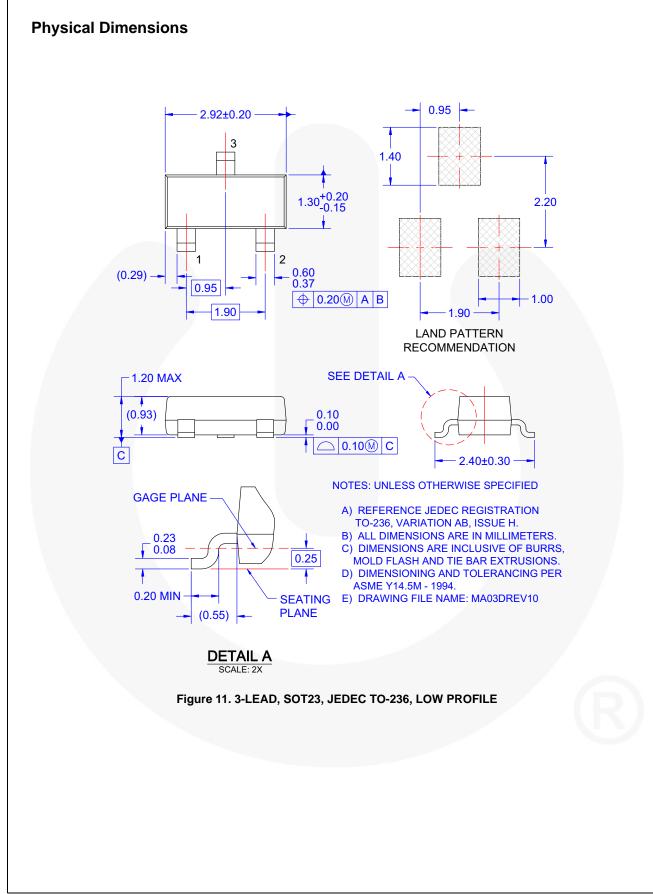


Figure 9. Capacitance vs. Voltage







MMBF5460 / MMBF5461 / MMBF5462

— P-Channel General-Purpose Amplifier

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