

60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| V _{(BR)DSS} | R _{DS(on)} | I _D T _A = 25°C |
|----------------------|---------------------------------------|---|
| -60V | $390\text{m}\Omega$ @ $V_{GS} = -10V$ | -2.3A |
| | 595m $Ω$ @ V _{GS} = -4.5V | -1.9A |

Description and Applications

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor control
- DC-DC Converters
- Power management functions
- Uninterrupted power supply

Features and Benefits

- · Fast switching speed
- Low gate drive
- Low input capacitance
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

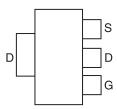
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper lead frame.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.112 grams (approximate)

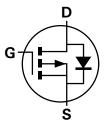
SOT223



Top View



Pin Out - Top View



Equivalent Circuit

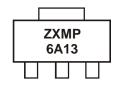
Ordering Information (Note 3)

| Product | Marking | Reel size (inches) | Tape width (mm) | Quantity per reel |
|-------------|----------|--------------------|-----------------|-------------------|
| ZXMP6A13GTA | ZXMP6A13 | 7 | 12 | 1,000 |

Notes:

- 1. No purposefully added lead
- 2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



ZXMP = Product Type Marking Code, Line 1 6A13 = Product Type Marking Code, Line 2





Maximum Ratings @T_A = 25°C unless otherwise specified

| Characteristic | | | Symbol | Value | Unit |
|---|-----------------------|------------------------|-----------------|-------|------|
| Drain-Source voltage | | | V_{DSS} | -60 | V |
| Gate-Source voltage | | V_{GS} | ±20 | V | |
| | | (Note 5) | | -2.3 | |
| Continuous Drain current | $V_{GS} = 10V$ | $T_A = 70$ °C (Note 5) | I_{D} | -1.9 | Α |
| | | (Note 4) | | -1.7 | |
| Pulsed Drain current | V _{GS} = 10V | (Note 6) | I _{DM} | -7.8 | Α |
| Continuous Source current (Body diode) (Note 5) | | I _S | -4.1 | Α | |
| Pulsed Source current (Body diode) (Note 6) | | I _{SM} | -7.8 | А | |

Thermal Characteristics @T_A = 25°C unless otherwise specified

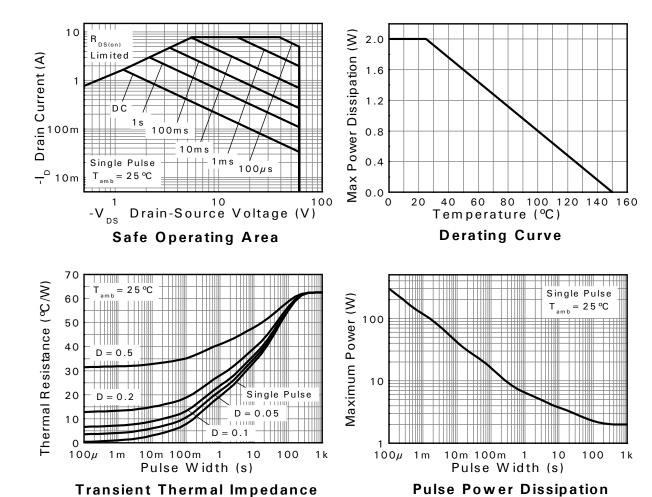
| Characteristic | Symbol | Value | Unit | | |
|---|----------|-----------------------------------|------------|------------|--|
| Power dissipation | (Note 4) | 0 | 2.0 16 | W mW/°C | |
| Linear derating factor | (Note 5) | P _D | 3.9 31 | | |
| Thermal Resistance, Junction to Ambient | (Note 4) | - | 62.5 | | |
| Thermal Resistance, Junction to Ambient | (Note 5) | $R_{\theta JA}$ | 32.0 | °C/W | |
| Thermal Resistance, Junction to Lead | (Note 7) | $R_{	hetaJL}$ | 9.8 | | |
| Operating and storage temperature range | | T _J , T _{STG} | -55 to 150 | °C | |

Notes:

- 4. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 5. Same as note (1), except the device is measured at $t \le 10$ sec.
- 6. Same as note (1), except the device is pulsed with D = 0.02 and pulse width 300µs. The pulse current is limited by the maximum junction temperature.
- 7. Thermal resistance from junction to solder-point (at the end of the drain lead).



Thermal Characteristics







Electrical Characteristics @T_A = 25°C unless otherwise specified

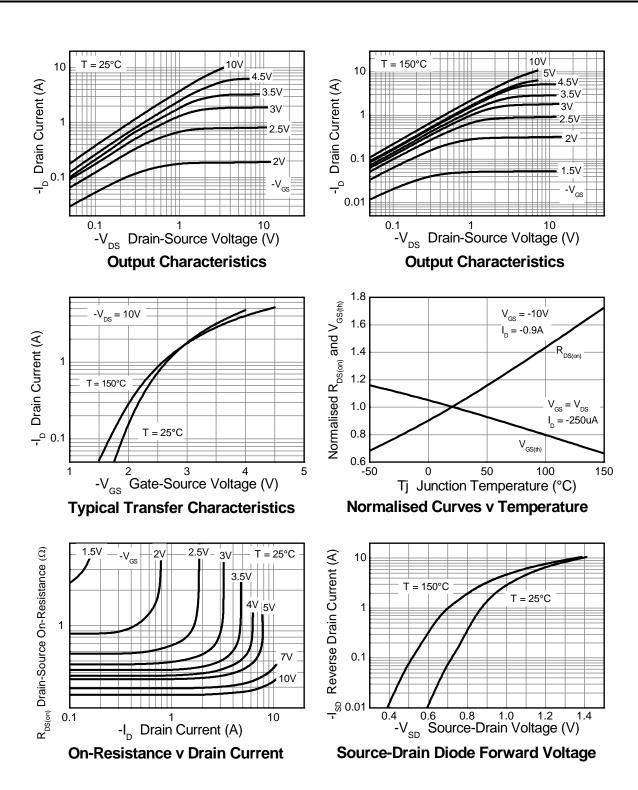
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Cor | ndition |
|--|----------------------|------|-------|-------|------|---|-------------------|
| OFF CHARACTERISTICS | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -60 | | _ | V | $I_D = -250 \mu A, V_{GS} = 0 V$ | |
| Zero Gate Voltage Drain Current | I _{DSS} | _ | _ | -0.5 | μΑ | $V_{DS} = -60V, V_{GS} =$ | : 0V |
| Gate-Source Leakage | I _{GSS} | _ | _ | ±100 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | -1.0 | | -3.0 | V | $I_D = -250 \mu A, V_{DS} =$ | = V _{GS} |
| Ctatic Dunin Course On Desigtance (Note 0) | | | | 0.390 | Ω | $V_{GS} = -10V, I_{D} = -0$ | 0.9A |
| Static Drain-Source On-Resistance (Note 8) | R _{DS} (ON) | _ | _ | 0.595 | Ω | $V_{GS} = -4.5V, I_{D} = -4.5V$ | 0.8A |
| Forward Transconductance (Notes 8 & 9) | 9 _{fs} | _ | 1.8 | _ | S | $V_{DS} = -15V, I_{D} = -0$ |).9A |
| Diode Forward Voltage (Note 8) | V _{SD} | | -0.85 | -0.95 | V | I _S = -0.8A, V _{GS} = 0V, T _J = 25°C | |
| Reverse recovery time (Note 9) | t _{rr} | | 21.1 | _ | ns | $I_S = -0.9A$, di/dt = ' | 100A/μs, |
| Reverse recovery charge (Note 9) | Q _{rr} | | 19.3 | _ | nC | T _J = 25°C | |
| DYNAMIC CHARACTERISTICS (Note 9) | | | | | | | |
| Input Capacitance | C _{iss} | _ | 219 | _ | pF | ., | 0) / |
| Output Capacitance | Coss | _ | 25.7 | _ | pF | V _{DS} = -30V, V _{GS} = -f = 1MHz | : UV |
| Reverse Transfer Capacitance | C _{rss} | _ | 20.5 | _ | pF | | |
| Total Gate Charge (Note 10) | Qq | | 2.9 | _ | nC | $V_{GS} = -4.5V$ | |
| Total Gate Charge (Note 10) | Qq | _ | 5.9 | _ | nC | $V_{DS} = -30V$ $I_{D} = -0.9A$ | |
| Gate-Source Charge (Note 10) | Q _{qs} | | 0.74 | _ | nC | | |
| Gate-Drain Charge (Note 10) | Q_{gd} | _ | 1.5 | _ | nC | | |
| Turn-On Delay Time (Note 10) | t _{D(on)} | _ | 1.6 | _ | ns | V_{DD} = -30V, V_{GS} = -10V I_D = -1A, $R_G \cong 6.0\Omega$ | |
| Turn-On Rise Time (Note 10) | t _r | | 2.2 | _ | ns | | |
| Turn-Off Delay Time (Note 10) | t _{D(off)} | _ | 11.2 | _ | ns | | |
| Turn-Off Fall Time (Note 10) | t _f | | 5.7 | _ | ns | | |

Notes:

- 8. Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$ 9. For design aid only, not subject to production testing. 10. Switching characteristics are independent of operating junction temperatures.

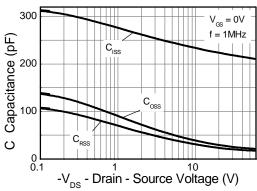


Typical Characteristics

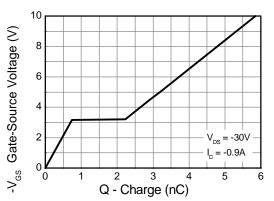




Typical Characteristics - continued

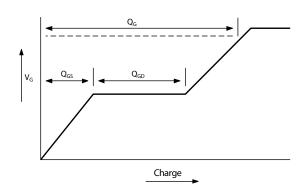


Capacitance v Drain-Source Voltage

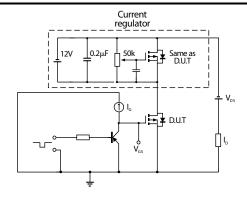


Gate-Source Voltage v Gate Charge

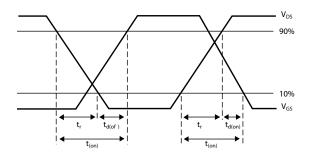
Test Circuits



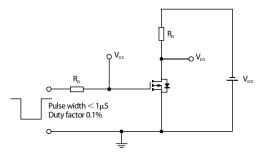
Basic gate charge waveform



Gate charge test circuit



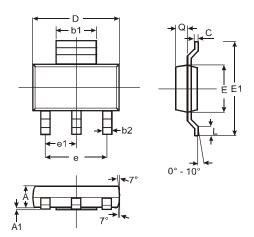
Switching time waveforms



Switching time test circuit

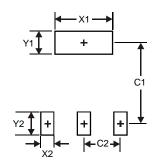


Package Outline Dimensions



| SOT223 | | | | | |
|----------------------|-------|------|------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 1.55 | 1.65 | 1.60 | | |
| A1 | 0.010 | 0.15 | 0.05 | | |
| b1 | 2.90 | 3.10 | 3.00 | | |
| b2 | 0.60 | 0.80 | 0.70 | | |
| С | 0.20 | 0.30 | 0.25 | | |
| D | 6.45 | 6.55 | 6.50 | | |
| Е | 3.45 | 3.55 | 3.50 | | |
| E1 | 6.90 | 7.10 | 7.00 | | |
| е | _ | _ | 4.60 | | |
| e1 | _ | _ | 2.30 | | |
| L | 0.85 | 1.05 | 0.95 | | |
| Q | 0.84 | 0.94 | 0.89 | | |
| All Dimensions in mm | | | | | |

Suggested Pad Layout



| Dimensions | Value (in mm) |
|------------|---------------|
| X1 | 3.3 |
| X2 | 1.2 |
| Y1 | 1.6 |
| Y2 | 1.6 |
| C1 | 6.4 |
| C2 | 2.3 |





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