

## 1:5/1:7 Clock Buffer for Networking Applications

### Features

- High Frequency >156 MHz
- High-speed, low-noise, non-inverting buffer
  - PI49FCT3802 is 1:5 buffer
  - PI49FCT3803 is 1:7 buffer
- Low-skew (<250ps) between any two output clocks
- Low duty cycle distortion <250ps
- Low propagation delay <2.5ns
- 5V Tolerant input
- Multiple V<sub>DD</sub>, GND pins for noise reduction
- 3.3V supply voltage
- Packaging (Pb-free & Green available):
  - 16-pin TSSOP (L)
  - 16-pin QSOP (Q)

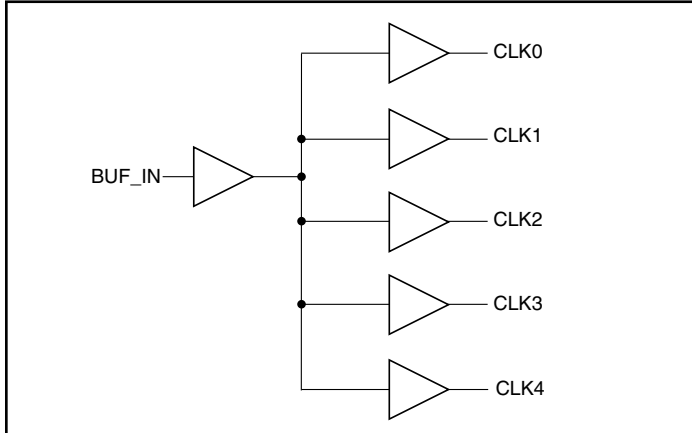
### Description

The PI49FCT380x is a 3.3V compatible, high-speed, low-noise non-inverting clock buffer. The key goal in designing the PI49FCT380x is to target networking applications that require low-skew, low-jitter, and high-frequency clock distribution. Providing output-to-output skew as low as 250ps, the PI49FCT380x is an ideal clock distribution device for synchronous systems. Designing synchronous networking systems requires a tight level of skew from a large number of outputs.

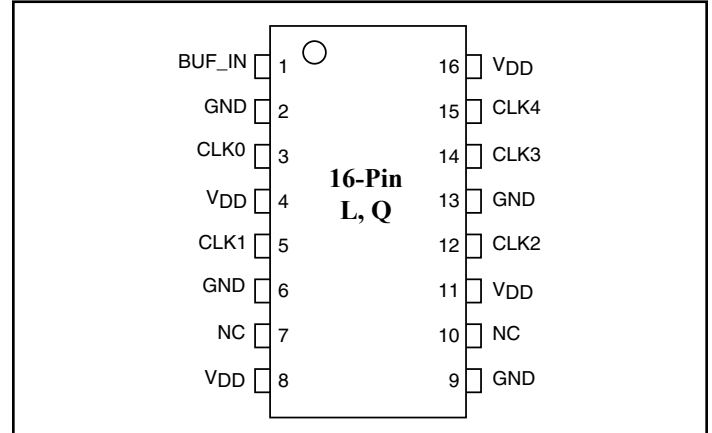
### Pin Description

Pin Name		Description
3802	3803	
BUF_IN	BUF_IN	Input
CLK[0:4]	CLK[0:4]	Outputs
GND	GND	GND
V <sub>DD</sub>	V <sub>DD</sub>	Power

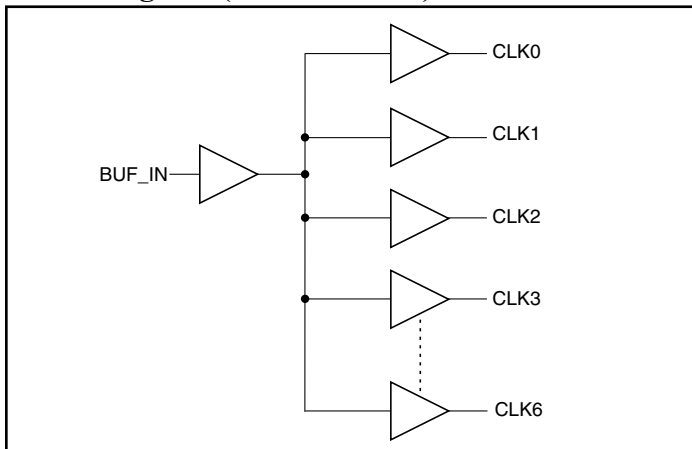
### Block Diagram (PI49FCT3802)



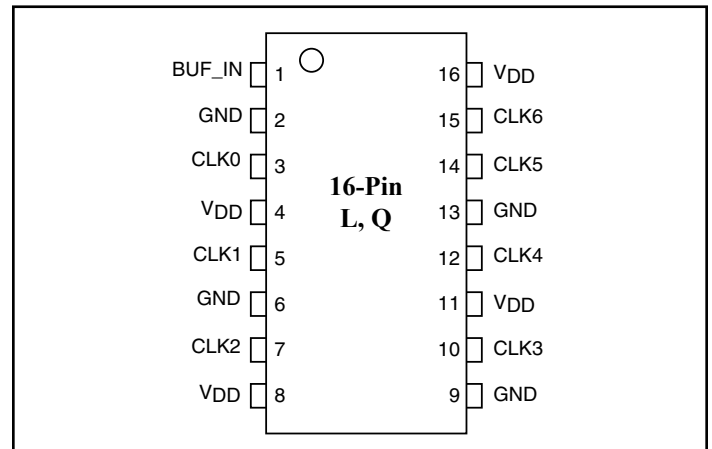
### Pin Configuration (PI49FCT3802)



### Block Diagram (PI49FCT3803)



### Pin Configuration (PI49FCT3803)



## Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature.....	-65°C to +150°C
V <sub>DD</sub> Voltage .....	-0.5V to 5.5V
Output Voltage.....	-0.5V to 5.5V
Input Voltage .....	-0.5V to 5.5V
DC Output Current.....	-60mA to +60mA
Power Dissipation .....	500mW

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## Operating Range

V <sub>DD</sub> Voltage .....	3.3V ± 0.3V
Commercial Temperature .....	0°C to +70°C
Industrial Temperature .....	-40°C to +85°C
Input Frequency .....	DC to 156 MHz
Capacitive Loading .....	10pF to 50pF

## DC Electrical Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ. <sup>(2)</sup>	Max.	Units
V <sub>IH</sub>	Input HIGH Voltage	Guaranteed Logic HIGH Level (Input Pins)	2.0		5.5	V
V <sub>IL</sub>	Input LOW Voltage	Guaranteed Logic LOW Level (Input Pins)	-0.5		0.8	
I <sub>IH</sub>	Input HIGH Current	V <sub>DD</sub> = Max.      V <sub>IN</sub> = V <sub>DD</sub>			1	µA
I <sub>IL</sub>	Input LOW Current	V <sub>DD</sub> = Max.      V <sub>IN</sub> = GND			-1	
V <sub>IK</sub>	Clamp Diode Voltage	V <sub>DD</sub> = Min., I <sub>IN</sub> = -18mA		-0.7	-1.2	V
V <sub>OH</sub>	Output HIGH Voltage	V <sub>CC</sub> = Min., V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -0.1mA	V <sub>DD</sub> -0.2		
			I <sub>OH</sub> = -12mA	2.4 <sup>(3)</sup>	3.0	
V <sub>OL</sub>	Output LOW Voltage	V <sub>CC</sub> = Min., V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = 0.1mA		0.2	
			I <sub>OH</sub> = 12mA		0.3	0.5
I <sub>OH</sub>	Output HIGH Current	V <sub>DD</sub> = 3.0V, V <sub>IN</sub> = V <sub>IH</sub> OR V <sub>IL</sub> , V <sub>OUT</sub> = 1.5V <sup>(4)</sup>	-45	-75	-180	mA
I <sub>OL</sub>	Output LOW Current	V <sub>DD</sub> = 3.0V, V <sub>IN</sub> = V <sub>IH</sub> OR V <sub>IL</sub> , V <sub>OUT</sub> = 1.5V <sup>(4)</sup>	50	92	200	

### Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V<sub>DD</sub> = 3.3V, +25°C ambient and maximum loading.
- V<sub>OH</sub> = V<sub>DD</sub> - 0.6V at rated current.
- This parameter is determined by device characterization but is not production tested.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

### Power Supply Characteristics

Parameters	Description	Test Conditions		Min.	Typ. <sup>(2)</sup>	Max.	Units
I <sub>DDQ</sub>	Quiescent Power Supply Current	V <sub>DD</sub> = Max.	V <sub>IN</sub> = GND or V <sub>DD</sub>		0.1	30	μA
ΔI <sub>DD</sub>	Supply Current per Inputs @ TTL High	V <sub>DD</sub> = Max.	V <sub>IN</sub> = V <sub>DD</sub> - 0.6V(3)		47	300	
I <sub>DD</sub>	Dynamic Supply Current	V <sub>DD</sub> = 3.6V, No load	50 MHz		43		mA
			67 MHz		56		
			80 MHz		66		
			100 MHz		81		
			125 MHz		97		
			156 Mhz		121		

#### Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V<sub>DD</sub> = 3.3V, +25°C ambient.
- Per TTL driven input (V<sub>IN</sub> = V<sub>DD</sub> - 0.6V); all other inputs at V<sub>DD</sub> or GND.

### Capacitance (T<sub>A</sub> = 25°C, f = 1 MHz)

Parameters <sup>(1)</sup>	Description	Test Conditions	Typ	Max.	Units
C <sub>IN</sub>	Input Capacitance	V <sub>IN</sub> = 0V	3.0	4	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>OUT</sub> = 0V	—	6	

#### Note:

- This parameter is determined by device characterization but is not production tested.

### Switching Characteristics (V<sub>DD</sub> = 3.3V ± 0.3V, T<sub>A</sub> = 85°C)

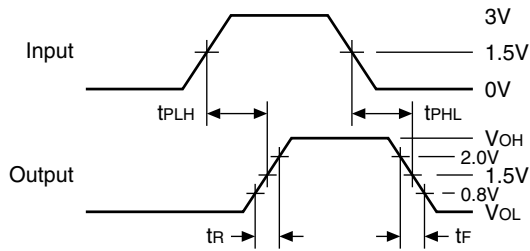
Parameters	Description	Test Conditions	Min.	Typ.	Max.	Units
t <sub>R</sub> /t <sub>F</sub>	CLK <sub>n</sub> Rise/Fall Time 0.8V ~ 2.0V	C <sub>L</sub> = 15pF, 125 Mhz		0.7	1.0	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay BUF_IN to CLK <sub>n</sub>		1.0	2.2	2.5	
t <sub>sk(o)</sub> <sup>(3)</sup>	Skew between two outputs of the same package (same transition)			110	250	ps
t <sub>sk(p)</sub> <sup>(3)</sup>	Skew between opposite transitions (t <sub>PHL</sub> - t <sub>PLH</sub> ) of the same output			200	250	
t <sub>sk(t)</sub> <sup>(3)</sup>	Skew between two outputs of different packages <sup>(4)</sup>				0.55	ns

#### Notes:

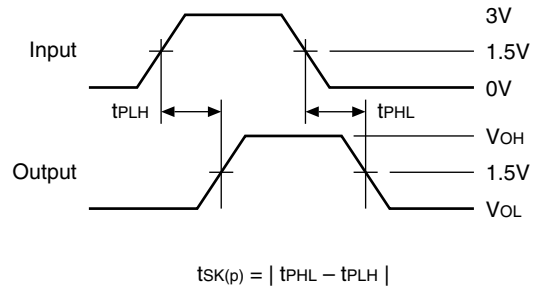
- See test circuit and waveforms.
- Minimum limits are guaranteed but not tested on Propagation Delays.
- Skew measured at worse cast temperature (max. temp).
- Identical conditions: loading, transitions, supply voltage, temperature, package type and speed grade.

## Switching Waveforms

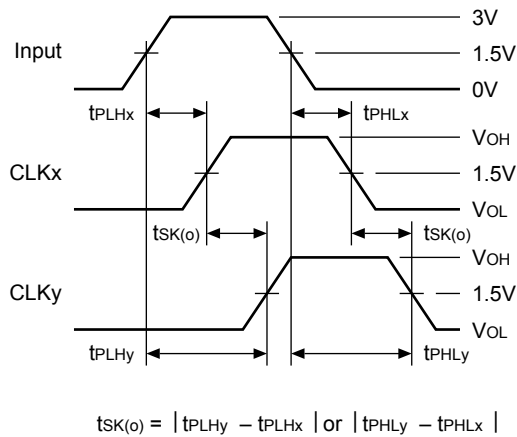
### Propagation Delay



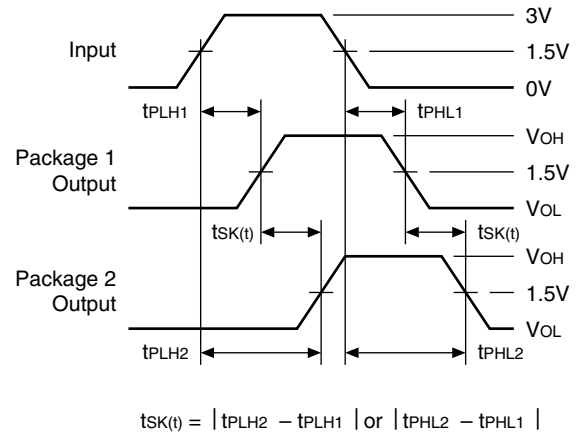
### Pulse Skew – $t_{SK(P)}$



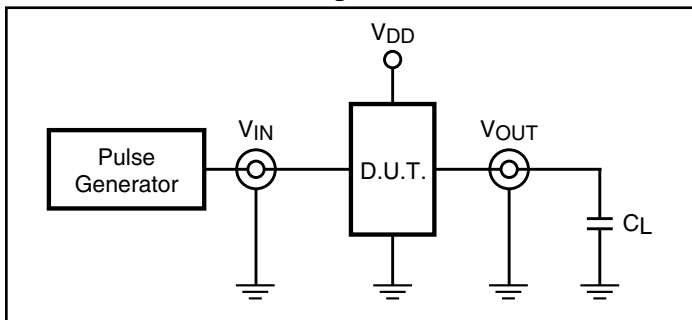
### Output Skew – $t_{SK(O)}$



### Package Skew – $t_{SK(T)}$



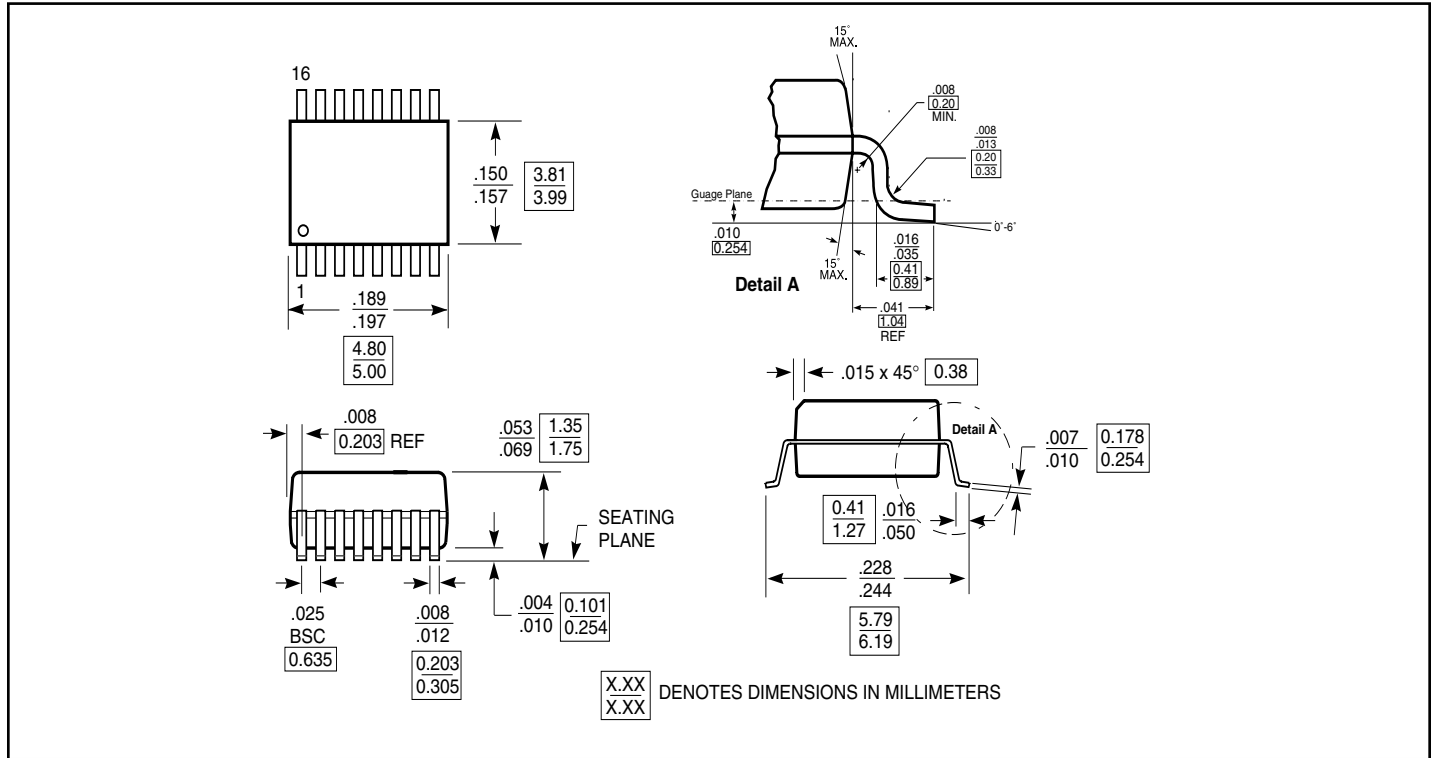
## Test Circuits for All Outputs



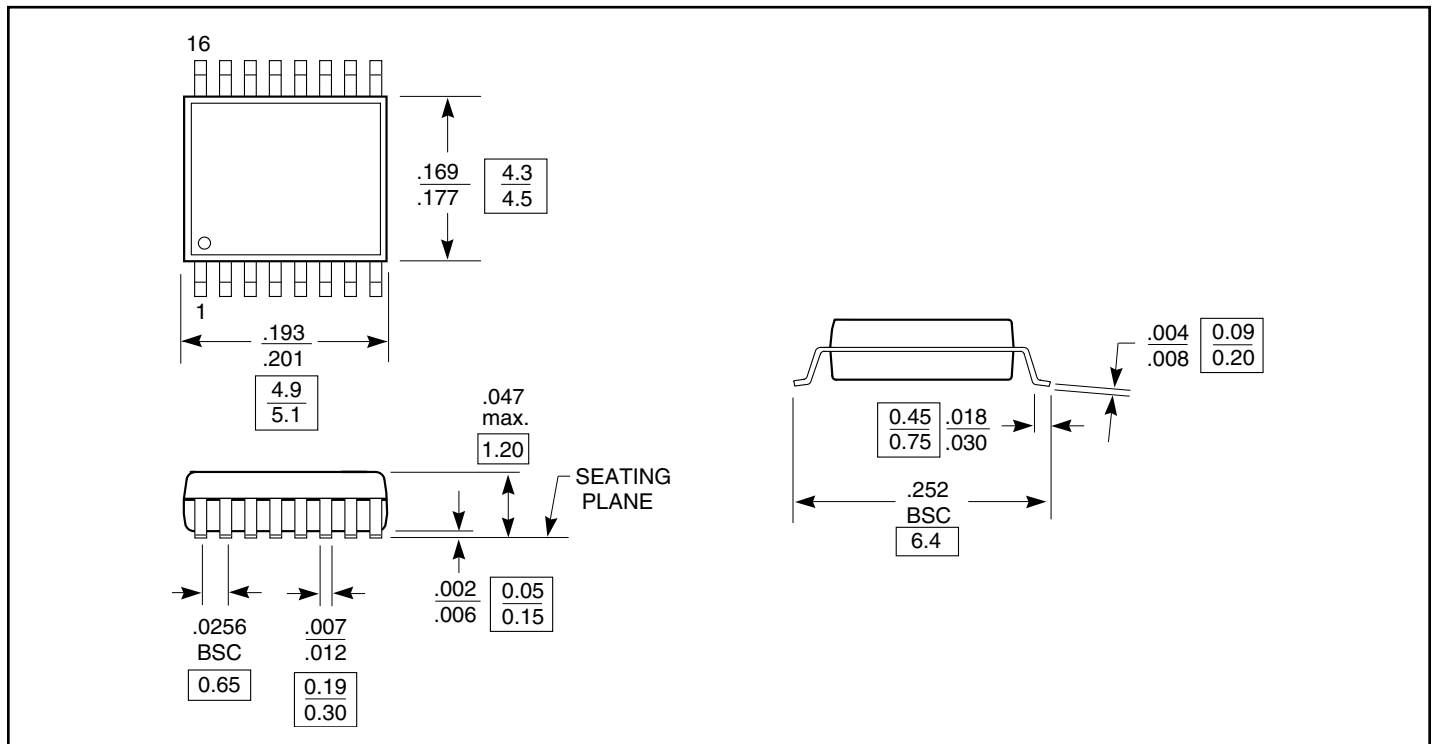
### Definitions:

$C_L$  = Load capacitance: includes jig and probe capacitance.  
 $R_T$  = Termination resistance, should be equal to  $Z_{out}$  of the Pulse Generator.

**Packaging Mechanical: 16-Pin QSOP (Q)**



**Packaging Mechanical: 16-Pin TSSOP (L)**



**Ordering Information**

<b>Ordering Code</b>	<b>Package Code</b>	<b>Package Description</b>
PI49FCT3802L	L	16-pin TSSOP
PI49FCT3802LE	L	Pb-free & Green, 16-pin TSSOP
PI49FCT3802Q	Q	16-pin QSOP
PI49FCT3802QE	Q	Pb-free & Green, 16-pin QSOP
PI49FCT3803L	L	16-pin TSSOP
PI49FCT3803LE	L	Pb-free & Green, 16-pin TSSOP
PI49FCT3803Q	Q	16-pin QSOP
PI49FCT3803QE	Q	Pb-free & Green, 16-pin QSOP

**Notes:**

1. Thermal characteristics can be found on the web at [www.pericom.com/packaging/](http://www.pericom.com/packaging/)



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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

**Телефон:** 8 (812) 309 58 32 (многоканальный)

**Факс:** 8 (812) 320-02-42

**Электронная почта:** [org@eplast1.ru](mailto:org@eplast1.ru)

**Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.