

Glass Passivated Junction Rectifier



FEATURES

- Superectifier structure for high reliability application
- Cavity-free glass-passivated junction
- Low forward voltage drop
- Low leakage current, typical I_R less than 0.1 μA
- High forward surge capability
- Meets environmental standard MIL-S-19500
- Solder dip 275 °C max. 10 s, per JESD 22-B102
- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



RoHS
COMPLIANT

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes application

MECHANICAL DATA

Case: DO-204AC, molded epoxy over glass body
Molding compound meets UL 94 V-0 flammability rating
Base P/N-E3 - RoHS compliant, commercial grade
Base P/NHE3 - RoHS compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102
E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	1.5 A
V_{RRM}	50 V to 1000 V
I_{FSM}	50 A
I_R	5.0 μA
V_F	1.4 V
T_J max.	175 °C

MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise noted) ⁽¹⁾											
PARAMETER	SYMBOL	1N53 91GP	1N53 92GP	1N53 93GP	1N53 94GP	1N53 95GP	1N53 96GP	1N53 97GP	1N53 98GP	1N53 99GP	UNIT
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	200	300	400	500	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	210	280	350	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	300	400	500	600	800	1000	V
Maximum average forward rectified current 0.375" (9.5 mm) lead length at $T_L = 70$ °C	$I_{F(AV)}$	1.5									A
Peak forward surge current 8.3 ms single half sine-wave super-imposed on rated load	I_{FSM}	50									A
Maximum full load reverse current, full cycle average 0.375" (9.5 mm) lead length at $T_A = 70$ °C	$I_{R(AV)}$	300									μA
Operating junction and storage temperature range	T_J, T_{STG}	- 65 to + 175									°C

Note

⁽¹⁾ JEDEC registered values

1N5391GP thru 1N5399GP

Vishay General Semiconductor



ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)													
PARAMETER	TEST CONDITIONS		SYMBOL	1N53 91GP	1N53 92GP	1N53 93GP	1N53 94GP	1N53 95GP	1N53 96GP	1N53 97GP	1N53 98GP	1N53 99GP	UNIT
Maximum instantaneous forward voltage	1.5 A	T _A = 70 °C	V _F ⁽¹⁾	1.4									V
Maximum DC reverse current at rated DC blocking voltage		T _A = 25 °C	I _R ⁽¹⁾	5.0									μA
		T _A = 150 °C		300									
Typical reverse recovery time	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		t _{rr}	2.0									μs
Typical junction capacitance	4.0 V, 1 MHz		C _J	15									pF

Note

(1) JEDEC registered values

THERMAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)												
PARAMETER	SYMBOL		1N53 91GP	1N53 92GP	1N53 93GP	1N53 94GP	1N53 95GP	1N53 96GP	1N53 97GP	1N53 98GP	1N53 99GP	UNIT
Typical thermal resistance	$R_{\theta JA}^{(1)}$		45									$^{\circ}\text{C/W}$

Note

(1) Thermal resistance from junction to ambient at 0.375" (9.5 mm) lead length, P.C.B. mounted

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
1N5397GP-E3/54	0.425	54	4000	13" diameter paper tape and reel
1N5397GP-E3/73	0.425	73	2000	Ammo pack packaging
1N5397GPHE3/54 (1)	0.425	54	4000	13" diameter paper tape and reel
1N5397GPHE3/73 (1)	0.425	73	2000	Ammo pack packaging

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES

($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

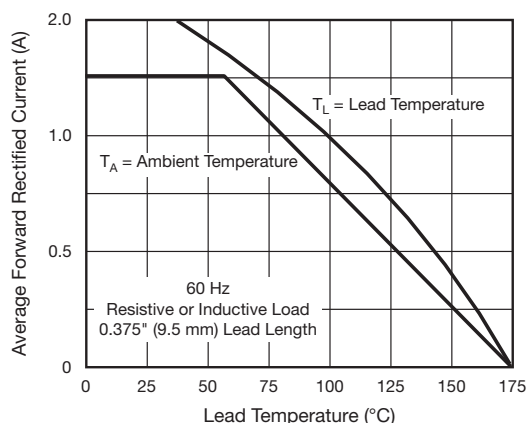


Fig. 1 - Forward Current Derating Curve

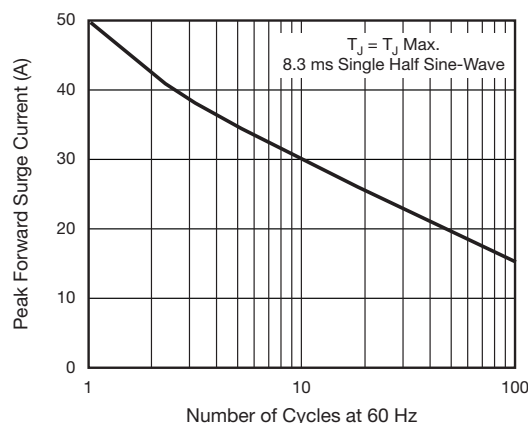


Fig. 2 - Maximum Non-repetitive Peak Forward Surge Current

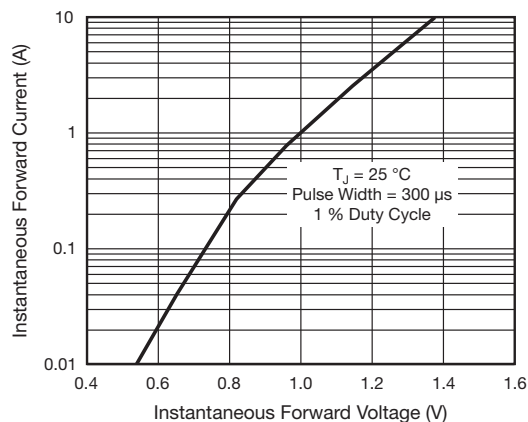


Fig. 3 - Typical Instantaneous Forward Characteristics

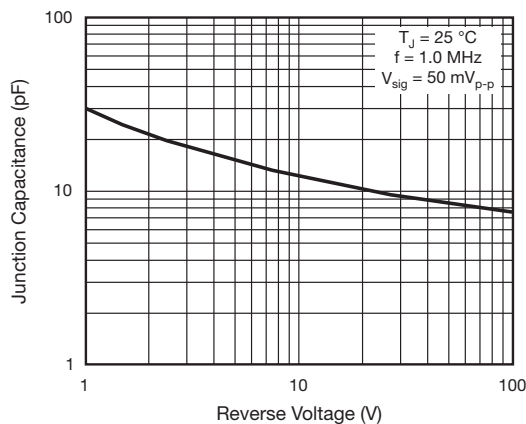


Fig. 5 - Typical Junction Capacitance

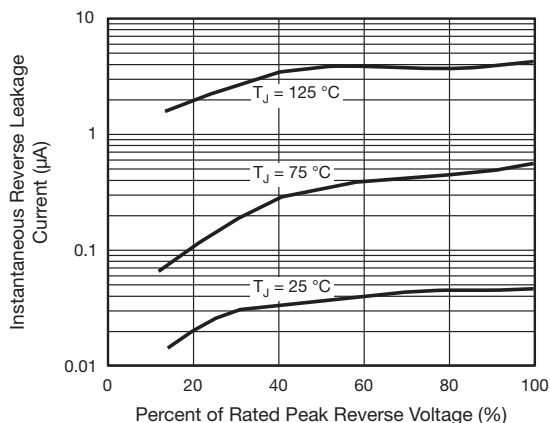


Fig. 4 - Typical Reverse Characteristics

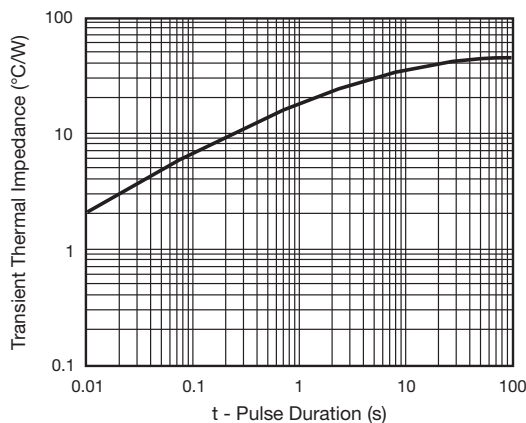
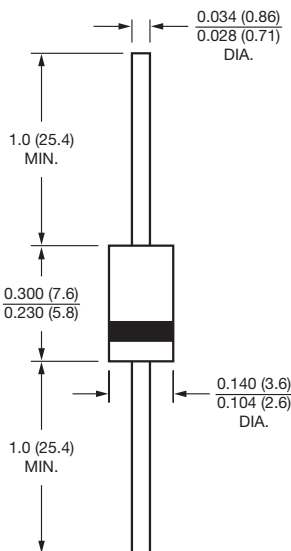


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

DO-204AC (DO-15)





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- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
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