BGU8007

SiGe:C Low Noise Amplifier MMIC for GPS, GLONASS, Galileo and Compass

Rev. 2 — 30 March 2012

Product data sheet

1. Product profile

1.1 General description

The BGU8007 is a Low Noise Amplifier (LNA) for GNSS receiver applications in a plastic leadless 6-pin, extremely small SOT886 package. The BGU8007 requires only one external matching inductor and one external decoupling capacitor.

The BGU8007 adapts itself to the changing environment resulting from co-habitation of different radio systems in modern cellular handsets. It has been designed for low power consumption and optimal performance when jamming signals from co-existing cellular transmitters are present. At low jamming power levels it delivers 19 dB gain at a noise figure of 0.75 dB. During high jamming power levels, resulting for example from a cellular transmit burst, it temporarily increases its bias current to improve sensitivity.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features and benefits

- Covers full GNSS L1 band, from 1559 MHz to 1610 MHz
- Noise figure (NF) = 0.75 dB
- Excellent low NF < 1 dB in the presence of strong jamming signals</p>
- Gain 19 dB
- High input 1 dB compression point P_{i(1dB)} of -11 dBm
- High out of band IP3_i of 4 dBm
- Supply voltage 1.5 V to 2.5 V
- Power-down mode current consumption < 1 μA</p>
- Optimized performance at low supply current of 4.6 mA
- Integrated temperature stabilized bias for easy design
- Requires only one input matching inductor and one supply decoupling capacitor
- Input and output DC decoupled
- ESD protection on all pins (HBM > 2 kV)
- Integrated matching for the output
- Small 6-pin leadless package 1 mm × 1.45 mm × 0.5 mm
- 110 GHz transit frequency SiGe:C technology



1.3 Applications

LNA for GPS, GLONASS, Galileo and Compass (BeiDou) in smart phones, feature phones, tablet PCs, Personal Navigation Devices, Digital Still Cameras, Digital Video Cameras, RF Front End modules, complete GPS chipset modules and theft protection (laptop, ATM)

1.4 Quick reference data

Table 1.Quick reference data

f = 1559 MHz to 1610 MHz; V_{CC} = 1.8 V; P_i < -40 dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

	····· , ·· · · · · · · · · · · · · · ·						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CC}	supply voltage	RF input AC coupled		1.5	-	2.5	V
I _{CC}	supply current	$V_{\text{ENABLE}} \ge 0.8 \text{ V}$					
		P _i < -40 dBm		3.3	4.6	5.9	mA
		$P_i = -20 \text{ dBm}$		8.4	11.9	14.7	mA
G _p	power gain	P _i < –40 dBm, no jammer		17.0	19.0	20.5	dB
		$P_i = -20 \text{ dBm}$, no jammer		18.5	20.5	21.5	dB
NF	noise figure	P _i < –40 dBm, no jammer	[1]	-	0.75	1.1	dB
		P _i < –40 dBm, no jammer	[2]	-	0.80	1.2	dB
		$P_i = -20 \text{ dBm}$, no jammer	[2]	-	1.0	1.4	dB
P _{i(1dB)}	input power at 1 dB	f = 1559 MHz to 1610 MHz					
	gain compression	V _{CC} = 1.5 V		-15	-12	-	dBm
		V _{CC} = 1.8 V		-14	-11	-	dBm
		$V_{CC} = 2.2 V$		-13	-10	-	dBm
IP3 _i	input third-order intercept point	f = 1.575 GHz					
		V _{CC} = 1.5 V	[3]	1	4	-	dBm
		V _{CC} = 1.8 V	[3]	1	4	-	dBm
		$V_{CC} = 2.2 V$	[3]	2	5	-	dBm

[1] PCB losses are subtracted.

[2] Including PCB losses.

[3] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_1 = P_2 = -30 \text{ dBm}$.

2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	GND	a 5 4	
2	GND		$\overset{4}{\sim}$
3	RF_IN		3-6
4	V _{CC}		
5	ENABLE		2 1 sym129
6	RF_OUT	1 2 3 Transparent top view	<i></i>

3. Ordering information

Table 3. Ordering information					
Type number Package					
	Name	Description	Version		
BGU8007	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body $1 \times 1.45 \times 0.5$ mm	SOT886		

4. Marking

Table 4. Marking codes	
Type number	Marking code
BGU8007	UZ

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage	RF input AC coupled	-0.5	+4.0	V
V _{ENABLE}	voltage on pin ENABLE	$V_{\text{ENABLE}} < V_{\text{CC}} + 0.6$	<u>[2]</u> –0.5	+4.0	V
$V_{RF_{IN}}$	voltage on pin RF_IN	DC; $V_{RF_{IN}} < V_{CC} + 0.6$	<u>[2][3]</u> –0.5	+4.0	V
V_{RF_OUT}	voltage on pin RF_OUT	DC; $V_{RF_{OUT}} < V_{CC} + 0.6$	<u>[2][3]</u> –0.5	+4.0	V
Pi	input power		-	0	dBm
P _{tot}	total power dissipation	$T_{sp} \le 130 \ ^{\circ}C$	<u>[1]</u>	55	mW
T _{stg}	storage temperature		-65	+150	°C
Т _ј	junction temperature		-	150	°C
V _{ESD}	electrostatic discharge voltage	Human Body Model (HBM); According JEDEC standard 22-A114E	-	2	kV
		Charged Device Model (CDM); According JEDEC standard 22-C101B	-	1	kV

[1] T_{sp} is the temperature at the soldering point of the emitter lead.

- [2] Warning: due to internal ESD diode proctection, the applied DC voltage should not exceed V_{CC} + 0.6 and shall not exceed 5.0 V in order to avoid excess current.
- [3] The RF input and RF output are AC coupled through internal DC blocking capacitors.

6. Thermal characteristics

Table 6.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		225	K/W

7. Characteristics

Table 7. Characteristics

f = 1559 MHz to 1610 MHz; $V_{CC} = 1.8 \text{ V}$; $V_{ENABLE} >= 0.8 \text{ V}$; $P_i < -40 \text{ dBm}$; $T_{amb} = 25 \text{ °C}$; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CC}	supply voltage	RF input AC coupled		1.5	-	2.5	V
I _{CC}	supply current	$V_{\text{ENABLE}} \ge 0.8 \text{ V}$					
		P _i < -40 dBm		3.3	4.6	5.9	mA
		$P_i = -20 \text{ dBm}$		8.4	11.9	14.7	mA
		$V_{\text{ENABLE}} \leq 0.3 \text{ V}$		-	-	1	μA
T _{amb}	ambient temperature			-40	+25	+85	°C
G _p	power gain	T _{amb} = 25 °C					
		P _i < −40 dBm, no jammer		17.0	19.0	20.5	dB
		P _i = −20 dBm, no jammer		18.5	20.5	21.5	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$		18.5	20.5	21.5	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$		18.5	20.5	21.5	dB
		$-40 \ ^\circ C \le T_{amb} \le +85 \ ^\circ C$					
		P _i < −40 dBm, no jammer		16	-	21	dB
		$P_i = -20 \text{ dBm}$, no jammer		17	-	22	dB
	$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$		17	-	22	dB	
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$		17	-	22	dB
RL _{in}	input return loss	$P_i < -40 \text{ dBm}$		4	5.5	-	dB
		$P_i = -20 \text{ dBm}$		6	9	-	dB
RL _{out}	output return loss	$P_i < -40 \text{ dBm}$		15	26	-	dB
		$P_i = -20 \text{ dBm}$		15	27	-	dB
ISL	isolation			22	24	-	dB
NF	noise figure	T _{amb} = 25 °C					
		P _i < −40 dBm, no jammer	<u>[1]</u>	-	0.75	1.1	dB
		P _i < −40 dBm, no jammer	[2]	-	0.80	1.2	dB
		$P_i = -20 \text{ dBm}$, no jammer	[2]	-	1.0	1.4	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$	[2]	-	0.8	1.2	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$	[2]	-	1.1	1.5	dB
		$-40 \text{ °C} \le T_{amb} \le +85 \text{ °C}$					
		P _i < −40 dBm, no jammer	[2]	-	-	1.5	dB
		P _i = −20 dBm, no jammer	[2]	-	-	1.7	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$	[2]	-	-	1.5	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$	[2]	-	-	1.8	dB

Table 7. Characteristics ...continued

f = 1559 MHz to 1610 MHz; $V_{CC} = 1.8 \text{ V}$; $V_{ENABLE} >= 0.8 \text{ V}$; $P_i < -40 \text{ dBm}$; $T_{amb} = 25 \text{ °C}$; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
P _{i(1dB)}	input power at 1 dB gain compression	f = 1559 MHz to 1610 MHz				
		V _{CC} = 1.5 V	-15	-12	-	dBm
		V _{CC} = 1.8 V	-14	-11	-	dBm
	$V_{CC} = 2.2 V$	-13	-10	-	dBm	
		f = 806 MHz to 928 MHz				
		V _{CC} = 1.5 V	<u>[3]</u> –13	-10	-	dBm
		V _{CC} = 1.8 V	<u>[3]</u> –13	-10	-	dBm
		$V_{CC} = 2.2 V$	<u>[3]</u> –12	-9	-	dBm
		f = 1612 MHz to 1909 MHz				
		V _{CC} = 1.5 V	<u>[3]</u> –12	-9	-	dBm
		V _{CC} = 1.8 V	<u>[3]</u> –11	-8	-	dBm
		$V_{CC} = 2.2 V$	<u>[3]</u> –10	-7	-	dBm
IP3 _i	input third-order intercept point	f = 1.575 GHz				
		V _{CC} = 1.5 V	<u>[4]</u> 1	4	-	dBm
		V _{CC} = 1.8 V	<u>[4]</u> 1	4	-	dBm
		V _{CC} = 2.2 V	<u>[4]</u> 2	5	-	dBm
t _{on}	turn-on time		<u>[5]</u> _	-	2	μs
t _{off}	turn-off time		[5] _	-	1	μs

[1] PCB losses are subtracted.

[2] Including PCB losses.

[3] Out of band.

[4] $f_1 = 1713 \text{ MHz}; f_2 = 1851 \text{ MHz}; P_1 = P_2 = -30 \text{ dBm}.$

[5] Within 10 % of the final gain.

Table 8.ENABLE (pin 5)

-40 °C \leq T_{amb} \leq +85 °C; 1.5 V \leq V_{CC} \leq 2.5 V

V _{ENABLE} (V)	State
≤ 0.3	OFF
≥ 0.8	ON

8. Application information

8.1 GNSS LNA

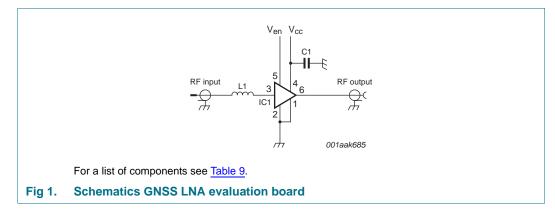
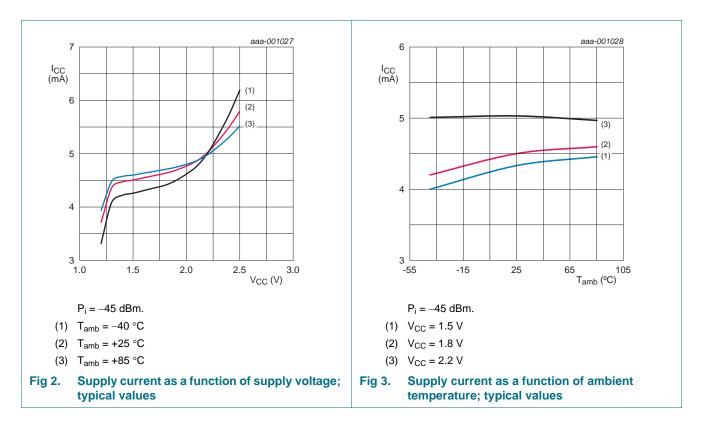


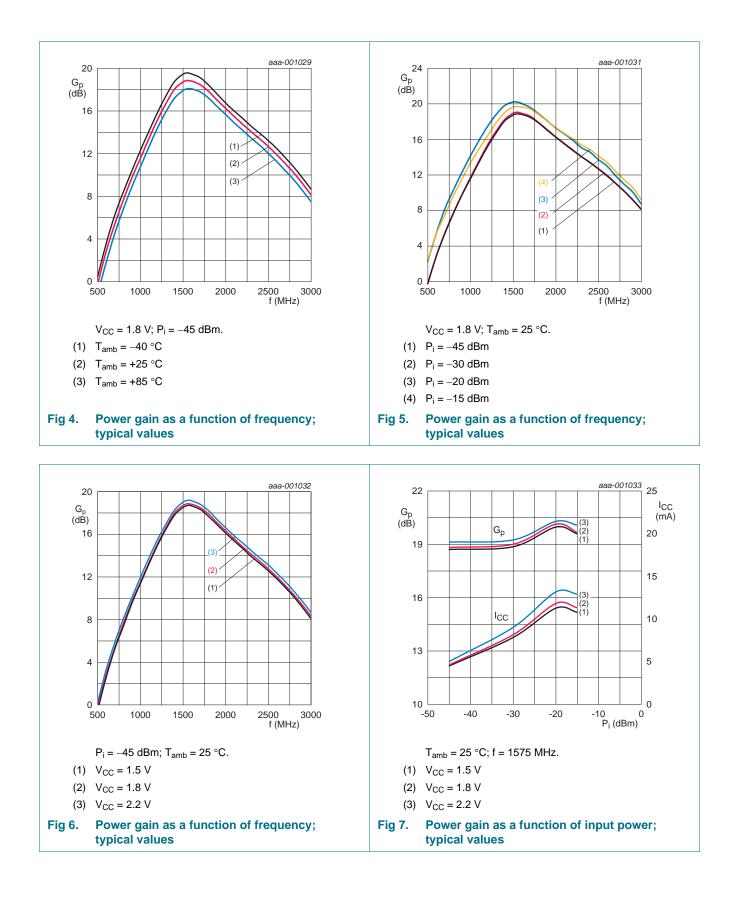
Table 9. List of components

For schematics see Figure 1.

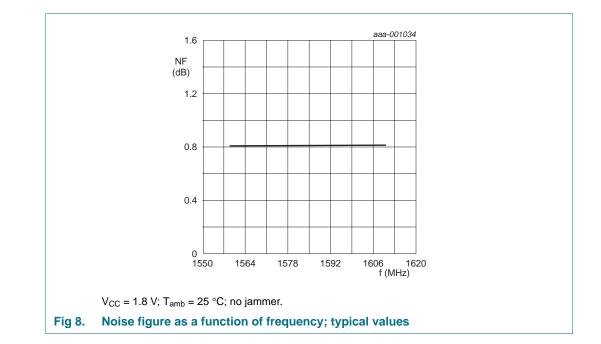
Component	Description	Value	Supplier	Remarks
C1	decoupling capacitor	1 nF	various	
IC1	BGU8007	-	NXP	
L1	high quality matching inductor	5.6 nH	Murata LQW15A	

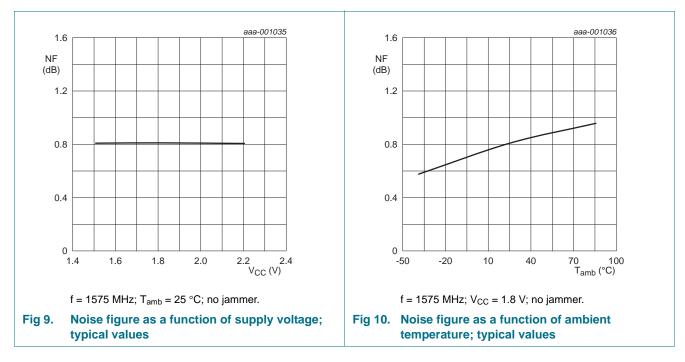


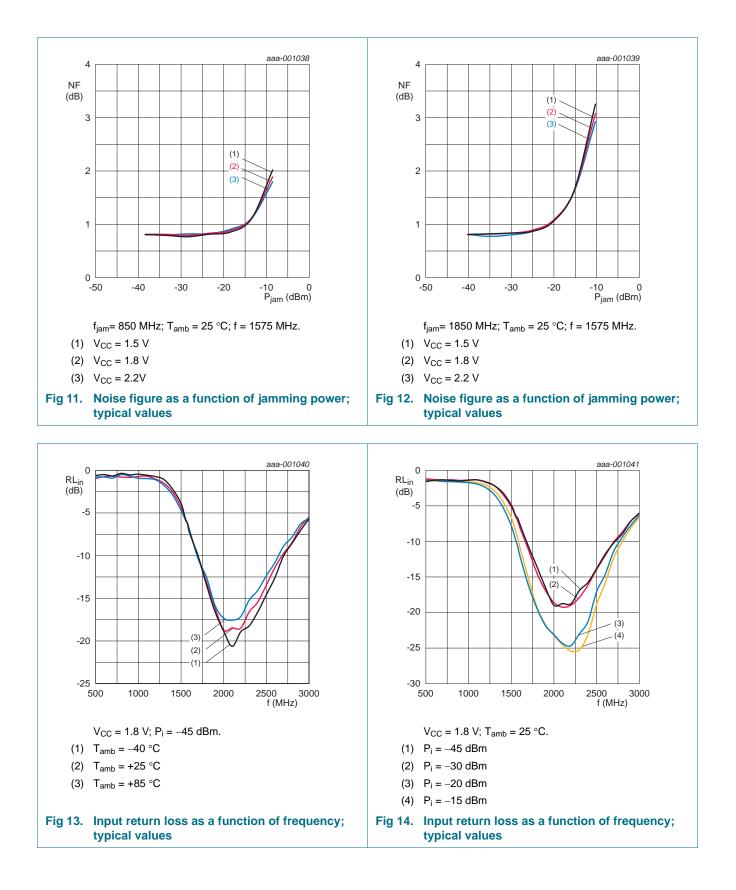
BGU8007

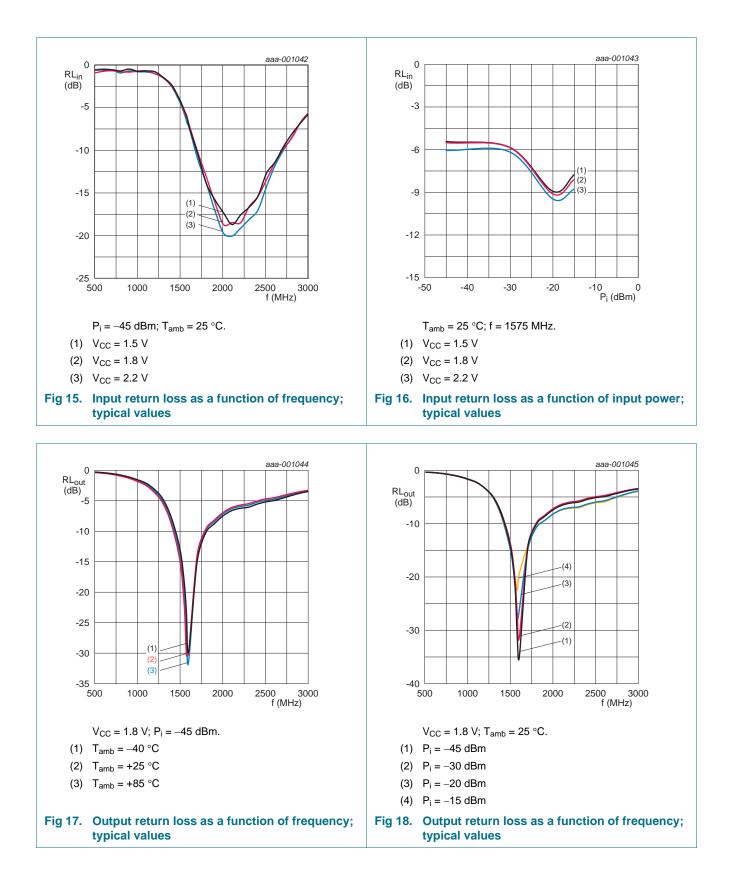


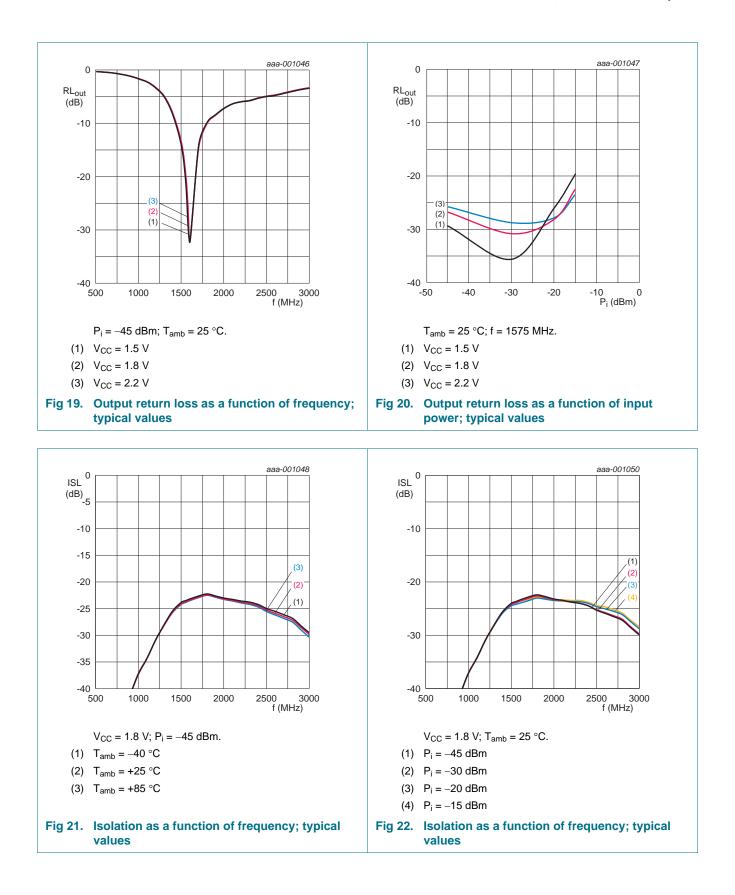
Product data sheet

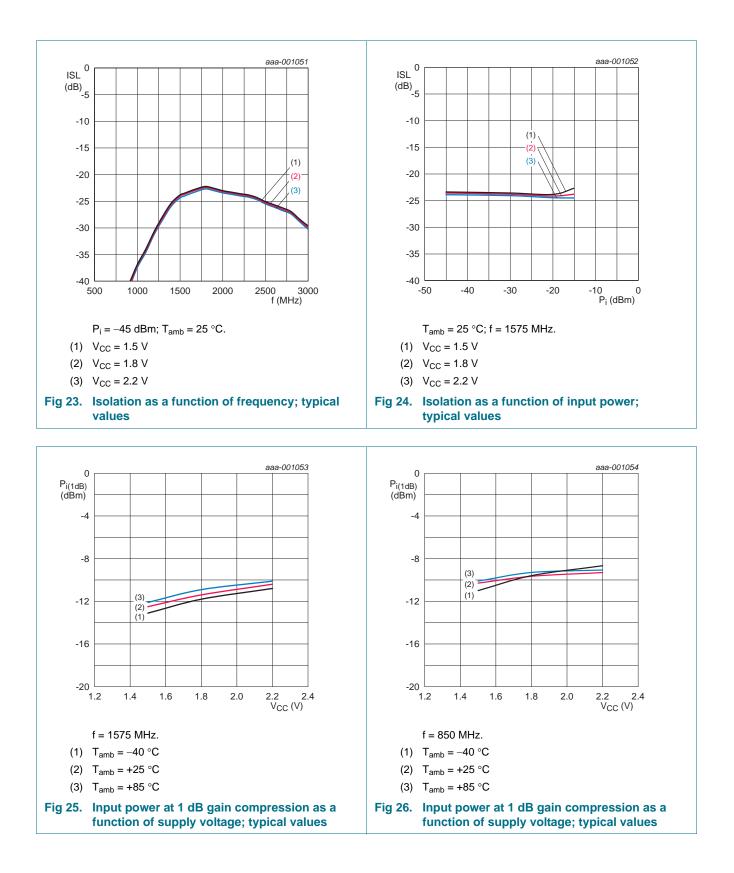


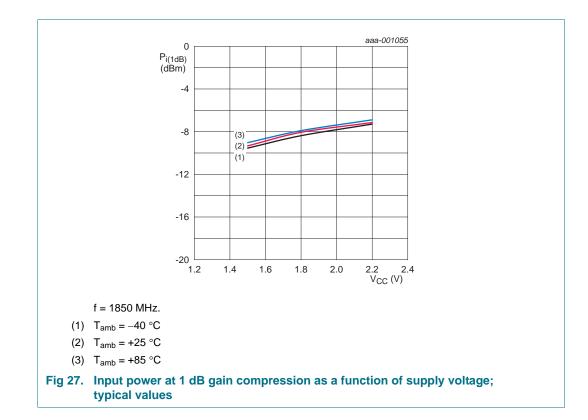


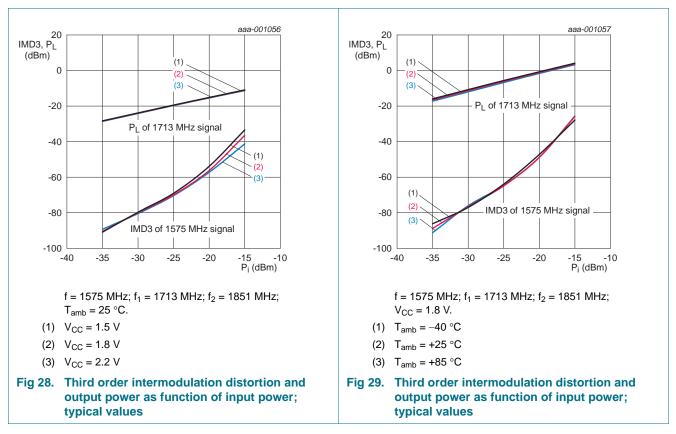




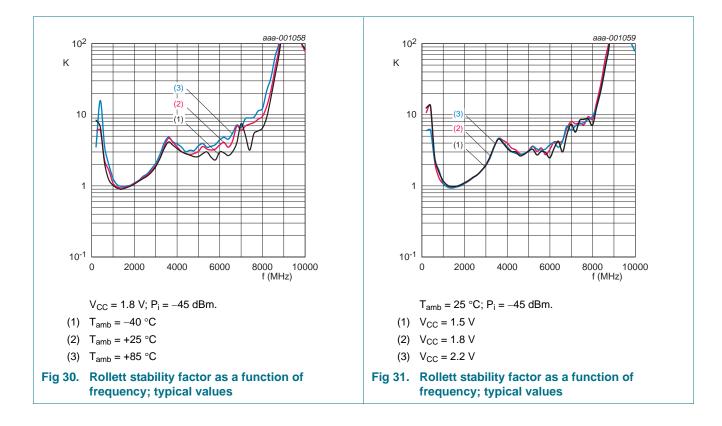








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9. Package outline

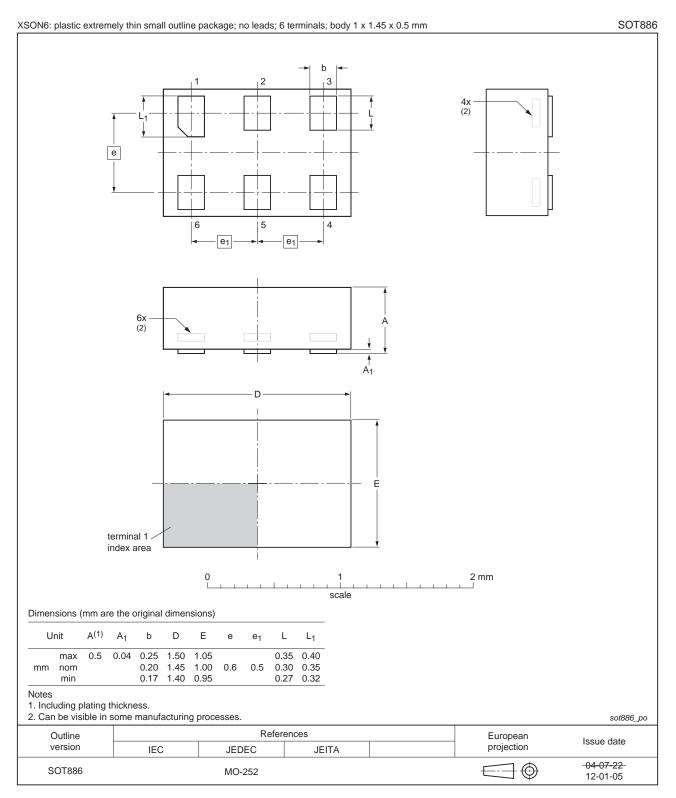


Fig 32. Package outline SOT886 (XSON6)

10. Abbreviations

Acronym	Description
AC	Alternating Current
ATM	Automated Teller Machine (cash dispenser)
DC	Direct Current
GLONASS	GLObal NAvigation Satellite System
GNSS	Global Navigation Satellite System
GPS	Global Positioning System
HBM	Human Body Model
MMIC	Monolithic Microwave Integrated Circuit
PC	Personal Computer
PCB	Printed Circuit Board
RF	Radio Frequency
SiGe:C	Silicon Germanium Carbon

11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BGU8007 v.2	20120330	Product data sheet	-	BGU8007 v.1		
Modifications:	 Added 'Compass' 	to descriptive title				
	 Section 1.3 on page 	<u>e 2</u> : added 'Compass' to text				
	 Section 1.2 on page 	. <u>2 on page 1</u> : row 7, changed 2.2 V to 2.5 V				
	• Table 1 on page 2:	changed max. value V_{CC} from 2	.2 V to 2.5 V			
	• Table 7 on page 4:	changed max. value V_{CC} from 2	.2 V to 2.5 V			
	 <u>Table 8 on page 5</u>: changed max. value V_{CC} from 2.2 V to 2.5 V 					
	• Table 5 on page 3:	 <u>Table 5 on page 3</u>: Several additions and changes 				
BGU8007 v.1	20111011	Product data sheet	-	-		

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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