

# 74AC240, 74ACT240

## Octal Buffer/Line Driver with 3-STATE Outputs

### Features

- $I_{CC}$  and  $I_{OZ}$  reduced by 50%
- Inverting 3-STATE outputs drive bus lines or buffer memory address registers
- Outputs source/sink 24mA
- ACT240 has TTL-compatible inputs

### General Description

The AC/ACT240 is an octal buffer and line driver designed to be employed as a memory address driver, clock driver and bus oriented transmitter or receiver which provides improved PC board density.

### Ordering Information

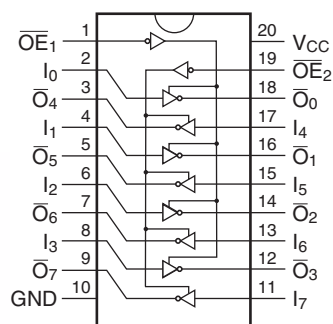
| Order Number | Package Number | Package Description                                                         |
|--------------|----------------|-----------------------------------------------------------------------------|
| 74AC240SC    | M20B           | 20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300" Wide  |
| 74AC240SJ    | M20D           | 20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide               |
| 74AC240MTC   | MTC20          | 20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide |
| 74AC240PC    | N20A           | 20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide      |
| 74ACT240SC   | M20B           | 20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300" Wide  |
| 74ACT240SJ   | M20D           | 20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide               |
| 74ACT240MTC  | MTC20          | 20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide |
| 74ACT240PC   | N20A           | 20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide      |

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.



All packages are lead free per JEDEC: J-STD-020B standard.

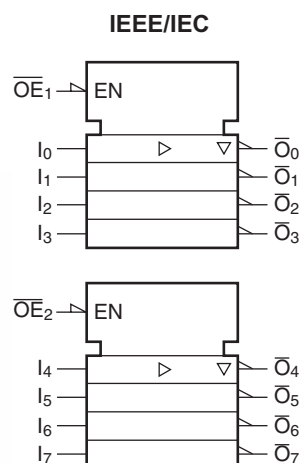
# Connection Diagram



# Pin Description

| Pin Names                          | Description                  |
|------------------------------------|------------------------------|
| $\overline{OE}_1, \overline{OE}_2$ | 3-STATE Output Enable Inputs |
| $I_0-I_7$                          | Inputs                       |
| $\overline{O}_0-\overline{O}_7$    | Outputs                      |

# Logic Symbol



# Truth Tables

| Inputs            |       | Outputs<br>(Pins 12, 14, 16, 18) |
|-------------------|-------|----------------------------------|
| $\overline{OE}_1$ | $I_n$ |                                  |
| L                 | L     | H                                |
| L                 | H     | L                                |
| H                 | X     | Z                                |

| Inputs            |       | Outputs<br>(Pins 3, 5, 7, 9) |
|-------------------|-------|------------------------------|
| $\overline{OE}_2$ | $I_n$ |                              |
| L                 | L     | H                            |
| L                 | H     | L                            |
| H                 | X     | Z                            |

H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

Z = High Impedance

## Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

| Symbol                | Parameter                                    | Rating                   |
|-----------------------|----------------------------------------------|--------------------------|
| $V_{CC}$              | Supply Voltage                               | −0.5V to +7.0V           |
| $I_{IK}$              | DC Input Diode Current<br>$V_I = -0.5V$      | −20mA                    |
|                       | $V_I = V_{CC} + 0.5$                         | +20mA                    |
| $V_I$                 | DC Input Voltage                             | −0.5V to $V_{CC} + 0.5V$ |
| $I_{OK}$              | DC Output Diode Current<br>$V_O = -0.5V$     | −20mA                    |
|                       | $V_O = V_{CC} + 0.5V$                        | +20mA                    |
| $V_O$                 | DC Output Voltage                            | −0.5V to $V_{CC} + 0.5V$ |
| $I_O$                 | DC Output Source or Sink Current             | ±50mA                    |
| $I_{CC}$ or $I_{GND}$ | DC $V_{CC}$ or Ground Current per Output Pin | ±50mA                    |
| $T_{STG}$             | Storage Temperature                          | −65°C to +150°C          |
| $T_J$                 | Junction Temperature                         | 140°C                    |

## Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

| Symbol                | Parameter                                                                                                  | Rating         |
|-----------------------|------------------------------------------------------------------------------------------------------------|----------------|
| $V_{CC}$              | Supply Voltage<br>AC                                                                                       | 2.0V to 6.0V   |
|                       | ACT                                                                                                        | 4.5V to 5.5V   |
| $V_I$                 | Input Voltage                                                                                              | 0V to $V_{CC}$ |
| $V_O$                 | Output Voltage                                                                                             | 0V to $V_{CC}$ |
| $T_A$                 | Operating Temperature                                                                                      | −40°C to +85°C |
| $\Delta V / \Delta t$ | Minimum Input Edge Rate, AC Devices:<br>$V_{IN}$ from 30% to 70% of $V_{CC}$ , $V_{CC}$ @ 3.3V, 4.5V, 5.5V | 125mV/ns       |
| $\Delta V / \Delta t$ | Minimum Input Edge Rate, ACT Devices:<br>$V_{IN}$ from 0.8V to 2.0V, $V_{CC}$ @ 4.5V, 5.5V                 | 125mV/ns       |

## DC Electrical Characteristics for AC

| Symbol                         | Parameter                                     | V <sub>CC</sub> (V) | Conditions                                                                                                                               | T <sub>A</sub> = +25°C |                   | T <sub>A</sub> = −40°C to +85°C |  | Units |
|--------------------------------|-----------------------------------------------|---------------------|------------------------------------------------------------------------------------------------------------------------------------------|------------------------|-------------------|---------------------------------|--|-------|
|                                |                                               |                     |                                                                                                                                          | Typ.                   | Guaranteed Limits |                                 |  |       |
| V <sub>IH</sub>                | Minimum HIGH Level Input Voltage              | 3.0                 | V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> − 0.1V                                                                                        | 1.5                    | 2.1               | 2.1                             |  | V     |
|                                |                                               | 4.5                 |                                                                                                                                          | 2.25                   | 3.15              | 3.15                            |  |       |
|                                |                                               | 5.5                 |                                                                                                                                          | 2.75                   | 3.85              | 3.85                            |  |       |
| V <sub>IL</sub>                | Maximum LOW Level Input Voltage               | 3.0                 | V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> − 0.1V                                                                                        | 1.5                    | 0.9               | 0.9                             |  | V     |
|                                |                                               | 4.5                 |                                                                                                                                          | 2.25                   | 1.35              | 1.35                            |  |       |
|                                |                                               | 5.5                 |                                                                                                                                          | 2.75                   | 1.65              | 1.65                            |  |       |
| V <sub>OH</sub>                | Minimum HIGH Level Output Voltage             | 3.0                 | I <sub>OUT</sub> = −50μA                                                                                                                 | 2.99                   | 2.9               | 2.9                             |  | V     |
|                                |                                               | 4.5                 |                                                                                                                                          | 4.49                   | 4.4               | 4.4                             |  |       |
|                                |                                               | 5.5                 |                                                                                                                                          | 5.49                   | 5.4               | 5.4                             |  |       |
|                                |                                               | 3.0                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OH</sub> = −12mA                                                           |                        | 2.56              | 2.46                            |  |       |
|                                |                                               | 4.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OH</sub> = −24mA                                                           |                        | 3.86              | 3.76                            |  |       |
|                                |                                               | 5.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OH</sub> = −24mA <sup>(1)</sup>                                            |                        | 4.86              | 4.76                            |  |       |
| V <sub>OL</sub>                | Maximum LOW Level Output Voltage              | 3.0                 | I <sub>OUT</sub> = 50μA                                                                                                                  | 0.002                  | 0.1               | 0.1                             |  | V     |
|                                |                                               | 4.5                 |                                                                                                                                          | 0.001                  | 0.1               | 0.1                             |  |       |
|                                |                                               | 5.5                 |                                                                                                                                          | 0.001                  | 0.1               | 0.1                             |  |       |
|                                |                                               | 3.0                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OL</sub> = 12mA                                                            |                        | 0.36              | 0.44                            |  |       |
|                                |                                               | 4.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OL</sub> = 24mA                                                            |                        | 0.36              | 0.44                            |  |       |
|                                |                                               | 5.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OL</sub> = 24mA <sup>(1)</sup>                                             |                        | 0.36              | 0.44                            |  |       |
| I <sub>IN</sub> <sup>(2)</sup> | Maximum Input Leakage Current                 | 5.5                 | V <sub>I</sub> = V <sub>CC</sub> , GND                                                                                                   |                        | ±0.1              | ±1.0                            |  | μA    |
| I <sub>OZ</sub>                | Maximum 3-STATE Leakage Current               | 5.5                 | V <sub>I</sub> (OE) = V <sub>IL</sub> , V <sub>IH</sub> ; V <sub>I</sub> = V <sub>CC</sub> , GND; V <sub>O</sub> = V <sub>CC</sub> , GND |                        | ±0.25             | ±2.5                            |  | μA    |
| I <sub>OLD</sub>               | Minimum Dynamic Output Current <sup>(3)</sup> | 5.5                 | V <sub>OLD</sub> = 1.65V Max.                                                                                                            |                        |                   | 75                              |  | mA    |
| I <sub>OHD</sub>               |                                               | 5.5                 | V <sub>OHD</sub> = 3.85V Min.                                                                                                            |                        |                   | -75                             |  | mA    |
| I <sub>CC</sub> <sup>(2)</sup> | Maximum Quiescent Supply Current              | 5.5                 | V <sub>IN</sub> = V <sub>CC</sub> or GND                                                                                                 |                        | 4.0               | 40.0                            |  | μA    |

## Notes:

1. All outputs loaded; thresholds on input associated with output under test.
2.  $I_{IN}$  and  $I_{CC}$  @ 3.0V are guaranteed to be less than or equal to the respective limit @ 5.5V  $V_{CC}$ .
3. Maximum test duration 2.0ms, one output loaded at a time.

## DC Electrical Characteristics for ACT

| Symbol           | Parameter                                     | V <sub>CC</sub> (V) | Conditions                                                                                    | T <sub>A</sub> = +25°C |                   | T <sub>A</sub> = −40°C to +85°C |  | Units |
|------------------|-----------------------------------------------|---------------------|-----------------------------------------------------------------------------------------------|------------------------|-------------------|---------------------------------|--|-------|
|                  |                                               |                     |                                                                                               | Typ.                   | Guaranteed Limits |                                 |  |       |
| V <sub>IH</sub>  | Minimum HIGH Level Input Voltage              | 4.5                 | V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> − 0.1V                                             | 1.5                    | 2.0               | 2.0                             |  | V     |
|                  |                                               | 5.5                 |                                                                                               | 1.5                    | 2.0               | 2.0                             |  |       |
| V <sub>IL</sub>  | Maximum LOW Level Input Voltage               | 4.5                 | V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> − 0.1V                                             | 1.5                    | 0.8               | 0.8                             |  | V     |
|                  |                                               | 5.5                 |                                                                                               | 1.5                    | 0.8               | 0.8                             |  |       |
| V <sub>OH</sub>  | Minimum HIGH Level Output Voltage             | 4.5                 | I <sub>OUT</sub> = −50μA                                                                      | 4.49                   | 4.4               | 4.4                             |  | V     |
|                  |                                               | 5.5                 |                                                                                               | 5.49                   | 5.4               | 5.4                             |  |       |
|                  |                                               | 4.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OH</sub> = −24mA                |                        | 3.86              | 3.76                            |  |       |
|                  |                                               | 5.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OH</sub> = −24mA <sup>(4)</sup> |                        | 4.86              | 4.76                            |  |       |
| V <sub>OL</sub>  | Maximum LOW Level Output Voltage              | 4.5                 | I <sub>OUT</sub> = 50μA                                                                       | 0.001                  | 0.1               | 0.1                             |  | V     |
|                  |                                               | 5.5                 |                                                                                               | 0.001                  | 0.1               | 0.1                             |  |       |
|                  |                                               | 4.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OL</sub> = 24mA                 |                        | 0.36              | 0.44                            |  |       |
|                  |                                               | 5.5                 | V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> , I <sub>OL</sub> = 24mA <sup>(4)</sup>  |                        | 0.36              | 0.44                            |  |       |
| I <sub>IN</sub>  | Maximum Input Leakage Current                 | 5.5                 | V <sub>I</sub> = V <sub>CC</sub> , GND                                                        |                        | ±0.1              | ±1.0                            |  | μA    |
| I <sub>OZ</sub>  | Maximum 3-STATE Leakage Current               | 5.5                 | V <sub>I</sub> = V <sub>IL</sub> , V <sub>IH</sub> ; V <sub>O</sub> = V <sub>CC</sub> , GND   |                        | ±0.25             | ±2.5                            |  | μA    |
| I <sub>CCT</sub> | Maximum I <sub>CC</sub> /Input                | 5.5                 | V <sub>I</sub> = V <sub>CC</sub> − 2.1V                                                       | 0.6                    |                   | 1.5                             |  | mA    |
| I <sub>OLD</sub> | Minimum Dynamic Output Current <sup>(5)</sup> | 5.5                 | V <sub>OLD</sub> = 1.65V Max.                                                                 |                        |                   | 75                              |  | mA    |
| I <sub>OHD</sub> |                                               | 5.5                 | V <sub>OHD</sub> = 3.85V Min.                                                                 |                        |                   | −75                             |  | mA    |
| I <sub>CC</sub>  | Maximum Quiescent Supply Current              | 5.5                 | V <sub>IN</sub> = V <sub>CC</sub> or GND                                                      |                        | 4.0               | 40.0                            |  | μA    |

**Notes:**

4. All outputs loaded; thresholds on input associated with output under test.  
 5. Maximum test duration 2.0ms, one output loaded at a time.

**AC Electrical Characteristics for AC**

| Symbol    | Parameter                           | $V_{CC}$ (V) <sup>(6)</sup> | $T_A = +25^\circ\text{C}$ ,<br>$C_L = 50\text{pF}$ |      |      | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ ,<br>$C_L = 50\text{pF}$ |      | Units |
|-----------|-------------------------------------|-----------------------------|----------------------------------------------------|------|------|--------------------------------------------------------------------------|------|-------|
|           |                                     |                             | Min.                                               | Typ. | Max. | Min.                                                                     | Max. |       |
| $t_{PLH}$ | Propagation Delay<br>Data to Output | 3.3                         | 1.5                                                | 6.0  | 8.0  | 1.0                                                                      | 9.0  | ns    |
|           |                                     | 5.0                         | 1.5                                                | 4.5  | 6.5  | 1.0                                                                      | 7.0  |       |
| $t_{PHL}$ | Propagation Delay<br>Data to Output | 3.3                         | 1.5                                                | 5.5  | 8.0  | 1.0                                                                      | 8.5  | ns    |
|           |                                     | 5.0                         | 1.5                                                | 4.5  | 6.0  | 1.0                                                                      | 6.5  |       |
| $t_{PZH}$ | Output Enable Time                  | 3.3                         | 1.5                                                | 6.0  | 10.5 | 1.0                                                                      | 11.0 | ns    |
|           |                                     | 5.0                         | 1.5                                                | 5.0  | 7.0  | 1.0                                                                      | 8.0  |       |
| $t_{PZL}$ | Output Enable Time                  | 3.3                         | 1.5                                                | 7.0  | 10.0 | 1.0                                                                      | 11.0 | ns    |
|           |                                     | 5.0                         | 1.5                                                | 5.5  | 8.0  | 1.0                                                                      | 8.5  |       |
| $t_{PHZ}$ | Output Disable Time                 | 3.3                         | 1.5                                                | 7.0  | 10.0 | 1.0                                                                      | 10.5 | ns    |
|           |                                     | 5.0                         | 1.5                                                | 6.5  | 9.0  | 1.0                                                                      | 9.5  |       |
| $t_{PLZ}$ | Output Disable Time                 | 3.3                         | 1.5                                                | 7.5  | 10.5 | 1.0                                                                      | 11.5 | ns    |
|           |                                     | 5.0                         | 1.5                                                | 6.5  | 9.0  | 1.0                                                                      | 9.5  |       |

**Note:**

6. Voltage range 3.3 is  $3.3\text{V} \pm 0.3\text{V}$ . Voltage range 5.0 is  $5.0\text{V} \pm 0.5\text{V}$ .

**AC Electrical Characteristics for ACT**

| Symbol    | Parameter                            | $V_{CC}$ (V) <sup>(7)</sup> | $T_A = +25^\circ\text{C}$ ,<br>$C_L = 50\text{pF}$ |      |      | $T_A = -40^\circ\text{C to } +85^\circ\text{C}$ ,<br>$C_L = 50\text{pF}$ |      | Units |
|-----------|--------------------------------------|-----------------------------|----------------------------------------------------|------|------|--------------------------------------------------------------------------|------|-------|
|           |                                      |                             | Min.                                               | Typ. | Max. | Min.                                                                     | Max. |       |
| $t_{PLH}$ | Propagation Delay,<br>Data to Output | 5.0                         | 1.5                                                | 6.0  | 8.5  | 1.5                                                                      | 9.5  | ns    |
| $t_{PHL}$ | Propagation Delay,<br>Data to Output | 5.0                         | 1.5                                                | 5.5  | 7.5  | 1.5                                                                      | 8.5  | ns    |
| $t_{PZH}$ | Output Enable Time                   | 5.0                         | 1.5                                                | 7.0  | 8.5  | 1.0                                                                      | 9.5  | ns    |
| $t_{PZL}$ | Output Enable Time                   | 5.0                         | 2.0                                                | 7.0  | 9.5  | 1.5                                                                      | 10.5 | ns    |
| $t_{PHZ}$ | Output Disable Time                  | 5.0                         | 2.0                                                | 8.0  | 9.5  | 2.0                                                                      | 10.5 | ns    |
| $t_{PLZ}$ | Output Disable Time                  | 5.0                         | 2.5                                                | 6.5  | 10.0 | 2.0                                                                      | 10.5 | ns    |

**Note:**

7. Voltage range 5.0 is  $5.0\text{V} \pm 0.5\text{V}$ .

**Capacitance**

| Symbol   | Parameter                     | Conditions             | Typ. | Units |
|----------|-------------------------------|------------------------|------|-------|
| $C_{IN}$ | Input Capacitance             | $V_{CC} = \text{OPEN}$ | 4.5  | pF    |
| $C_{PD}$ | Power Dissipation Capacitance | $V_{CC} = 5.0\text{V}$ | 45.0 | pF    |

## Physical Dimensions



**Figure 1. 20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300" Wide**

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| Build it Now <sup>™</sup>                                                         | FRFET <sup>®</sup>                           | Power220 <sup>®</sup>                  | SYSTEM <sup>®</sup>              |
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| FAST <sup>®</sup>                                                                 | Motion-SPM <sup>™</sup>                      | STEALTH <sup>™</sup>                   | UHC <sup>®</sup>                 |
| FastvCore <sup>™</sup>                                                            | OPTOLOGIC <sup>®</sup>                       | SuperFET <sup>™</sup>                  | Ultra FRFET <sup>™</sup>         |
| FlashWriter <sup>®</sup> *                                                        | OPTOPLANAR <sup>®</sup>                      | SuperSOT <sup>™</sup> -3               | UniFET <sup>™</sup>              |
|                                                                                   |                                              | SuperSOT <sup>™</sup> -6               | VCX <sup>™</sup>                 |
|                                                                                   |                                              | SuperSOT <sup>™</sup> -8               |                                  |

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2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

| Datasheet Identification | Product Status         | Definition                                                                                                                                                                                               |
|--------------------------|------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Advance Information      | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.                                                                       |
| Preliminary              | First Production       | This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |
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| Obsolete                 | Not In Production      | This datasheet contains specifications on a product that has been discontinued by Fairchild Semiconductor. The datasheet is printed for reference information only.                                      |

Rev. I32



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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