



# BC847QAS

45 V, 200 mA NPN/NPN general-purpose transistor

10 September 2018

Product data sheet

## 1. General description

NPN/NPN general-purpose transistor in a leadless ultra small DFN1010B-6 (SOT1216) Surface-Mounted Device (SMD) plastic package.

PNP/PNP complement: BC857QAS.

NPN/PNP complement: BC847QAPN.

## 2. Features and benefits

- Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified
- Low package height of 0.37 mm

## 3. Applications

- General-purpose switching and amplification
- Mobile applications

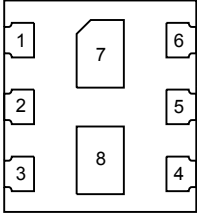
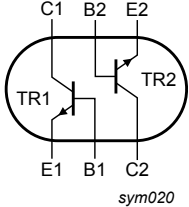
## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per transistor</b>						
$V_{CEO}$	collector-emitter voltage	open base	-	-	45	V
$I_C$	collector current		-	-	200	mA
$h_{FE}$	DC current gain	$V_{CE} = 5\text{ V}; I_C = 2\text{ mA}$	200	-	450	

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	 <p>Transparent top view DFN1010B-6 (SOT1216)</p>	 <p>sym020</p>
2	B1	base TR1		
3	C2	collector TR2		
4	E2	emitter TR2		
5	B2	base TR2		
6	C1	collector TR1		
7	C1	collector TR1		
8	C2	collector TR2		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC847QAS	DFN1010B-6	DFN1010B-6: plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1216

## 7. Marking

Table 4. Marking codes

Type number	Marking code
BC847QAS	00 01 00

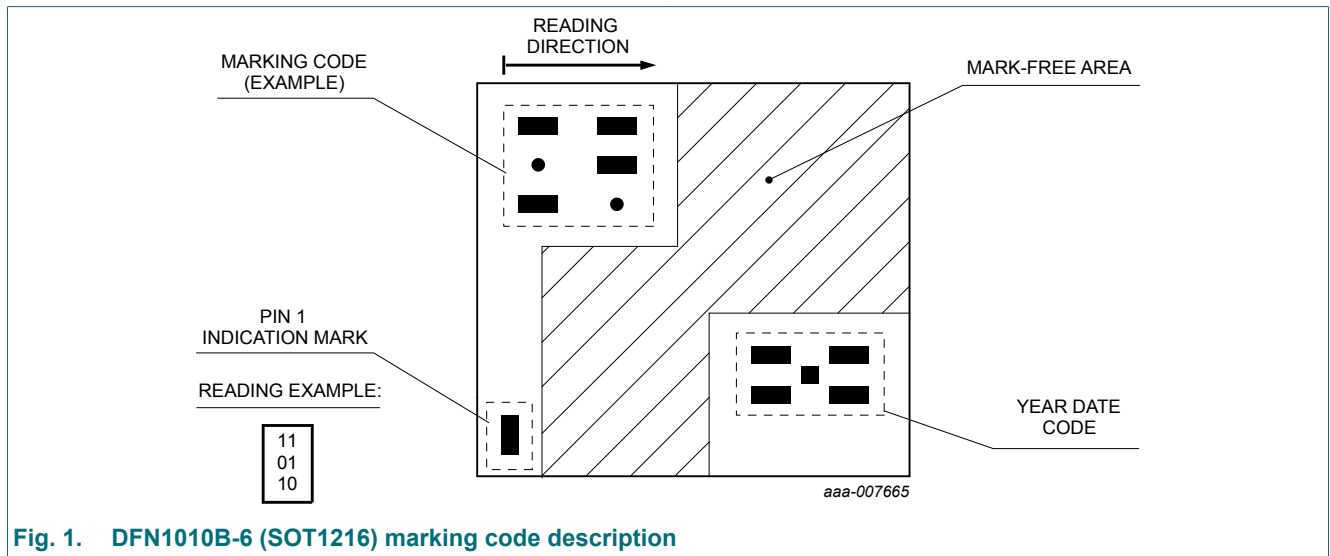


Fig. 1. DFN1010B-6 (SOT1216) marking code description

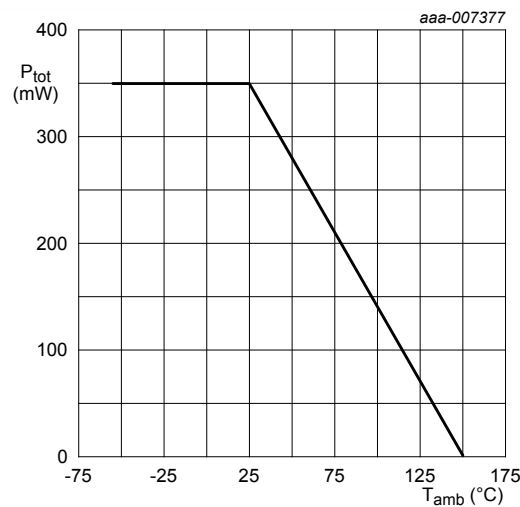
## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
<b>Per transistor</b>						
$V_{CBO}$	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	45	V
$V_{EBO}$	emitter-base voltage	open collector		-	6	V
$I_C$	collector current			-	200	mA
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms		-	200	mA
$I_{BM}$	peak base current			-	100	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	230	mW
<b>Per device</b>						
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	350	mW
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-55	150	°C
$T_{stg}$	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



FR4 PCB, standard footprint

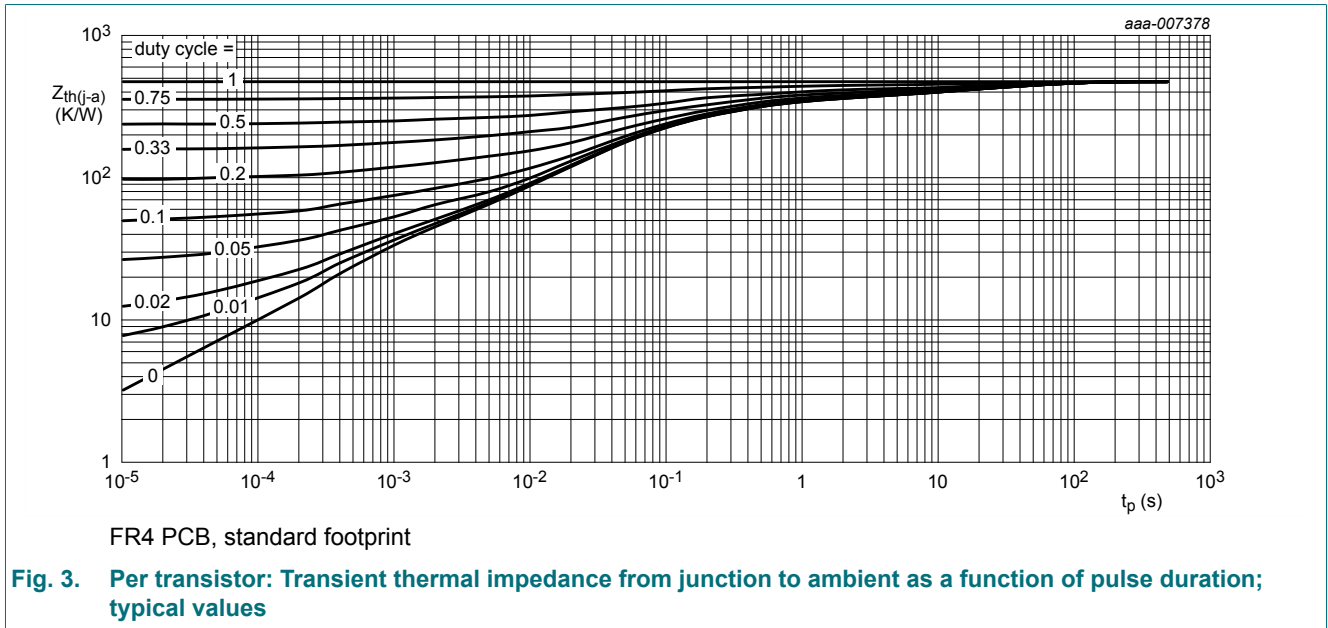
**Fig. 2. Per device: Power derating curve**

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
<b>Per transistor</b>							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	543	K/W
<b>Per device</b>							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	357	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

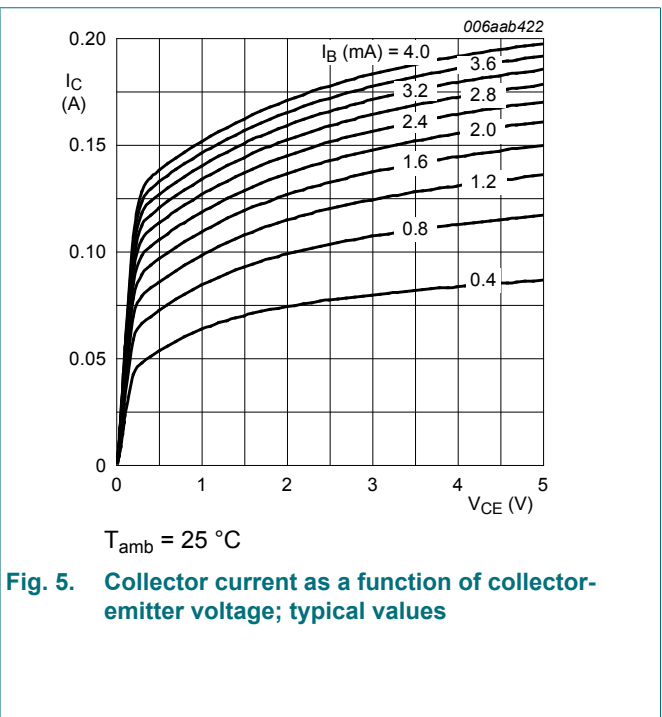
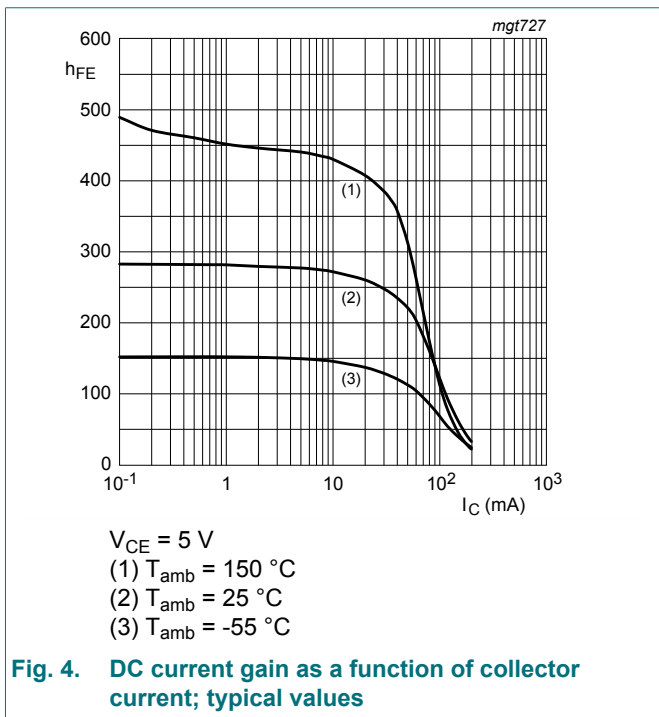


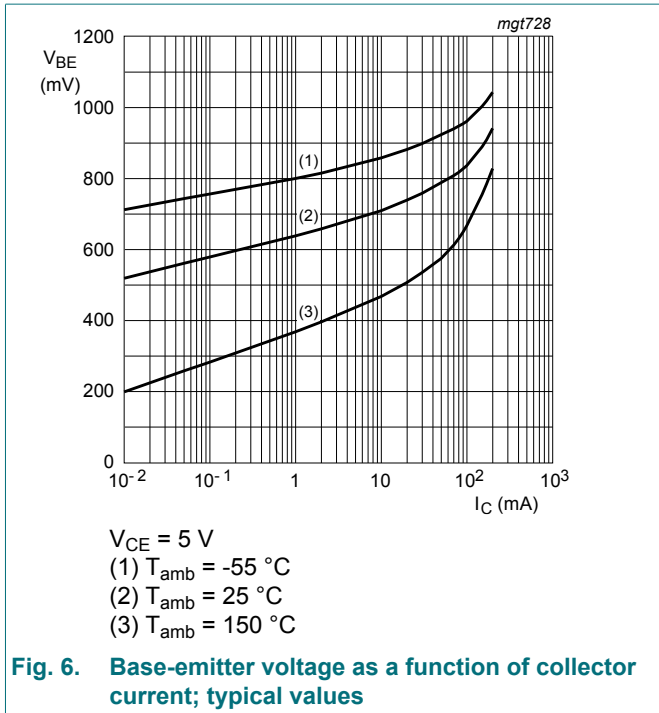
### 10. Characteristics

**Table 7. Characteristics**

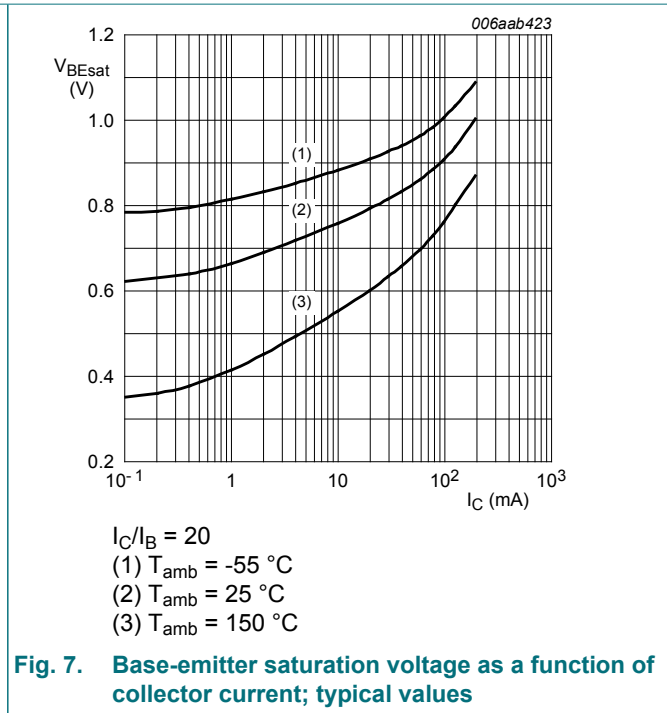
$T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per transistor</b>						
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 30\text{ V}; I_E = 0\text{ A}; T_j = 150\text{ }^{\circ}\text{C}$	-	-	5	$\mu\text{A}$
		$V_{CB} = 30\text{ V}; I_E = 0\text{ A}$	-	-	15	nA
$I_{EBO}$	emitter-base cut-off current	$V_{EB} = 5\text{ V}; I_C = 0\text{ A}$	-	-	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 5\text{ V}; I_C = 2\text{ mA}$	200	-	450	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 0.5\text{ mA}$	-	-	100	mV
		$I_C = 100\text{ mA}; I_B = 5\text{ mA}; \text{pulsed}; t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.02$	-	-	300	mV
$V_{BEsat}$	base-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 0.5\text{ mA}$	-	760	-	mV
		$I_C = 100\text{ mA}; I_B = 5\text{ mA}; \text{pulsed}; t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.02$	-	900	-	mV
$V_{BE}$	base-emitter voltage	$V_{CE} = 5\text{ V}; I_C = 2\text{ mA}$	600	660	725	mV
		$V_{CE} = 5\text{ V}; I_C = 10\text{ mA}$	-	710	820	mV
$C_c$	collector capacitance	$V_{CB} = 10\text{ V}; I_E = 0\text{ A}; i_e = 0\text{ A}; f = 1\text{ MHz}$	-	-	4	pF
$C_e$	emitter capacitance	$V_{EB} = 0.5\text{ V}; I_C = 0\text{ A}; f = 1\text{ MHz}$	-	11	-	pF
$f_T$	transition frequency	$V_{CE} = 5\text{ V}; I_C = 10\text{ mA}; f = 100\text{ MHz}$	100	-	-	MHz
NF	noise figure	$V_{CE} = 5\text{ V}; I_C = 0.2\text{ mA}; R_S = 2\text{ k}\Omega; f = 1\text{ MHz}; B = 200\text{ Hz}$	-	-	10	dB

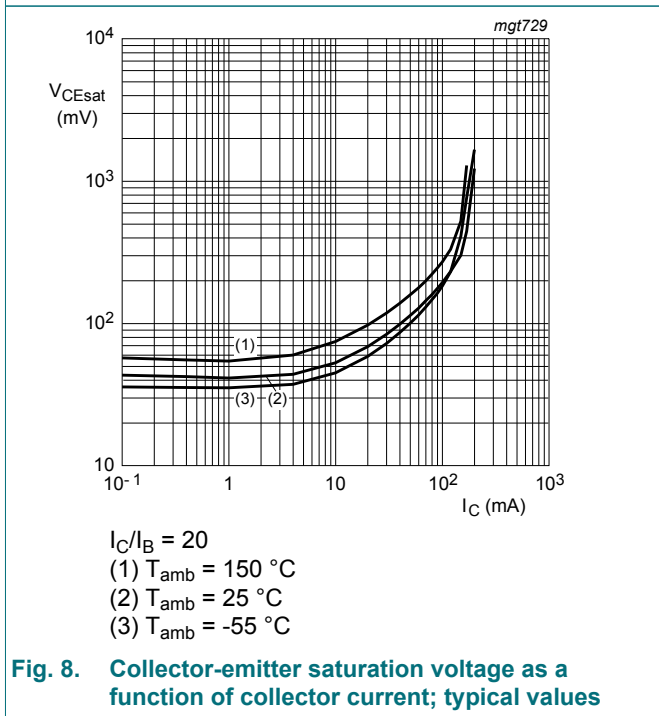




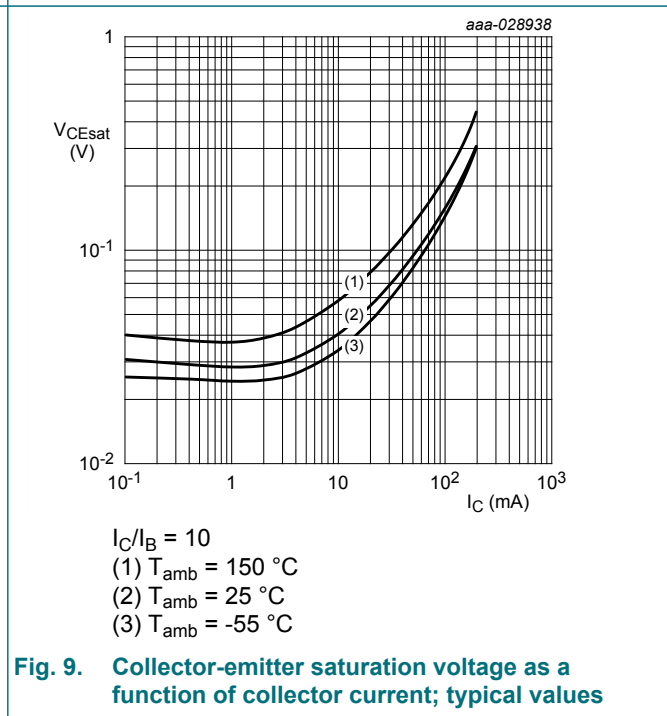
**Fig. 6. Base-emitter voltage as a function of collector current; typical values**



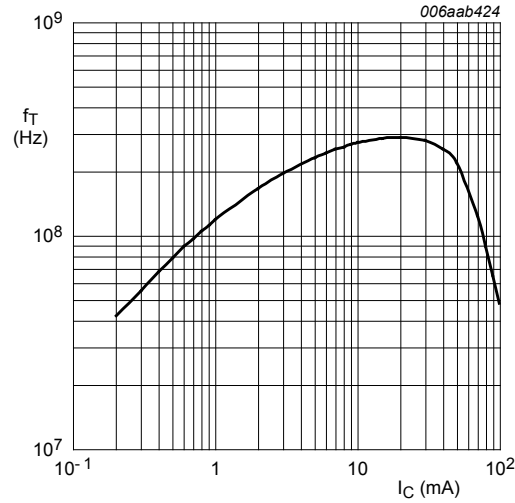
**Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values**



**Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values**



**Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values**



$T_{amb} = 25\text{ }^{\circ}\text{C};$   
 $V_{CE} = 5\text{ V};$   
 $f = 100\text{ MHz}$

Fig. 10. Transition frequency as a function of collector current; typical values

## 11. Test information

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

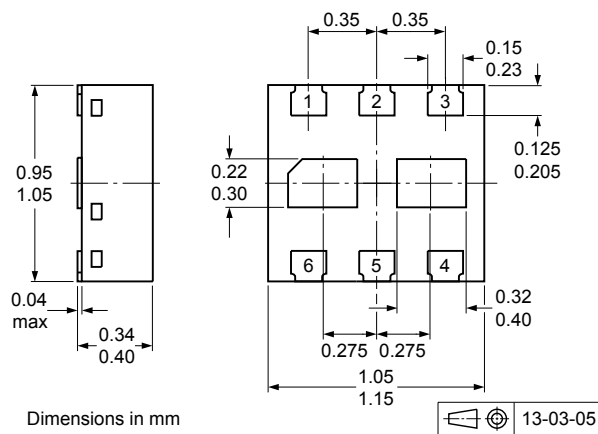


Fig. 11. Package outline DFN1010B-6 (SOT1216)



### 13. Soldering

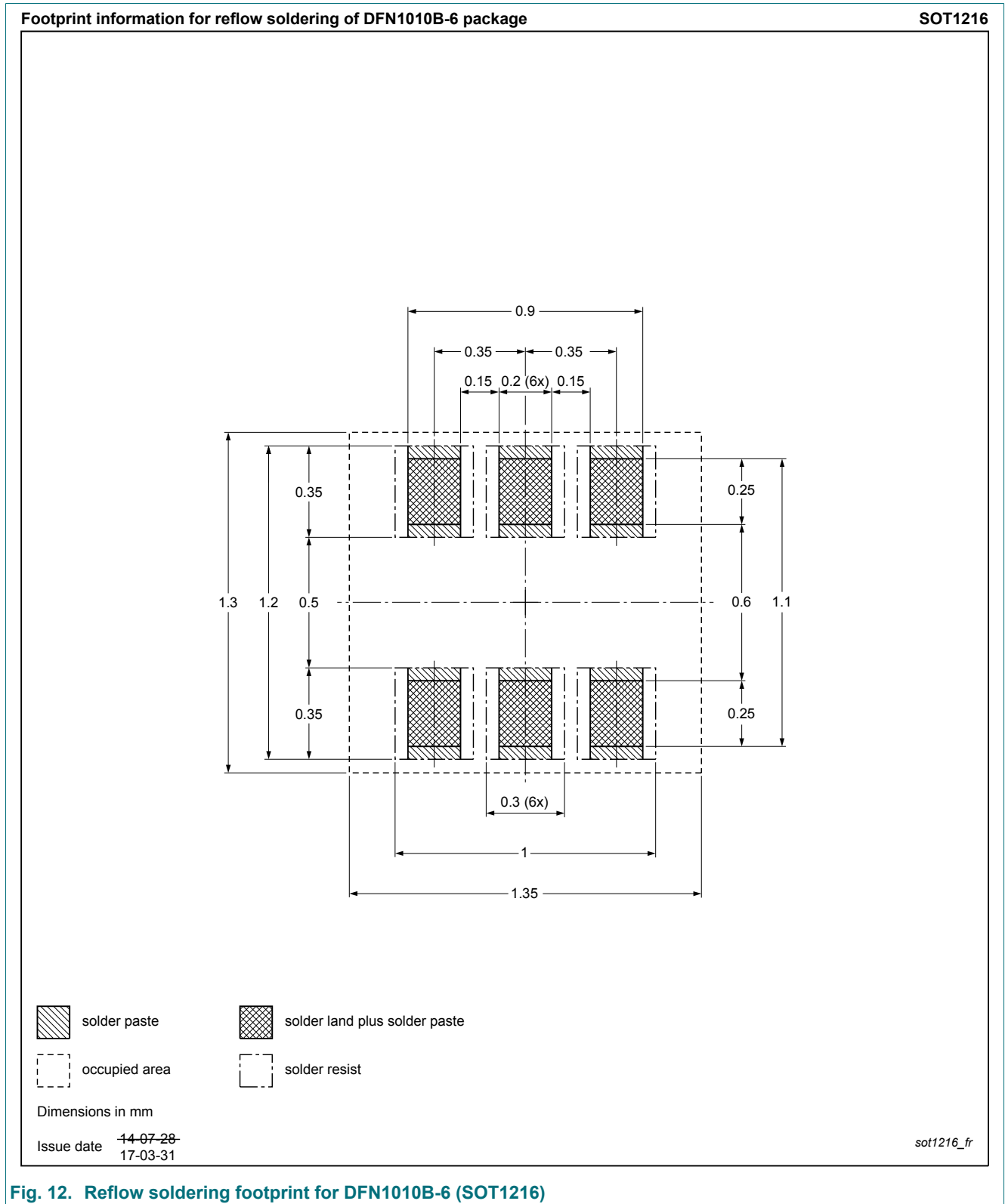


Fig. 12. Reflow soldering footprint for DFN1010B-6 (SOT1216)

## 14. Revision history

**Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC847QAS v.3	20181009	Product data sheet	-	BC847QAS v.2
Modifications:	<ul style="list-style-type: none"><li>Limiting values: <math>I_C</math> value changed to 200 mA</li><li>Characteristics: Figure 9 added</li></ul>			
BC847QAS v.2	20150708	Product data sheet	-	BC847QAS v.1
BC847QAS v.1	20140729	Product data sheet	-	-

## 45 V, 200 mA NPN/NPN general-purpose transistor

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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**Телефон:** 8 (812) 309 58 32 (многоканальный)

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**Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.