V40DM120C-M3, V40DM120CHM3

Vishay General Semiconductor

### **Dual High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.46$  V at  $I_F = 5$  A



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# V40DM120C

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 20 A				
V <sub>RRM</sub>	120 V				
I <sub>FSM</sub>	250 A				
$V_F$ at $I_F$ = 20 A ( $T_A$ = 125 °C)	0.64 V				
T <sub>J</sub> max.	150 °C				
Package	TO-263AC (SMPD)				
Diode variations	Common cathode				

### FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
  Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, inductrial, and automotive application.

### **MECHANICAL DATA**

Case: TO-263AC (SMPD)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test **Polarity:** As marked

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V40DM120C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	120	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	40	^	
	per diode		20	A	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	250	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	



COMPLIANT

HALOGEN

FREE



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.54	-	V	
	I <sub>F</sub> = 10 A			0.64	-		
	I <sub>F</sub> = 20 A			0.79	0.89		
	$I_F = 5 A$	T <sub>A</sub> = 125 °C		0.46	-		
	$I_F = 10 A$			0.55	-		
	I <sub>F</sub> = 20 A			0.64	0.72		
Reverse current per diode	V <sub>R</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	9	-	μA	
		T <sub>A</sub> = 125 °C		4.3	-	mA	
	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C		-	500	μA	
		T <sub>A</sub> = 125 °C		7	32	mA	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40DM120C	UNIT	
Typical thermal resistance	per diode	$R_{ ext{ heta}JC}$	2.0		
	per device		1.1	°C/W	
	per device	R <sub>0JA</sub> <sup>(1)(2)</sup>	45		

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AC (SMPD)	V40DM120C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
TO-263AC (SMPD)	V40DM120CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel

Note

<sup>(1)</sup> AEC-Q101 qualified

#### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

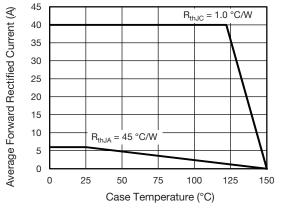


Fig. 1 - Forward Current Derating Curve

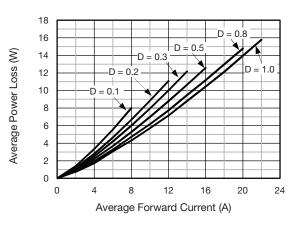


Fig. 2 - Forward Power Loss Characteristics Per Diode

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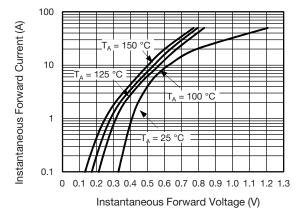


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

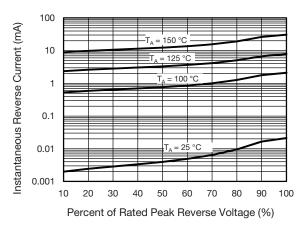


Fig. 4 - Typical Reverse Characteristics Per Diode

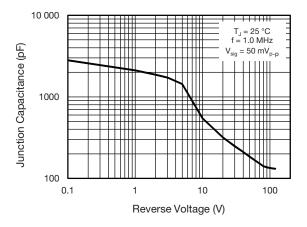


Fig. 5 - Typical Junction Capacitance Per Diode

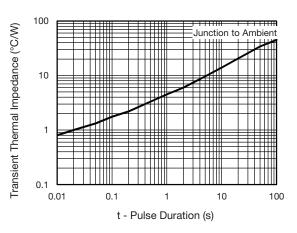


Fig. 6 - Typical Transient Thermal Impedance Per Device

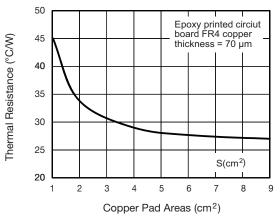


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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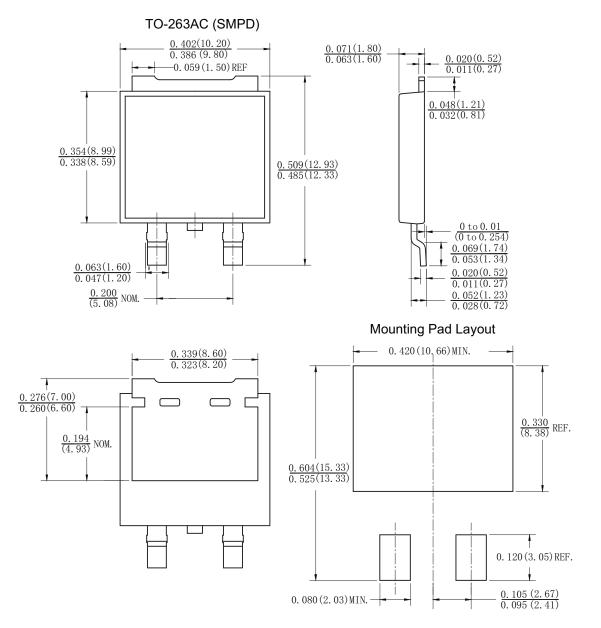
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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**Телефон:** 8 (812) 309 58 32 (многоканальный) **Факс:** 8 (812) 320-02-42 **Электронная почта:** <u>org@eplast1.ru</u> **Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.