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USB1T20 — Universal Serial Bus Transceiver

Features

- Complies with Universal Serial Bus Specification 2.0 for FS/LS Applications
- Utilizes Digital Inputs and Outputs to Transmit and Receive USB Cable Data
- Supports 12Mbit/s Full Speed (FS) and 1.5Mbit/s Low Speed (LS) Serial Data Transmission
- Supports Single-ended and Differential Data Interface as Function of MODE
- Single 3.3 V Supply
- ESD Performance: Human Body Model
 - 9.5 kV on D-, D+ Pins Only
 - 4.0 kV on All Other Pins

Description

USB1T20 is a generic USB 2.0 compliant transceiver. Using a single voltage supply, the USB1T20 provides an ideal USB interface solution for any electronic device able to supply 3.0 V to 3.6 V. It is designed to allow 5.0 V or 3.3 V programmable and standard logic to interface with the physical layer of the Universal Serial Bus (USB). It is capable of transmitting and receiving serial data at both full speed (12Mbit/s) and low speed (1.5Mbit/s) data rates.

Packaged in industry-standard TSSOP package. The USB1T20 is ideal for mobile electronics and other space-constrained applications.

Ordering Information

Part Number	Operating Temperature Range	Package	Packing Method
USB1T20MTCX	-40° to +85°C	14-Lead, Thin-Shrink Small-Outline Package (TSSOP) JEDEC MO-153, 4.4mm Wide	Tape and Reel

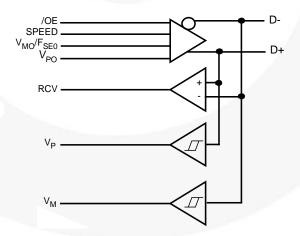


Figure 1. Logic Diagram

Pin Configuration

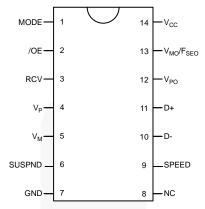


Figure 2. Pin Configuration (Top View)

Pin Definitions

Pin#	Name	I/O		Desc	ription				
1	MODE	1		Mode . When left unconnected, a weak pull-up transistor pulls mode pin to V_{CC} and, in this GND, the V_{MO}/F_{SEO} pin takes the function of F_{SEO} (force SEO).					
2	/OE	1	Output Enable. Active When not active, the t		transceiver to transmit ve mode.	data on the bus.			
3	RCV	0	Receive Data. CMOS	Level output for US	B differential input.	4			
				Gated version of D- and D+. Outputs are logic "0" and logic "1." Used to detect single ended zero (/SEO), error conditions, and interconnected speed. (Input to SIE).					
	4, 5 V _P ,V _M		V _P		V _M	RESULT			
4, 5		0	0		0	/SEO			
			0		1	Low Speed			
			1	V	0	Full Speed			
			0	<i>y</i>	1	Error			
6	SUSPND	I	Suspend . Enables a low-power state while the USB bus is inactive. While the suspend pin is active, it drives the RCV pin to a logic "0" state. Both D+ and D- are 3-state.						
7	GND		Ground reference.		4				
8	NC		No connect.						
9	SPEED	I	Edge Rate Control. Ledge rates for low spe		edge rates for full spee	ed. Logic "0" operates			
10, 11	D-, D+	AI/O	Data+, Data Differen	tial data bus conform	ning to the Universal S	erial Bus standard.			
			Inputs to differential d	river. (Outputs from S	SIE.)				
			Mode	V_{PO}	V _{MO} /F _{SEO}	RESULT			
				0	0	Logic "0"			
				0	1	/SEO			
40.40	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		0	1	0	Logic "1"			
12, 13	$V_{PO}, V_{MO}/F_{SEO}$	I		1	1	/SEO			
				0	0	/SEO			
				0	1	Logic "0"			
			1	1	0	Logic "1"			
				1	1	Illegal Code			
14	V _{CC}		3.0 to 3.6 power supply.						

Functional Truth Table

	Input			I/	I/O Outputs			Outputs		
Mode	V_{PO}	V _{MO} /F _{SEO}	/OE	SUSPND	D+	D-	RCV	V_P	V_{M}	Result
0	0	0	0	0	0	1	0	0	1	Logic "0"
0	0	1	0	0	0	0	Undefined State	0	0	/SEO
0	1	0	0	0	1	0	1	1	0	Logic "1"
0	1	1	0	0	0	0	Undefined State	0	0	/SEO
1	0	0	0	0	0	0	Undefined State	0	0	/SEO
1	0	1	0	0	0	1	0	0	1	Logic "0"
1	1	0	0	0	1	0	1	1	0	Logic "1"
1	1	1	0	0	1	1	Undefined State	Undefined State	Undefined State	Illegal Code
Don't Care	Don't Care	Don't Care	1	0	3- State	3- State	Undefined State	Undefined State	Undefined State	D+/D- Hi-Z
Don't Care	Don't Care	Don't Care	1	1	3- State	3- State	Undefined State	Undefined State	Undefined State	D+/D- Hi-Z

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Paramete	er	Min.	Max.	Unit
V _{CC}	DC Supply Voltage		-0.5	7.0	V
I _{IK}	DC Input Diode Current, V _{IN} < 0	V		-50	mA
V _{IN}	Input Voltage ⁽¹⁾		-0.5	5.5	V
V _{I/O}	Input / Output Voltage		-0.5	V _{CC} + 0.5	V
I _{OK}	Output Diode Current, Vo > Vcc	or V _O < 0 V		±50	mA
Vo	Output Voltage ⁽¹⁾		-0.5	V _{CC} + 0.5	V
	Output Source or Sink Current	V _P , V _M , RCV Pins		±15	m Λ
Io	$(V_O = 0 \text{ to } V_{CC})$	D+/D- Pins	1	±50	mA
I _{CC} / I _{GND}	V _{CC} / GND Current			±100	mA
T _{STG}	Storage Temperature Range		-60	+150	°C

Note:

 The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V _{cc}	Supply Voltage	3.0	3.6	V
V_{IN}	Input Voltage	0	5.5	V
V _{AI/O}	Input Range for AI/0	0	V_{CC}	V
Vo	Output Voltage	0	V _{CC}	V
T _A	Operating Ambient Temperature, Free Air	-40	+85	°C

DC Electrical Characteristics Digital Pins

Over the recommended range of supply voltage and operating free air temperature unless otherwise noted. $V_{CC} = 3.0 \text{ V}$ to 3.6 V.

Cumbal	Parameter	Conditions	T _A =	11		
Symbol		Conditions	Min.	Тур.	Max.	Units
Input Levels	s					
V_{IL}	Low-Level Input Voltage				0.8	V
V _{IH}	High-Level Input Voltage		2			V
Output Leve	els					
\ /	V _{OL} Low-Level Output Voltage	I _{OL} = 4 mA			0.4	
V _{OL}		I _{OL} = 20 μA			0.1	V
1/	High-Level Output Voltage	$I_{OH} = 4 \text{ mA}$	2.4			
V_{OH}		I _{OH} = 20 μA	V _{CC} -0.1			V
Leakage Cu	ırrent	1	1/			
I _{IN}	Input Leakage Current	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$			±5	μA
I _{CCFS}	Supply Current, Full Speed	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$			5	mA
I _{CCLS}	Supply Current, Low Speed	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$			5	mA
I _{CCQ}	Quiescent Supply Current	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V},$ $V_{IN} = V_{CC} \text{ or GND}$			5	mA
I _{ccs}	Supply Current in Suspend	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V},$ $Mode = V_{CC}$		N.	10	μΑ

DC Electrical Characteristics D+/D- Pins

Over the recommended range of supply voltage and operating free air temperature unless otherwise noted. $V_{CC} = 3.0V$ to 3.6V.

Symbol	Davamatan	0 1111	T _A =-			
	Parameter	Conditions	Min.	Тур.	Max.	Units
Input Levels	S				•	
V_{DI}	Differential Input Sensitivity	(D+) - (D-)	0.2			V
V_{CM}	Differential Common-Mode Range	Includes V _{DI} Range	0.8		2.5	V
V_{SE}	Single-Ended Receiver Threshold		8.0		2.0	V
Output Leve	els		- //			
V _{OL}	Static Output Low-Voltage	R_L of 1.5 k Ω to 3.6 V	/		0.3	V
V _{OH}	Static Output High-Voltage	R_L of 1.5 k Ω to GND	2.8		3.6	V
V_{CR}	Differential Crossover		1.3		2.0	V
Leakage Cu	rrent				72	
l _{oz}	High-Z State Data Line Leakage Current	0 V <v<sub>IN<3.3 V</v<sub>			±5	μA
Capacitance	9					
	Transceiver Capacitance ⁽²⁾	Pin to GND			10	pF
C_{IN}	Capacitance Match ⁽²⁾				10	%
Output Res	istance			•		•
7	Driver Output Resistance ⁽³⁾	Steady-State Drive	4		20	Ω
Z_{DRV}	Resistance Match ⁽³⁾				10	%

Notes:

- 2. This specification is guaranteed by design and statistical process distribution.
- 3. Excludes external resistor. To comply with USB specification 1.1, external series resistors of 24 Ω ±1% each on D+ and D- are recommended.

AC Electrical Characteristics D+/D- Pins, Full Speed

Over the recommended range of supply voltage and operating free air temperature unless otherwise noted. V_{CC} = 3.0 V to 3.6 V; C_L = 50 pF; R_L = 1.5 k Ω on D+ to V_{CC} .

Cumbal	Parameter	Conditions	T _A =-	lluite.			
Symbol		Conditions	Min.	Тур.	Max.	Units	
Driver Chara	acteristics						
t _{R,} t _F	Rise and Fall Time	10 and 90%, Figure 3	4		20	ns	
t _{RFM}	Rise/Fall Time Matching	t _r / t _f	90		110	%	
V_{CRS}	Output Signal Crossover Voltage		1.3		2.0	V	
Driver Timin	Driver Timings						
t _{PLH}	Driver Propagation Delay (V _{PO} ,V _{MO} /F _{SEO} to D+D-)	Figure 4			18	ns	
t _{PHZ,} t _{PLZ}	Driver Disable Delay (/OE to D+/D-)	Figure 6	1		13	ns	
t _{PZH} , t _{PZL}	Driver Enable Delay (/OE to D+/D-)	Figure 6	1		17	ns	
Receiver Tir	Receiver Timings						
t _{PLH}	Receiver Propagation Delay	Figure F			16	ns	
t _{PHL}	D+/D- to RVC	Figure 5			19	ns	
t _{PLH} , t _{PHL}	Single-ended Receiver Delay (D+,D- to V _P , V _M)	Figure 5			8	ns	

AC Electrical Characteristics D+/D- Pins, Low Speed

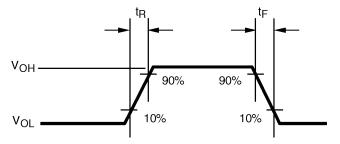
Over the recommended range of supply voltage and operating free air temperature unless otherwise noted. V_{CC} = 3.0 V to 3.6 V; C_L = 200 pF to 600 pF; R_L = 1.5 k Ω on D- to V_{CC} .

Or seed to ad	Parameter	Conditions	T _A =-			
Symbol		Conditions	Min.	Тур.	Max.	Units
Driver Chara	acteristics					
t _{LR} , t _{LF}	Rise and Fall Time	10 and 90%, Figure 3	75	/	300	ns
t _{RFM}	Rise/Fall Time Matching	t_r / t_f	80		120	%
V _{CRS}	Output Signal Crossover Voltage		1.3		2.0	V
Driver Timin	igs		/			
t _{PLH} , t _{PHL}	Driver Propagation Delay (V _{PO} ,V _{MO} /F _{SEO} to D+D-)	Figure 4			300	ns
t _{PHZ} , t _{PLZ}	Driver Disable Delay (/OE to D+/D-)	Figure 6			13	ns
t _{PZH,} t _{PZL}	Driver Enable Delay (/OE to D+/D-)	Figure 6			205	ns
Receiver Tir	nings					-21
t _{PLH} , t _{PHL}	Receiver Propagation Delay (D+/D- to RVC)	Figure 5			18	ns
t _{PLH} , t _{PHL}	Single-ended Receiver Delay (D+,D- to V _P , V _M)	Figure 5			28	ns

AC Loadings and Waveforms

 V_{OL} and V_{OH} are the typical output voltage drops that occur with the output load. V_{CC} never goes below 3.0 V.

2.7V



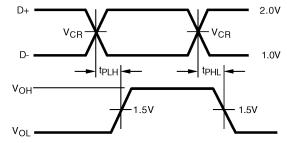
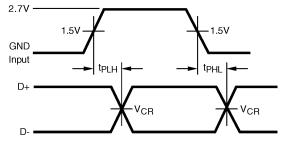


Figure 3. Rise and Fall Times

Figure 4. V_{PO}, V_{MO}/F_{SEO} to D+/D-



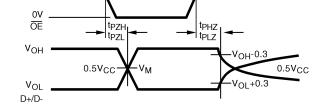
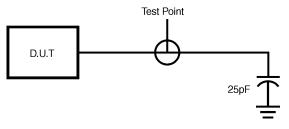


Figure 5. D+/D- to RCV, V_P/V_M

Figure 6. /OE to D+/D-

Test Circuits and Waveforms



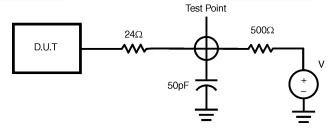
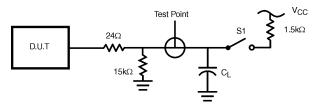


Figure 7. Load for V_M/V_P and RCV

Figure 8. Load for Enable and Disable Times



Test	S1
D-/LS	Close
D+/LS	Open
D-/FS	Open
D+/FS	Close

C_L=50pF, Full Speed

 C_L =200pF, Low Speed (Minimum Timing)

C_L=600pF, Low Speed (Maximum Timing)

 $1.5k\Omega$ on D- (Low Speed) or D+ (Full Speed) only

Figure 9. Load for D+/D-



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